



## Using the Aries™ Hardware Counters

S-0045-10

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# Using the Aries™ Hardware Counters

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This document describes the Aries™ network hardware performance counters and how to use them to collect and analyze performance data.

The Aries ASIC used in Cray XC30 systems is the successor to the Gemini™ router chip used in Cray XE and Cray XK systems, and consists of four Network Interface Controllers (NICs), a 48-port tiled router, and a multiplexer known as the *Netlink* (NL). A single Aries device provides all the network connectivity for all four nodes on a Cray XC30 blade, and each of the four NICs on the Aries chip provides an independent PCI-Express host interface. In the current Cray XC30 blade design, these NICs connect via the PCIe interface to four independent dual-socket Intel® Xeon® nodes, each of which has its own memory channels.

On the router side, the NICs connect to the Netlink multiplexer, which provides dynamic load-balancing by distributing packets over the injection ports according to load, and from there to the router tiles themselves. Each Aries ASIC has 48 router tiles, arranged in a 6-row by 8-column grid.

There are two basic categories of hardware performance counters available to the user: NIC counters and router tile counters. In addition some counters are configurable, meaning they not only can be read but also written to, in order to configure and control data collection.

For a complete list of the available Aries counters, use the `papi_native_avail` command, or examine the contents of the `$(CRAYPAT_ROOT)/share/Counters.papi_aries` file.

## 1.1 Using CrayPat to Monitor Aries Counters

To collect network performance counter data, first instrument the application for data collection as described in the `pat_build(1)` man page. If data collection at one or more specific points in the application is desired, rather than for the entire application, use the CrayPat API to define data collection regions.

**Note:** Cray recommends that you do not collect other performance data while collecting network counters. Network counter data collection consumes more overhead than other types of performance data collection and thus skews the results of other experiments.

When an instrumented executable program is launched using the `aprun` command, a set of environment variables, `PAT_RT_NWPC*`, control access to the network performance counters. These environment variables are listed in [Run Time Environment Variables on page 43](#).

The CrayPat utility `pat_build` instruments an executable file. One aspect of the instrumentation includes intercepting entries into and returns out of a function. This is known formally as *tracing*. Information such as time stamps and performance counter values are recorded at this time.

CrayPat supports instrumentation of an application binary for collection of network hardware performance counters. Counter values are recorded at application runtime, and are presented to the user through a table generated by `pat_report`. The CrayPat user interface to request instrumentation is similar to that for processor performance counters.

Although the user interface to request network counters is similar to processor counters, there are some significant differences that must be understood. Depending on the type of counters requested, some are shared across all processors within a node, some are shared between two nodes, and some are shared across all applications passing through a chip. Some counters monitor all traffic for your application, even on nodes not reserved for your application, and some monitor locally—that is, they monitor only traffic associated with nodes assigned to an Aries chip and no other network traffic.

Users should also be aware that access to the network counters is more resource-intensive than access to the processor performance counters. Because network counters are a shared resource, the system software is designed to provide dedicated access whenever possible. This is done through the Application Level Placement Scheduler (ALPS), by ensuring that an application collecting counters is not placed on the same node chip as another application collecting performance counters. This does not prevent a second application that is not collecting counters from being placed on the node, however. This compromise assures better system utilization because compute nodes are not left unavailable for use by another application.

CrayPat focuses on the use of the NIC and ORB counters available within the Aries chip. The values collected from these counters are local to a node and therefore specific to an application. Traffic between MPI ranks cannot be distinguished through the counters. The event names that CrayPat supports are listed at the end of this document. Network counters are only collected for the `MAIN` thread. When values are collected only at the beginning and end of the instrumented application, instrumentation overhead is minimal. This frequency of collection gives a high-level view of the program's use of the networking router in terms of the counters specified.

**Note:** By default, that is, if `PAT_RT_TRACE_FUNCTION_NAME` is not set and a function is traced, all network performance counters (NWPCs) specified by the user via the `PAT_RT_NWPC*` environment variables will be collected. As mentioned earlier, this can become very resource-intensive.

Before attempting the following examples, verify that your system has an Aries network and that the requisite PE modules are loaded:

```
$ module list
  craype-network-aries
```

Attempting to collect Aries performance counters on a system that does not have the Aries network will result in a fatal error:

```
$ aprun -n 16 my_program+pat
CrayPat/X: Version 6.1 Revision 3329 03/20/13 11:26:16
pat[FATAL][0]: initialization of NW performance counter API failed
[No such file or directory]
```

### Example 1. Collect statistics about the Outstanding Request Buffer on the NIC Network Logic Monitor

This example enables tracing of `MAIN`.

```
$ pat_build -w my_program
$ export PAT_RT_NWPC=_ORB
$ aprun my_program+pat
```

### Example 2. Display the resulting collected statistics

```
$ pat_report my_program+pat+11171-41tdot.xf> counter_rpt
```

Example output from `pat_report`:

Table 9: NWPC Data by Function Group and Function

Group / Function / Node Id=HIDE

```

=====
Total
-----
Time%                100.0%
Time                858.532889 secs
AR_NIC_NETMON_ORB_EVENT_CNTR_ORD_RAM_READ    2936772
AR_NIC_NETMON_ORB_EVENT_CNTR_REQ_FLITS      6558075
AR_NIC_NETMON_ORB_EVENT_CNTR_REQ_PKTS      2936830
AR_NIC_NETMON_ORB_EVENT_CNTR_RSP_BLOCKED_PKT_GEN 3882181
AR_NIC_NETMON_ORB_EVENT_CNTR_RSP_FLITS     12017064
AR_NIC_NETMON_ORB_EVENT_CNTR_RSP_NET_TRACK  2936803
AR_NIC_NETMON_ORB_EVENT_CNTR_RSP_PKTS      2936823
AR_NIC_NETMON_ORB_EVENT_CNTR_RSP_STALLED    3882181
AR_NIC_NETMON_RAT_EVENT_CNTR_ORB_FLITS     16049495
AR_NIC_NETMON_RAT_EVENT_CNTR_ORB_PKTS      4953037
=====

```

**Example 3. Suppress instrumented entry points from recording performance data to reduce overhead**

This example uses the predefined NWPC group `_AMO`. Because the program is traced, the `PAT_RT_TRACE_FUNCTION_NAME` is set to suppress any data collection by already instrumented entry points in `my_program+pat`. This means that NWPC values will only be recorded for the MAIN thread (at the start and the end of the instrumented program). Instrumentation overhead is minimal.

```

$ pat_build -u -g mpi my_program
$ export PAT_RT_NWPC=_AMO
$ export PAT_RT_TRACE_FUNCTION_NAME=*:0
$ aprun -n32 my_program+pat

```

This gives a high-level view of the program's use of the networking router in terms of what the `_AMO` group describes. If more details about NWPC use during execution of the program are desired, the `PAT_RT_TRACE_FUNCTION_NAME` environment variable need not be set, but the significant overhead injected by reading the NWPCs may make the resulting performance data inaccurate.

To selectively collect NWPCs and the other performance data for traced functions, add them to the end of `PAT_RT_TRACE_FUNCTION_NAME`:

```

$ export PAT_RT_TRACE_FUNCTION_NAME=0:*,mxm,MPI_Bcast

```

## 1.2 NIC Performance Counters

To better understand how to use the NIC counters, you need to understand some of the terminology specific to the Aries network architecture.



The Block Transfer Engine (BTE) provides asynchronous transfers of blocks of data between local memory and remote memory. This frees up the software from having to manage memory-to-memory transfers.

A *network packet* typically consists of one or more *flits*, which are the units of flow control for the network. Because flits are usually larger than the physical data path, they are divided into *phits*, which are the units of data that the network can handle physically. A packet must contain at least two phits, one for the header and one for the cyclical redundancy check (CRC).

The V0 counters support the request channel and the V1 counters support the response channel. A flit/packet ratio can tell the user if the data entering the network was not aligned; a ratio greater than 1 indicates misaligned data is being sent across the network. Because there is a bandwidth/pipe size difference between outgoing and incoming (outgoing is smaller), in general you will notice more stalls on the V0 (request) channel.

The following counters are recommended as a way to begin using the Aries NWPC:

```
AR_NIC_NETMON_ORB_EVENT_CNTR_REQ_STALLED
AR_NIC_NETMON_ORB_EVENT_CNTR_RSP_STALLED
AR_NIC_NETMON_ORB_EVENT_CNTR_REQ_PKTS
AR_NIC_NETMON_ORB_EVENT_CNTR_RSP_PKTS
AR_NIC_NETMON_ORB_EVENT_CNTR_REQ_FLITS
AR_NIC_NETMON_ORB_EVENT_CNTR_RSP_FLITS
```

**Table 1. Atomic Memory Operations (AMO) Performance Counters**

Name	Description
AR_NIC_AMO_PRF_EN	Set to zero to configure default values
AR_NIC_AMO_PRF_EN:PRF_ORDERED_EN	Enable AR_NIC_AMO_PRF_STALL_DURATION_ORDERED
AR_NIC_AMO_PRF_EN:PRF_FULL_EN	Enable AR_NIC_AMO_PRF_STALL_DURATION_FULL
AR_NIC_AMO_PRF_EN:PRF_FLUSH_EN	Enable AR_NIC_AMO_PRF_STALL_DURATION_FLUSH
AR_NIC_AMO_PRF_EN:PRF_MATCH_EN	Enable AR_NIC_AMO_PRF_STALL_DURATION_MATCH
AR_NIC_AMO_PRF_STALL_DURATION_MATCH	Maximum duration of stall (match)
AR_NIC_AMO_PRF_STALL_DURATION_FLUSH	Maximum duration of stall (flush)
AR_NIC_AMO_PRF_STALL_DURATION_FULL	Maximum duration of stall (full)
AR_NIC_AMO_PRF_STALL_DURATION_ORDERED	Maximum duration of stall (ordered)

Name	Description
AR_NIC_REQMON_AMO_EVENT_CNTR_REQ0_PKT	Packet sent from normal request queue
AR_NIC_REQMON_AMO_EVENT_CNTR_REQ0_FLIT	Flit sent from normal request queue
AR_NIC_REQMON_AMO_EVENT_CNTR_REQ0_STALL	Stalling normal request queue
AR_NIC_REQMON_AMO_EVENT_CNTR_REQ0_BLOCK	Blocking normal request queue
AR_NIC_REQMON_AMO_EVENT_CNTR_REQ1_PKT	Packet sent from AMO-generated request queue (each packet is two flits)
AR_NIC_REQMON_AMO_EVENT_CNTR_REQ1_STALL	Stalling AMO-generated request
AR_NIC_REQMON_AMO_EVENT_CNTR_REQ1_BLOCK	Blocking AMO-generated request queue
AR_NIC_REQMON_AMO_EVENT_CNTR_RSP0_PKT	Packet sent from normal response queue
AR_NIC_REQMON_AMO_EVENT_CNTR_RSP0_FLIT	Flit sent from normal response queue
AR_NIC_REQMON_AMO_EVENT_CNTR_RSP0_BLOCK	Blocking normal response queue
AR_NIC_REQMON_AMO_EVENT_CNTR_RSP1_PKT	Packet sent from AMO-generated response queue
AR_NIC_REQMON_AMO_EVENT_CNTR_RSP1_FLIT	Flit sent from AMO-generated response queue
AR_NIC_REQMON_AMO_EVENT_CNTR_RSP1_BLOCK	Blocking AMO-generated response queue
AR_NIC_REQMON_AMO_EVENT_CNTR_AMO_DONE	AMO requests processed
AR_NIC_REQMON_AMO_EVENT_CNTR_AMO_HIT	Hits in AMO cache (AMO requests only)
AR_NIC_REQMON_AMO_EVENT_CNTR_AMO_MISS	Misses in AMO cache (AMO requests only, generate fill requests)
AR_NIC_REQMON_AMO_EVENT_CNTR_STALL_MATCH	Stalls due to a match in the cache
AR_NIC_REQMON_AMO_EVENT_CNTR_STALL_FLUSH	Stalls due to flushing the cache
AR_NIC_REQMON_AMO_EVENT_CNTR_STALL_FULL	Stalls due to the cache being full
AR_NIC_REQMON_AMO_EVENT_CNTR_STALL_ORDERED	Stalls waiting for the cache to become clean
AR_NIC_REQMON_AMO_EVENT_CNTR_FILL_RSP	Fill responses received
AR_NIC_REQMON_AMO_EVENT_CNTR_INVALIDATE	Cache invalidations (capacity)
AR_NIC_REQMON_AMO_EVENT_CNTR_ERR_REQ_PARITY	Parity errors on requests
AR_NIC_REQMON_AMO_EVENT_CNTR_ERR_REQ_SBE	SBEs in AMO request payload
AR_NIC_REQMON_AMO_EVENT_CNTR_ERR_REQ_MBE	MBEs in AMO request payload
AR_NIC_REQMON_AMO_EVENT_CNTR_ERR_RSP_PARITY	Parity errors on responses
AR_NIC_REQMON_AMO_EVENT_CNTR_ERR_RSP_SBE	SBEs in fill response payload
AR_NIC_REQMON_AMO_EVENT_CNTR_DATASTORE_WR_POISON	Datastore was written with poisoned fill data
AR_NIC_REQMON_AMO_EVENT_CNTR_DATASTORE_RD_POISON	Datastore was read with poisoned fill data
AR_NIC_REQMON_AMO_EVENT_CNTR_ERR_REQLIST_SBE	SBEs in request list

Name	Description
AR_NIC_REQMON_AMO_EVENT_CNTR_ERR_REQLIST_MBE	MBEs in request list
AR_NIC_REQMON_AMO_EVENT_CNTR_TIMEOUT_ARM	Cycles spent in this pre-timeout state
AR_NIC_RSPMON_PARB_EVENT_CNTR_AMO_PKTS	AMO packet count
AR_NIC_RSPMON_PARB_EVENT_CNTR_AMO_FLITS	AMO flit count
AR_NIC_RSPMON_PARB_EVENT_CNTR_AMO_BLOCKED	AMO blocked count
AR_PI_PMI_NREQ_AMO_PKTS	Incremented when the PMI receives an AMO request w/o errors from the NIC

**Table 2. Fast Memory Access (FMA) Performance Counters**

Name	Description
AR_NIC_REQMON_FMA_EVENT_CNTR_PKT	FMA to TARB packets
AR_NIC_REQMON_FMA_EVENT_CNTR_FLIT	FMA to DLA flits
AR_NIC_REQMON_FMA_EVENT_CNTR_TARB_STALLED	FMA to DLA stalls
AR_NIC_REQMON_FMA_EVENT_CNTR_PI_PKT	Processor interface packets
AR_NIC_REQMON_FMA_EVENT_CNTR_PI_FLIT	Processor interface flits
AR_NIC_REQMON_FMA_EVENT_CNTR_CQ	CQ read index updates
AR_NIC_REQMON_FMA_EVENT_CNTR_CQ_STALL	CQ read index update stalls
AR_NIC_REQMON_FMA_EVENT_CNTR_GET_REQ	Network GET requests
AR_NIC_REQMON_FMA_EVENT_CNTR_FAMO_REQ	Fetching AMO requests
AR_NIC_REQMON_FMA_EVENT_CNTR_PUT_REQ	Network PUT requests
AR_NIC_REQMON_FMA_EVENT_CNTR_NFAMO_REQ	Non-fetching AMO requests
AR_NIC_REQMON_FMA_EVENT_CNTR_HDR_PERR	Header flit parity errors
AR_NIC_REQMON_FMA_EVENT_CNTR_DATA_MBE	Uncorrectable data flit errors
AR_NIC_REQMON_FMA_EVENT_CNTR_DESC_MBE	Uncorrectable descriptor errors
AR_NIC_REQMON_FMA_EVENT_CNTR_INV_CMD	Invalid command errors
AR_NIC_REQMON_FMA_EVENT_CNTR_TDATA_SBE	TARB data buffer SBEs
AR_NIC_REQMON_DLA_EVENT_CNTR_FMA_PKT_ERR	AllocSSID, SyncComplete, or DLAMARKER request packet received from FMA with an lstatus of A_STATUS_FMA_UNCORRECTABLE or A_STATUS_DATA_ERR
AR_NIC_REQMON_FMA_EVENT_CNTR_PKT	FMA to TARB packets
AR_NIC_REQMON_FMA_EVENT_CNTR_FLIT	FMA to DLA flits
AR_NIC_REQMON_FMA_EVENT_CNTR_TARB_STALLED	FMA to DLA stalls

Name	Description
AR_NIC_REQMON_FMA_EVENT_CNTR_PI_PKT	Processor interface packets
AR_NIC_REQMON_FMA_EVENT_CNTR_PI_FLIT	Processor interface flits
AR_NIC_REQMON_FMA_EVENT_CNTR_CQ	CQ read index updates
AR_NIC_REQMON_FMA_EVENT_CNTR_CQ_STALL	CQ read index update stalls
AR_NIC_REQMON_FMA_EVENT_CNTR_GET_REQ	Network GET requests
AR_NIC_REQMON_FMA_EVENT_CNTR_FAMO_REQ	Fetching AMO requests
AR_NIC_REQMON_FMA_EVENT_CNTR_PUT_REQ	Network PUT requests
AR_NIC_REQMON_FMA_EVENT_CNTR_NFAMO_REQ	Non-fetching AMO requests
AR_NIC_REQMON_FMA_EVENT_CNTR_HDR_PERR	Header flit parity errors
AR_NIC_REQMON_FMA_EVENT_CNTR_DATA_MBE	Uncorrectable data flit errors
AR_NIC_REQMON_FMA_EVENT_CNTR_DESC_MBE	Uncorrectable descriptor errors
AR_NIC_REQMON_FMA_EVENT_CNTR_INV_CMD	Invalid command errors
AR_NIC_REQMON_FMA_EVENT_CNTR_TDATA_SBE	TARB data buffer SBEs
AR_NIC_REQMON_DLA_EVENT_CNTR_FMA_PKT_ERR	AllocSSID, SyncComplete, or DLAMARKER request packet received from FMA with an lstatus of A_STATUS_FMA_UNCORRECTABLE or A_STATUS_DATA_ERR
AR_NIC_REQMON_FMA_EVENT_CNTR_PKT	FMA to TARB packets
AR_NIC_REQMON_FMA_EVENT_CNTR_FLIT	FMA to DLA flits
AR_NIC_REQMON_FMA_EVENT_CNTR_TARB_STALLED	FMA to DLA stalls
AR_NIC_REQMON_FMA_EVENT_CNTR_PI_PKT	Processor interface packets
AR_NIC_REQMON_FMA_EVENT_CNTR_PI_FLIT	Processor interface flits
AR_NIC_REQMON_FMA_EVENT_CNTR_CQ	CQ read index updates
AR_NIC_REQMON_FMA_EVENT_CNTR_CQ_STALL	CQ read index update stalls
AR_NIC_REQMON_FMA_EVENT_CNTR_GET_REQ	Network GET requests
AR_NIC_REQMON_FMA_EVENT_CNTR_FAMO_REQ	Fetching AMO requests
AR_NIC_REQMON_FMA_EVENT_CNTR_PUT_REQ	Network PUT requests
AR_NIC_REQMON_FMA_EVENT_CNTR_NFAMO_REQ	Non-fetching AMO requests
AR_NIC_REQMON_FMA_EVENT_CNTR_HDR_PERR	Header flit parity errors
AR_NIC_REQMON_FMA_EVENT_CNTR_DATA_MBE	Uncorrectable data flit errors
AR_NIC_REQMON_FMA_EVENT_CNTR_DESC_MBE	Uncorrectable descriptor errors
AR_NIC_REQMON_FMA_EVENT_CNTR_INV_CMD	Invalid command errors
AR_NIC_REQMON_FMA_EVENT_CNTR_TDATA_SBE	TARB data buffer SBEs

Name	Description
AR_NIC_REQMON_DLA_EVENT_CNTR_FMA_PKT_ERR	AllocSSID, SyncComplete, or DLAMARKER request packet received from FMA with an lstatus of A_STATUS_FMA_UNCORRECTABLE or A_STATUS_DATA_ERR
AR_NIC_REQMON_FMA_EVENT_CNTR_PKT	FMA to TARB packets
AR_NIC_REQMON_FMA_EVENT_CNTR_FLIT	FMA to DLA flits
AR_NIC_REQMON_FMA_EVENT_CNTR_TARB_STALLED	FMA to DLA stalls
AR_NIC_REQMON_FMA_EVENT_CNTR_PI_PKT	Processor interface packets
AR_NIC_REQMON_FMA_EVENT_CNTR_PI_FLIT	Processor interface flits
AR_NIC_REQMON_FMA_EVENT_CNTR_CQ	CQ read index updates
AR_NIC_REQMON_FMA_EVENT_CNTR_CQ_STALL	CQ read index update stalls
AR_NIC_REQMON_FMA_EVENT_CNTR_GET_REQ	Network GET requests
AR_NIC_REQMON_FMA_EVENT_CNTR_FAMO_REQ	Fetching AMO requests
AR_NIC_REQMON_FMA_EVENT_CNTR_PUT_REQ	Network PUT requests
AR_NIC_REQMON_FMA_EVENT_CNTR_NFAMO_REQ	Non-fetching AMO requests
AR_NIC_REQMON_FMA_EVENT_CNTR_HDR_PERR	Header flit parity errors
AR_NIC_REQMON_FMA_EVENT_CNTR_DATA_MBE	Uncorrectable data flit errors
AR_NIC_REQMON_FMA_EVENT_CNTR_DESC_MBE	Uncorrectable descriptor errors
AR_NIC_REQMON_FMA_EVENT_CNTR_INV_CMD	Invalid command errors
AR_NIC_REQMON_FMA_EVENT_CNTR_TDATA_SBE	TARB data buffer SBEs
AR_NIC_REQMON_DLA_EVENT_CNTR_FMA_PKT_ERR	AllocSSID, SyncComplete, or DLAMARKER request packet received from FMA with an lstatus of A_STATUS_FMA_UNCORRECTABLE or A_STATUS_DATA_ERR
AR_NIC_REQMON_FMA_EVENT_CNTR_PKT	FMA to TARB packets
AR_NIC_REQMON_FMA_EVENT_CNTR_FLIT	FMA to DLA flits
AR_NIC_REQMON_FMA_EVENT_CNTR_TARB_STALLED	FMA to DLA stalls
AR_NIC_REQMON_FMA_EVENT_CNTR_PI_PKT	Processor interface packets

**Table 3. Network Address Translation (NAT) Performance Counters**

<b>Name</b>	<b>Description</b>
AR_NIC_NETMON_NAT_EVENT_CNTR_RAT_TRANSLATIONS	RAT translation count
AR_NIC_NETMON_NAT_EVENT_CNTR_RAT_STALLED	RAT stalled count
AR_NIC_NETMON_NAT_EVENT_CNTR_RAT_BLOCKED	RAT blocked count
AR_NIC_NETMON_NAT_EVENT_CNTR_CQ_TRANSLATIONS	CQ translation count
AR_NIC_NETMON_NAT_EVENT_CNTR_CQ_STALLED	CQ stalled count
AR_NIC_NETMON_NAT_EVENT_CNTR_CQ_BLOCKED	CQ blocked count
AR_NIC_NETMON_NAT_EVENT_CNTR_BTE_TRANSLATIONS	BTE translation count
AR_NIC_NETMON_NAT_EVENT_CNTR_BTE_STALLED	BTE stalled count
AR_NIC_NETMON_NAT_EVENT_CNTR_BTE_BLOCKED	BTE blocked count
AR_NIC_NETMON_NAT_EVENT_CNTR_PIPELINE_STALLED	Translation pipeline stalls
AR_NIC_NETMON_NAT_EVENT_CNTR_MDDT0_SBE	SBE's in MDDT RAM 0
AR_NIC_NETMON_NAT_EVENT_CNTR_MDDT0_MBE	MBE's in MDDT RAM 0
AR_NIC_NETMON_NAT_EVENT_CNTR_MDDT1_SBE	SBE's in MDDT RAM 1
AR_NIC_NETMON_NAT_EVENT_CNTR_MDDT1_MBE	MBE's in MDDT RAM 1
AR_NIC_NETMON_NAT_EVENT_CNTR_PTT_SBE	SBE's in PTT RAM
AR_NIC_NETMON_NAT_EVENT_CNTR_PTT_MBE	MBE's in PTT RAM
AR_NIC_REQMON_BTE_EVENT_CNTR_NAT_ERR0	Channel 0 NAT errors
AR_NIC_REQMON_BTE_EVENT_CNTR_NAT_ERR1	Channel 1 NAT errors
AR_NIC_REQMON_BTE_EVENT_CNTR_NAT_ERR2	Channel 2 NAT errors
AR_NIC_REQMON_BTE_EVENT_CNTR_NAT_ERR3	Channel 3 NAT errors

**Table 4. Netlink (NL) Performance Counters**

<b>Name</b>	<b>Description</b>
AR_NL_PRF_CTRL	Set to zero to configure default values
AR_NL_PRF_CTRL:DISABLE_AUTO_UPDATE	Disables the auto-updating of the performance counters, turns off widget
AR_NL_PRF_CTRL:ENABLE	Writing a 1 enables performance counters, writing 0 disables counting
AR_NL_PRF_CTRL:CLEAR	Initiates the clear sequence which clears all performance counters, read returns zero
AR_NL_PRF_CTRL:SEL_LM_TRIGGERS	Select logic monitor triggers
AR_NL_PRF_CTRL:COUNTERS_CLEAR	This bit becomes set upon the completion of the clear sequence

Name	Description
AR_NIC_NPT_PRF_EN:PRF_STALL_EN	Enable AR_NIC_NPT_PRF_NL_STALL_DURATION
AR_NIC_NPT_PRF_NL_STALL_DURATION	NL stall duration
AR_NL_PRF_PTILE_0_REQ_TO_NIC_0	Requests from PTILE 0 to NIC 0
AR_NL_PRF_PTILE_0_REQ_TO_NIC_1	Requests from PTILE 0 to NIC 1
AR_NL_PRF_PTILE_0_REQ_TO_NIC_0_STALLED	Clock cycles PTILE 0 requests are stalled to NIC 0
AR_NL_PRF_PTILE_0_REQ_TO_NIC_1_STALLED	Clock cycles PTILE 0 requests are stalled to NIC 1
AR_NL_PRF_PTILE_0_REQ_IN_NW_FLITS	NW request flits received from PTILE 0
AR_NL_PRF_PTILE_0_REQ_IN_NW_PKTS	Request packets received from PTILE 0
AR_NL_PRF_PTILE_1_REQ_TO_NIC_0	Requests from PTILE 1 to NIC 0
AR_NL_PRF_PTILE_1_REQ_TO_NIC_1	Requests from PTILE 1 to NIC 1
AR_NL_PRF_PTILE_1_REQ_TO_NIC_0_STALLED	Clock cycles PTILE 1 requests are stalled to NIC 0
AR_NL_PRF_PTILE_1_REQ_TO_NIC_1_STALLED	Clock cycles PTILE 1 requests are stalled to NIC 1
AR_NL_PRF_PTILE_1_REQ_IN_NW_FLITS	NW request flits received from PTILE 1
AR_NL_PRF_PTILE_1_REQ_IN_NW_PKTS	Request packets received from PTILE 1
AR_NL_PRF_PTILE_2_REQ_TO_NIC_0	Requests from PTILE 2 to NIC 0
AR_NL_PRF_PTILE_2_REQ_TO_NIC_1	Requests from PTILE 2 to NIC 1
AR_NL_PRF_PTILE_2_REQ_TO_NIC_0_STALLED	Clock cycles PTILE 2 requests are stalled to NIC 0
AR_NL_PRF_PTILE_2_REQ_TO_NIC_1_STALLED	Clock cycles PTILE 2 requests are stalled to NIC 1
AR_NL_PRF_PTILE_2_REQ_IN_NW_FLITS	NW request flits received from PTILE 2
AR_NL_PRF_PTILE_2_REQ_IN_NW_PKTS	Request packets received from PTILE 2
AR_NL_PRF_PTILE_3_REQ_TO_NIC_0	Requests from PTILE 3 to NIC 0
AR_NL_PRF_PTILE_3_REQ_TO_NIC_1	Requests from PTILE 3 to NIC 1
AR_NL_PRF_PTILE_3_REQ_TO_NIC_0_STALLED	Clock cycles PTILE 3 requests are stalled to NIC 0
AR_NL_PRF_PTILE_3_REQ_TO_NIC_1_STALLED	Clock cycles PTILE 3 requests are stalled to NIC 1
AR_NL_PRF_PTILE_3_REQ_IN_NW_FLITS	NW request flits received from PTILE 3
AR_NL_PRF_PTILE_3_REQ_IN_NW_PKTS	Request packets received from PTILE 3
AR_NL_PRF_PTILE_4_REQ_TO_NIC_2	Requests from PTILE 4 to NIC 2

Name	Description
AR_NL_PRF_PTILE_4_REQ_TO_NIC_3	Requests from PTILE 4 to NIC 3
AR_NL_PRF_PTILE_4_REQ_TO_NIC_2_STALLED	Clock cycles PTILE 4 requests are stalled to NIC 2
AR_NL_PRF_PTILE_4_REQ_TO_NIC_3_STALLED	Clock cycles PTILE 4 requests are stalled to NIC 3
AR_NL_PRF_PTILE_4_REQ_IN_NW_FLITS	NW request flits received from PTILE 4
AR_NL_PRF_PTILE_4_REQ_IN_NW_PKTS	Request packets received from PTILE 4
AR_NL_PRF_PTILE_5_REQ_TO_NIC_2	Requests from PTILE 5 to NIC 2
AR_NL_PRF_PTILE_5_REQ_TO_NIC_3	Requests from PTILE 5 to NIC 3
AR_NL_PRF_PTILE_5_REQ_TO_NIC_2_STALLED	Clock cycles PTILE 5 requests are stalled to NIC 2
AR_NL_PRF_PTILE_5_REQ_TO_NIC_3_STALLED	Clock cycles PTILE 5 requests are stalled to NIC 3
AR_NL_PRF_PTILE_5_REQ_IN_NW_FLITS	NW request flits received from PTILE 5
AR_NL_PRF_PTILE_5_REQ_IN_NW_PKTS	Request packets received from PTILE 5
AR_NL_PRF_PTILE_6_REQ_TO_NIC_2	Requests from PTILE 6 to NIC 2
AR_NL_PRF_PTILE_6_REQ_TO_NIC_3	Requests from PTILE 6 to NIC 3
AR_NL_PRF_PTILE_6_REQ_TO_NIC_2_STALLED	Clock cycles PTILE 6 requests are stalled to NIC 2
AR_NL_PRF_PTILE_6_REQ_TO_NIC_3_STALLED	Clock cycles PTILE 6 requests are stalled to NIC 3
AR_NL_PRF_PTILE_6_REQ_IN_NW_FLITS	NW request flits received from PTILE 6
AR_NL_PRF_PTILE_6_REQ_IN_NW_PKTS	Request packets received from PTILE 6
AR_NL_PRF_PTILE_7_REQ_TO_NIC_2	Requests from PTILE 7 to NIC 2
AR_NL_PRF_PTILE_7_REQ_TO_NIC_3	Requests from PTILE 7 to NIC 3
AR_NL_PRF_PTILE_7_REQ_TO_NIC_2_STALLED	Clock cycles PTILE 7 requests are stalled to NIC 2
AR_NL_PRF_PTILE_7_REQ_TO_NIC_3_STALLED	Clock cycles PTILE 7 requests are stalled to NIC 3
AR_NL_PRF_PTILE_7_REQ_IN_NW_FLITS	NW request flits received from PTILE 7
AR_NL_PRF_PTILE_7_REQ_IN_NW_PKTS	Request packets received from PTILE 7
AR_NL_PRF_PTILE_0_RSP_TO_NIC_0	Responses from PTILE 0 to NIC 0
AR_NL_PRF_PTILE_0_RSP_TO_NIC_1	Responses from PTILE 0 to NIC 1
AR_NL_PRF_PTILE_0_RSP_TO_NIC_0_STALLED	Clock cycles PTILE 0 responses are stalled to NIC 0



Name	Description
AR_NL_PRF_PTILE_0_RSP_TO_NIC_1_STALLED	Clock cycles PTILE 0 responses are stalled to NIC 1
AR_NL_PRF_PTILE_0_RSP_IN_NW_FLITS	NW response flits received from PTILE 0
AR_NL_PRF_PTILE_0_RSP_IN_NW_PKTS	Response packets received from PTILE 0
AR_NL_PRF_PTILE_1_RSP_TO_NIC_0	Responses from PTILE 1 to NIC 0
AR_NL_PRF_PTILE_1_RSP_TO_NIC_1	Responses from PTILE 1 to NIC 1
AR_NL_PRF_PTILE_1_RSP_TO_NIC_0_STALLED	Clock cycles PTILE 1 responses are stalled to NIC 0
AR_NL_PRF_PTILE_1_RSP_TO_NIC_1_STALLED	Clock cycles PTILE 1 responses are stalled to NIC 1
AR_NL_PRF_PTILE_1_RSP_IN_NW_FLITS	NW response flits received from PTILE 1
AR_NL_PRF_PTILE_1_RSP_IN_NW_PKTS	Response packets received from PTILE 1
AR_NL_PRF_PTILE_2_RSP_TO_NIC_0	Responses from PTILE 2 to NIC 0
AR_NL_PRF_PTILE_2_RSP_TO_NIC_1	Responses from PTILE 2 to NIC 1
AR_NL_PRF_PTILE_2_RSP_TO_NIC_0_STALLED	Clock cycles PTILE 2 responses are stalled to NIC 0
AR_NL_PRF_PTILE_2_RSP_TO_NIC_1_STALLED	Clock cycles PTILE 2 responses are stalled to NIC 1
AR_NL_PRF_PTILE_2_RSP_IN_NW_FLITS	NW response flits received from PTILE 2
AR_NL_PRF_PTILE_2_RSP_IN_NW_PKTS	Response packets received from PTILE 2
AR_NL_PRF_PTILE_3_RSP_TO_NIC_0	Responses from PTILE 3 to NIC 0
AR_NL_PRF_PTILE_3_RSP_TO_NIC_1	Responses from PTILE 3 to NIC 1
AR_NL_PRF_PTILE_3_RSP_TO_NIC_0_STALLED	Clock cycles PTILE 3 responses are stalled to NIC 0
AR_NL_PRF_PTILE_3_RSP_TO_NIC_1_STALLED	Clock cycles PTILE 3 responses are stalled to NIC 1
AR_NL_PRF_PTILE_3_RSP_IN_NW_FLITS	NW response flits received from PTILE 3
AR_NL_PRF_PTILE_3_RSP_IN_NW_PKTS	Response packets received from PTILE 3
AR_NL_PRF_PTILE_4_RSP_TO_NIC_2	Responses from PTILE 4 to NIC 2
AR_NL_PRF_PTILE_4_RSP_TO_NIC_3	Responses from PTILE 4 to NIC 3
AR_NL_PRF_PTILE_4_RSP_TO_NIC_2_STALLED	Clock cycles PTILE 4 responses are stalled to NIC 2
AR_NL_PRF_PTILE_4_RSP_TO_NIC_3_STALLED	Clock cycles PTILE 4 responses are stalled to NIC 3
AR_NL_PRF_PTILE_4_RSP_IN_NW_FLITS	NW response flits received from PTILE 4

Name	Description
AR_NL_PRF_PTILE_4_RSP_IN_NW_PKTS	Response packets received from PTILE 4
AR_NL_PRF_PTILE_5_RSP_TO_NIC_2	Responses from PTILE 5 to NIC 2
AR_NL_PRF_PTILE_5_RSP_TO_NIC_3	Responses from PTILE 5 to NIC 3
AR_NL_PRF_PTILE_5_RSP_TO_NIC_2_STALLED	Clock cycles PTILE 5 responses are stalled to NIC 2
AR_NL_PRF_PTILE_5_RSP_TO_NIC_3_STALLED	Clock cycles PTILE 5 responses are stalled to NIC 3
AR_NL_PRF_PTILE_5_RSP_IN_NW_FLITS	NW response flits received from PTILE 5
AR_NL_PRF_PTILE_5_RSP_IN_NW_PKTS	Response packets received from PTILE 5
AR_NL_PRF_PTILE_6_RSP_TO_NIC_2	Responses from PTILE 6 to NIC 2
AR_NL_PRF_PTILE_6_RSP_TO_NIC_3	Responses from PTILE 6 to NIC 3
AR_NL_PRF_PTILE_6_RSP_TO_NIC_2_STALLED	Clock cycles PTILE 6 responses are stalled to NIC 2
AR_NL_PRF_PTILE_6_RSP_TO_NIC_3_STALLED	Clock cycles PTILE 6 responses are stalled to NIC 3
AR_NL_PRF_PTILE_6_RSP_IN_NW_FLITS	NW response flits received from PTILE 6
AR_NL_PRF_PTILE_6_RSP_IN_NW_PKTS	Response packets received from PTILE 6
AR_NL_PRF_PTILE_7_RSP_TO_NIC_2	Responses from PTILE 7 to NIC 2
AR_NL_PRF_PTILE_7_RSP_TO_NIC_3	Responses from PTILE 7 to NIC 3
AR_NL_PRF_PTILE_7_RSP_TO_NIC_2_STALLED	Clock cycles PTILE 7 responses are stalled to NIC 2
AR_NL_PRF_PTILE_7_RSP_TO_NIC_3_STALLED	Clock cycles PTILE 7 responses are stalled to NIC 3
AR_NL_PRF_PTILE_7_RSP_IN_NW_FLITS	NW response flits received from PTILE 7
AR_NL_PRF_PTILE_7_RSP_IN_NW_PKTS	Response packets received from PTILE 7
AR_NL_PRF_REQ_PTILES_TO_NIC_0_FLITS	NIC request flits to NIC 0
AR_NL_PRF_REQ_PTILES_TO_NIC_0_PKTS	Request packets to NIC 0
AR_NL_PRF_REQ_PTILES_TO_NIC_0_STALLED	Clocks all request PTILES have stalled to NIC 0
AR_NL_PRF_REQ_PTILES_TO_NIC_1_FLITS	NIC request flits to NIC 1
AR_NL_PRF_REQ_PTILES_TO_NIC_1_PKTS	Request packets to NIC 1
AR_NL_PRF_REQ_PTILES_TO_NIC_1_STALLED	Clocks all request PTILES have stalled to NIC 1
AR_NL_PRF_REQ_PTILES_TO_NIC_2_FLITS	NIC request flits to NIC 2
AR_NL_PRF_REQ_PTILES_TO_NIC_2_PKTS	Request packets to NIC 2
AR_NL_PRF_REQ_PTILES_TO_NIC_2_STALLED	Clocks all request PTILES have stalled to NIC 2

Name	Description
AR_NL_PRF_REQ_PTILES_TO_NIC_3_FLITS	NIC request flits to NIC 3
AR_NL_PRF_REQ_PTILES_TO_NIC_3_PKTS	Request packets to NIC 3
AR_NL_PRF_REQ_PTILES_TO_NIC_3_STALLED	Clocks all request PTILES have stalled to NIC 3
AR_NL_PRF_RSP_PTILES_TO_NIC_0_FLITS	NIC response flits to NIC 0
AR_NL_PRF_RSP_PTILES_TO_NIC_0_PKTS	Response packets to NIC 0
AR_NL_PRF_RSP_PTILES_TO_NIC_0_STALLED	Clocks all response PTILES have stalled to NIC 0
AR_NL_PRF_RSP_PTILES_TO_NIC_1_FLITS	NIC response flits to NIC 1
AR_NL_PRF_RSP_PTILES_TO_NIC_1_PKTS	Response packets to NIC 1
AR_NL_PRF_RSP_PTILES_TO_NIC_1_STALLED	Clocks all response PTILES have stalled to NIC 1
AR_NL_PRF_RSP_PTILES_TO_NIC_2_FLITS	NIC response flits to NIC 2
AR_NL_PRF_RSP_PTILES_TO_NIC_2_PKTS	Response packets to NIC 2
AR_NL_PRF_RSP_PTILES_TO_NIC_2_STALLED	Clocks all response PTILES have stalled to NIC 2
AR_NL_PRF_RSP_PTILES_TO_NIC_3_FLITS	NIC response flits to NIC 3
AR_NL_PRF_RSP_PTILES_TO_NIC_3_PKTS	Response packets to NIC 3
AR_NL_PRF_RSP_PTILES_TO_NIC_3_STALLED	Clocks all response PTILES have stalled to NIC 3
AR_NL_PRF_NIC_0_REQ_TO_PTILE_0	Requests from NIC 0 to PTILE 0
AR_NL_PRF_NIC_1_REQ_TO_PTILE_0	Requests from NIC 1 to PTILE 0
AR_NL_PRF_NIC_0_REQ_TO_PTILE_0_STALLED	Clock cycles NIC 0 requests are stalled to PTILE 0
AR_NL_PRF_NIC_1_REQ_TO_PTILE_0_STALLED	Clock cycles NIC 1 requests are stalled to PTILE 0
AR_NL_PRF_PTILE_0_REQ_OUT_NW_FLITS	NW request flits sent to PTILE 0
AR_NL_PRF_PTILE_0_REQ_OUT_NW_PKTS	Request packets sent to PTILE 0
AR_NL_PRF_NIC_0_REQ_TO_PTILE_1	Requests from NIC 0 to PTILE 1
AR_NL_PRF_NIC_1_REQ_TO_PTILE_1	Requests from NIC 1 to PTILE 1
AR_NL_PRF_NIC_0_REQ_TO_PTILE_1_STALLED	Clock cycles NIC 0 requests are stalled to PTILE 1
AR_NL_PRF_NIC_1_REQ_TO_PTILE_1_STALLED	Clock cycles NIC 1 requests are stalled to PTILE 1
AR_NL_PRF_PTILE_1_REQ_OUT_NW_FLITS	NW request flits sent to PTILE 1
AR_NL_PRF_PTILE_1_REQ_OUT_NW_PKTS	Request packets sent to PTILE 1

Name	Description
AR_NL_PRF_NIC_0_REQ_TO_PTILE_2	Requests from NIC 0 to PTILE 2
AR_NL_PRF_NIC_1_REQ_TO_PTILE_2	Requests from NIC 1 to PTILE 2
AR_NL_PRF_NIC_0_REQ_TO_PTILE_2_STALLED	Clock cycles NIC 0 requests are stalled to PTILE 2
AR_NL_PRF_NIC_1_REQ_TO_PTILE_2_STALLED	Clock cycles NIC 1 requests are stalled to PTILE 2
AR_NL_PRF_PTILE_2_REQ_OUT_NW_FLITS	NW request flits sent to PTILE 2
AR_NL_PRF_PTILE_2_REQ_OUT_NW_PKTS	Request packets sent to PTILE 2
AR_NL_PRF_NIC_0_REQ_TO_PTILE_3	Requests from NIC 0 to PTILE 3
AR_NL_PRF_NIC_1_REQ_TO_PTILE_3	Requests from NIC 1 to PTILE 3
AR_NL_PRF_NIC_0_REQ_TO_PTILE_3_STALLED	Clock cycles NIC 0 requests are stalled to PTILE 3
AR_NL_PRF_NIC_1_REQ_TO_PTILE_3_STALLED	Clock cycles NIC 1 requests are stalled to PTILE 3
AR_NL_PRF_PTILE_3_REQ_OUT_NW_FLITS	NW request flits sent to PTILE 3
AR_NL_PRF_PTILE_3_REQ_OUT_NW_PKTS	Request packets sent to PTILE 3
AR_NL_PRF_NIC_2_REQ_TO_PTILE_4	Requests from NIC 2 to PTILE 4
AR_NL_PRF_NIC_3_REQ_TO_PTILE_4	Requests from NIC 3 to PTILE 4
AR_NL_PRF_NIC_2_REQ_TO_PTILE_4_STALLED	Clock cycles NIC 2 requests are stalled to PTILE 4
AR_NL_PRF_NIC_3_REQ_TO_PTILE_4_STALLED	Clock cycles NIC 3 requests are stalled to PTILE 4
AR_NL_PRF_PTILE_4_REQ_OUT_NW_FLITS	NW request flits sent to PTILE 4
AR_NL_PRF_PTILE_4_REQ_OUT_NW_PKTS	Request packets sent to PTILE 4
AR_NL_PRF_NIC_2_REQ_TO_PTILE_5	Requests from NIC 2 to PTILE 5
AR_NL_PRF_NIC_3_REQ_TO_PTILE_5	Requests from NIC 3 to PTILE 5
AR_NL_PRF_NIC_2_REQ_TO_PTILE_5_STALLED	Clock cycles NIC 2 requests are stalled to PTILE 5
AR_NL_PRF_NIC_3_REQ_TO_PTILE_5_STALLED	Clock cycles NIC 3 requests are stalled to PTILE 5
AR_NL_PRF_PTILE_5_REQ_OUT_NW_PKTS	NW request flits sent to PTILE 5
AR_NL_PRF_PTILE_5_REQ_OUT_NW_FLITS	Request packets sent to PTILE 5
AR_NL_PRF_NIC_2_REQ_TO_PTILE_6	Requests from NIC 2 to PTILE 6
AR_NL_PRF_NIC_3_REQ_TO_PTILE_6	Requests from NIC 3 to PTILE 6

Name	Description
AR_NL_PRF_NIC_2_REQ_TO_PTILE_6_STALLED	Clock cycles NIC 2 requests are stalled to PTILE 6
AR_NL_PRF_NIC_3_REQ_TO_PTILE_6_STALLED	Clock cycles NIC 3 requests are stalled to PTILE 6
AR_NL_PRF_PTILE_6_REQ_OUT_NW_FLITS	NW request flits sent to PTILE 6
AR_NL_PRF_PTILE_6_REQ_OUT_NW_PKTS	Request packets sent to PTILE 6
AR_NL_PRF_NIC_2_REQ_TO_PTILE_7	Requests from NIC 2 to PTILE 7
AR_NL_PRF_NIC_3_REQ_TO_PTILE_7	Requests from NIC 3 to PTILE 7
AR_NL_PRF_NIC_2_REQ_TO_PTILE_7_STALLED	Clock cycles NIC 2 requests are stalled to PTILE 7
AR_NL_PRF_NIC_3_REQ_TO_PTILE_7_STALLED	Clock cycles NIC 3 requests are stalled to PTILE 7
AR_NL_PRF_PTILE_7_REQ_OUT_NW_FLITS	NW request flits sent to PTILE 7
AR_NL_PRF_PTILE_7_REQ_OUT_NW_PKTS	Request packets sent to PTILE 7
AR_NL_PRF_NIC_0_RSP_TO_PTILE_0	Responses from NIC 0 to PTILE 0
AR_NL_PRF_NIC_1_RSP_TO_PTILE_0	Responses from NIC 1 to PTILE 0
AR_NL_PRF_NIC_0_RSP_TO_PTILE_0_STALLED	Clock cycles NIC 0 responses are stalled to PTILE 0
AR_NL_PRF_NIC_1_RSP_TO_PTILE_0_STALLED	Clock cycles NIC 1 responses are stalled to PTILE 0
AR_NL_PRF_PTILE_0_RSP_OUT_NW_FLITS	NW response flits sent to PTILE 0
AR_NL_PRF_PTILE_0_RSP_OUT_NW_PKTS	Response packets sent to PTILE 0
AR_NL_PRF_NIC_0_RSP_TO_PTILE_1	Responses from NIC 0 to PTILE 1
AR_NL_PRF_NIC_1_RSP_TO_PTILE_1	Responses from NIC 1 to PTILE 1
AR_NL_PRF_NIC_0_RSP_TO_PTILE_1_STALLED	Clock cycles NIC 0 responses are stalled to PTILE 1
AR_NL_PRF_NIC_1_RSP_TO_PTILE_1_STALLED	Clock cycles NIC 1 responses are stalled to PTILE 1
AR_NL_PRF_PTILE_1_RSP_OUT_NW_FLITS	NW response flits sent to PTILE 1
AR_NL_PRF_PTILE_1_RSP_OUT_NW_PKTS	Response packets sent to PTILE 1
AR_NL_PRF_NIC_0_RSP_TO_PTILE_2	Responses from NIC 0 to PTILE 2
AR_NL_PRF_NIC_1_RSP_TO_PTILE_2	Responses from NIC 1 to PTILE 2
AR_NL_PRF_NIC_0_RSP_TO_PTILE_2_STALLED	Clock cycles NIC 0 responses are stalled to PTILE 2

Name	Description
AR_NL_PRF_NIC_1_RSP_TO_PTILE_2_STALLED	Clock cycles NIC 1 responses are stalled to PTILE 2
AR_NL_PRF_PTILE_2_RSP_OUT_NW_FLITS	NW response flits sent to PTILE 2
AR_NL_PRF_PTILE_2_RSP_OUT_NW_PKTS	Response packets sent to PTILE 2
AR_NL_PRF_NIC_0_RSP_TO_PTILE_3	Responses from NIC 0 to PTILE 3
AR_NL_PRF_NIC_1_RSP_TO_PTILE_3	Responses from NIC 1 to PTILE 3
AR_NL_PRF_NIC_0_RSP_TO_PTILE_3_STALLED	Clock cycles NIC 0 responses are stalled to PTILE 3
AR_NL_PRF_NIC_1_RSP_TO_PTILE_3_STALLED	Clock cycles NIC 1 responses are stalled to PTILE 3
AR_NL_PRF_PTILE_3_RSP_OUT_NW_FLITS	NW response flits sent to PTILE 3
AR_NL_PRF_PTILE_3_RSP_OUT_NW_PKTS	Response packets sent to PTILE 3
AR_NL_PRF_NIC_2_RSP_TO_PTILE_4	Responses from NIC 2 to PTILE 4
AR_NL_PRF_NIC_3_RSP_TO_PTILE_4	Responses from NIC 3 to PTILE 4
AR_NL_PRF_NIC_2_RSP_TO_PTILE_4_STALLED	Clock cycles NIC 2 responses are stalled to PTILE 4
AR_NL_PRF_NIC_3_RSP_TO_PTILE_4_STALLED	Clock cycles NIC 3 responses are stalled to PTILE 4
AR_NL_PRF_PTILE_4_RSP_OUT_NW_FLITS	NW response flits sent to PTILE 4
AR_NL_PRF_PTILE_4_RSP_OUT_NW_PKTS	Response packets sent to PTILE 4
AR_NL_PRF_NIC_2_RSP_TO_PTILE_5	Responses from NIC 2 to PTILE 5
AR_NL_PRF_NIC_3_RSP_TO_PTILE_5	Responses from NIC 3 to PTILE 5
AR_NL_PRF_NIC_2_RSP_TO_PTILE_5_STALLED	Clock cycles NIC 2 responses are stalled to PTILE 5
AR_NL_PRF_NIC_3_RSP_TO_PTILE_5_STALLED	Clock cycles NIC 3 responses are stalled to PTILE 5
AR_NL_PRF_PTILE_5_RSP_OUT_NW_FLITS	NW response flits sent to PTILE 5
AR_NL_PRF_PTILE_5_RSP_OUT_NW_PKTS	Response packets sent to PTILE 5
AR_NL_PRF_NIC_2_RSP_TO_PTILE_6	Responses from NIC 2 to PTILE 6
AR_NL_PRF_NIC_3_RSP_TO_PTILE_6	Responses from NIC 3 to PTILE 6
AR_NL_PRF_NIC_2_RSP_TO_PTILE_6_STALLED	Clock cycles NIC 2 responses are stalled to PTILE 6
AR_NL_PRF_NIC_3_RSP_TO_PTILE_6_STALLED	Clock cycles NIC 3 responses are stalled to PTILE 6
AR_NL_PRF_PTILE_6_RSP_OUT_NW_FLITS	NW response flits sent to PTILE 6

Name	Description
AR_NL_PRF_PTILE_6_RSP_OUT_NW_PKTS	Response packets sent to PTILE 6
AR_NL_PRF_NIC_2_RSP_TO_PTILE_7	Responses from NIC 2 to PTILE 7
AR_NL_PRF_NIC_3_RSP_TO_PTILE_7	Responses from NIC 3 to PTILE 7
AR_NL_PRF_NIC_2_RSP_TO_PTILE_7_STALLED	Clock cycles NIC 2 responses are stalled to PTILE 7
AR_NL_PRF_NIC_3_RSP_TO_PTILE_7_STALLED	Clock cycles NIC 3 responses are stalled to PTILE 7
AR_NL_PRF_PTILE_7_RSP_OUT_NW_FLITS	NW response flits sent to PTILE 7
AR_NL_PRF_PTILE_7_RSP_OUT_NW_PKTS	Response packets sent to PTILE 7
AR_NL_PRF_REQ_NIC_0_TO_PTILES_FLITS	NIC request flits from NIC 0
AR_NL_PRF_REQ_NIC_0_TO_PTILES_PKTS	Request packets from NIC 0
AR_NL_PRF_REQ_NIC_0_TO_PTILES_STALLED	CPs requests from NIC 0 have stalled to all PTILEs
AR_NL_PRF_REQ_NIC_1_TO_PTILES_FLITS	NIC request flits from NIC 1
AR_NL_PRF_REQ_NIC_1_TO_PTILES_PKTS	Request packets from NIC 1
AR_NL_PRF_REQ_NIC_1_TO_PTILES_STALLED	CPs requests from NIC 1 have stalled to all PTILEs
AR_NL_PRF_REQ_NIC_2_TO_PTILES_FLITS	NIC request flits from NIC 2
AR_NL_PRF_REQ_NIC_2_TO_PTILES_PKTS	Request packets from NIC 2
AR_NL_PRF_REQ_NIC_2_TO_PTILES_STALLED	CPs requests from NIC 2 have stalled to all PTILEs
AR_NL_PRF_REQ_NIC_3_TO_PTILES_FLITS	NIC request flits from NIC 3
AR_NL_PRF_REQ_NIC_3_TO_PTILES_PKTS	Request packets from NIC 3
AR_NL_PRF_REQ_NIC_3_TO_PTILES_STALLED	CPs requests from NIC 3 have stalled to all PTILEs
AR_NL_PRF_RSP_NIC_0_TO_PTILES_FLITS	NIC response flits from NIC 0
AR_NL_PRF_RSP_NIC_0_TO_PTILES_PKTS	Response packets from NIC 0
AR_NL_PRF_RSP_NIC_0_TO_PTILES_STALLED	CPs responses from NIC 0 have stalled to all PTILEs
AR_NL_PRF_RSP_NIC_1_TO_PTILES_FLITS	NIC response flits from NIC 1
AR_NL_PRF_RSP_NIC_1_TO_PTILES_PKTS	Response packets from NIC 1
AR_NL_PRF_RSP_NIC_1_TO_PTILES_STALLED	CPs responses from NIC 1 have stalled to all PTILEs
AR_NL_PRF_RSP_NIC_2_TO_PTILES_FLITS	NIC response flits from NIC 2
AR_NL_PRF_RSP_NIC_2_TO_PTILES_PKTS	Response packets from NIC 2

Name	Description
AR_NL_PRF_RSP_NIC_2_TO_PTILES_STALLED	CPs responses from NIC 2 have stalled to all PTILEs
AR_NL_PRF_RSP_NIC_3_TO_PTILES_FLITS	NIC response flits from NIC 3
AR_NL_PRF_RSP_NIC_3_TO_PTILES_PKTS	Response packets from NIC 3
AR_NL_PRF_RSP_NIC_3_TO_PTILES_STALLED	CPs responses from NIC 3 have stalled to all PTILEs
AR_NIC_NETMON_RAT_EVENT_CNTR_NL_PKTS	NL packet count
AR_NIC_NETMON_RAT_EVENT_CNTR_NL_FLITS	NL flit count
AR_NIC_NETMON_RAT_EVENT_CNTR_NL_BLOCKED	NL blocked count
AR_NIC_RSPMON_NPT_EVENT_CNTR_NL_PKTS	NL response packet count
AR_NIC_RSPMON_NPT_EVENT_CNTR_NL_FLITS	NL response flit count
AR_NIC_RSPMON_NPT_EVENT_CNTR_NL_STALLED	NL response stalled count

**Table 5. Non-posted Table (NPT) Performance Counters**

Name	Description
AR_NIC_NPT_PRF_EN	Set to zero to configure default values
AR_NIC_NPT_PRF_EN:PRF_STALL_EN	Enable AR_NIC_NPT_PRF_NL_STALL_DURATION
AR_NIC_NPT_PRF_EN:PRF_FULL_EN	Enable AR_NIC_NPT_PRF_FULL_DURATION
AR_NIC_NPT_PRF_FULL_DURATION	Full duration
AR_NIC_NPT_PRF_NL_STALL_DURATION	NL stall duration
AR_NIC_RSPMON_CQ_EVENT_CNTR_NPT_PKT	CQ NPT packets
AR_NIC_RSPMON_CQ_EVENT_CNTR_NPT_FLIT	CQ NPT flits
AR_NIC_RSPMON_CQ_EVENT_CNTR_NPT_STALLED	Clocks when NPT packet is stalled due to not enough credits
AR_NIC_RSPMON_NPT_EVENT_CNTR_CQ_PKTS	CQ input packet count
AR_NIC_RSPMON_NPT_EVENT_CNTR_CQ_FLITS	CQ input flit count
AR_NIC_RSPMON_NPT_EVENT_CNTR_CQ_BLOCKED	CQ input blocked count
AR_NIC_RSPMON_NPT_EVENT_CNTR_RMT_PKTS	RMT input packet count
AR_NIC_RSPMON_NPT_EVENT_CNTR_RMT_FLITS	RMT input flit count
AR_NIC_RSPMON_NPT_EVENT_CNTR_RMT_BLOCKED	RMT input blocked count
AR_NIC_RSPMON_NPT_EVENT_CNTR_INPUT_PKTS	INPUT (RMT or CQ) packet count
AR_NIC_RSPMON_NPT_EVENT_CNTR_INPUT_FLITS	INPUT (RMT or CQ) flit count
AR_NIC_RSPMON_NPT_EVENT_CNTR_INPUT_STALLED	INPUT (RMT or CQ) stalled count



<b>Name</b>	<b>Description</b>
AR_NIC_RSPMON_NPT_EVENT_CNTR_NP_PKTS	Non-posted response packet count (packets that are not PKT_IGNORE , PKT_DROP, or PKT_CW)
AR_NIC_RSPMON_NPT_EVENT_CNTR_NP_FLITS	Non-posted response flit count
AR_NIC_RSPMON_NPT_EVENT_CNTR_NP_BLOCKED	Non-posted response blocked count
AR_NIC_RSPMON_NPT_EVENT_CNTR_P_PKTS	Posted response packet count
AR_NIC_RSPMON_NPT_EVENT_CNTR_P_BLOCKED	Posted response blocked count
AR_NIC_RSPMON_NPT_EVENT_CNTR_CE_PKTS	CE response packet count
AR_NIC_RSPMON_NPT_EVENT_CNTR_CE_BLOCKED	CE response blocked count
AR_NIC_RSPMON_NPT_EVENT_CNTR_NL_PKTS	NL response packet count
AR_NIC_RSPMON_NPT_EVENT_CNTR_NL_FLITS	NL response flit count
AR_NIC_RSPMON_NPT_EVENT_CNTR_NL_STALLED	NL response stalled count
AR_NIC_RSPMON_NPT_EVENT_CNTR_RSP_PKT	PI response packet count
AR_NIC_RSPMON_NPT_EVENT_CNTR_RSP_FLIT	PI response flit count
AR_NIC_RSPMON_NPT_EVENT_CNTR_PKT_IGNORE	PI packets not intended for the NPT (including unsolicited and parity errors)
AR_NIC_RSPMON_NPT_EVENT_CNTR_PKT_DROP	PI packets intended for the NPT but dropped (either response suppressed or MBE on NPT RAM)
AR_NIC_RSPMON_NPT_EVENT_CNTR_PKT_CW	PI packets (completion wait flush response)
AR_NIC_RSPMON_NPT_EVENT_CNTR_ERR_SBE	SBE's on NPT RAM
AR_NIC_RSPMON_NPT_EVENT_CNTR_ERR_MBE	MBE's on NPT RAM
AR_NIC_RSPMON_NPT_EVENT_CNTR_REQ_PE	Parity error in request header
AR_NIC_RSPMON_NPT_EVENT_CNTR_RSP_PE	Parity error in response header
AR_NIC_RSPMON_NPT_EVENT_CNTR_RSP_SBE	SBE in response payload
AR_NIC_RSPMON_NPT_EVENT_CNTR_RSP_MBE	MBE in response payload

**Table 6. Outstanding Request Buffer (ORB) Performance Counters**

<b>Name</b>	<b>Description</b>
AR_NIC_ORB_CFG_NET_RSP_HIST_1	Set to zero to configure default values
AR_NIC_ORB_CFG_NET_RSP_HIST_1:BIN3_MIN	Boundary between bin 2 and bin 3
AR_NIC_ORB_CFG_NET_RSP_HIST_1:BIN2_MIN	Boundary between bin 1 and bin 2
AR_NIC_ORB_CFG_NET_RSP_HIST_1:BIN1_MIN	Boundary between bin 0 and bin 1
AR_NIC_ORB_CFG_NET_RSP_HIST_2	Set to zero to configure default values
AR_NIC_ORB_CFG_NET_RSP_HIST_2:BIN7_MIN	Boundary between bin 6 and bin 7

Name	Description
AR_NIC_ORB_CFG_NET_RSP_HIST_2:BIN6_MIN	Boundary between bin 5 and bin 6
AR_NIC_ORB_CFG_NET_RSP_HIST_2:BIN5_MIN	Boundary between bin 4 and bin 5
AR_NIC_ORB_CFG_NET_RSP_HIST_2:BIN4_MIN	Boundary between bin 3 and bin 4
AR_NIC_ORB_CFG_PRF_TRK_COMP_0	Set to zero to configure default values
AR_NIC_ORB_CFG_PRF_TRK_COMP_0:ADDR_63_36	Address[63:36]. Corresponds to bits 63:36 of an SSID-to-ORB Request Header Flit
AR_NIC_ORB_CFG_PRF_TRK_COMP_0:P0	Parity Bit 0. Corresponds to bit 35 of an SSID-to-ORB Request Header Flit
AR_NIC_ORB_CFG_PRF_TRK_COMP_0:NTT	Node Translation Table Enable. Corresponds to bit 34 of an SSID-to-ORB Request Header Flit
AR_NIC_ORB_CFG_PRF_TRK_COMP_0:RESERVED_33_28	Reserved[33:28]. Corresponds to bits 33:28 of an SSID-to-ORB Request Header Flit
AR_NIC_ORB_CFG_PRF_TRK_COMP_0:PKT_SRC	Packet Source. Corresponds to bits 27:26 of an SSID-to-ORB Request Header Flit
AR_NIC_ORB_CFG_PRF_TRK_COMP_0:PTAG	Protection Tag. Corresponds to bits 25:18 of an SSID-to-ORB Request Header Flit
AR_NIC_ORB_CFG_PRF_TRK_COMP_0:DSTID	Destination Core Identifier. Corresponds to bits 17:16 of an SSID-to-ORB Request Header Flit
AR_NIC_ORB_CFG_PRF_TRK_COMP_0:DST	Destination Endpoint. Corresponds to bits 15:0 of an SSID-to-ORB Request Header Flit
AR_NIC_ORB_CFG_PRF_TRK_COMP_1	Set to zero to configure default values
AR_NIC_ORB_CFG_PRF_TRK_COMP_1:RESERVED_127_125	Reserved[127:125]. Corresponds to bits 127:125 of an SSID-to-ORB Request Header Flit
AR_NIC_ORB_CFG_PRF_TRK_COMP_1:SSID	Synchronization Sequence Identifier. Corresponds to bits 124:116 of an SSID-to-ORB Request Header Flit
AR_NIC_ORB_CFG_PRF_TRK_COMP_1:ADDR_115_112	Address[115:112]. Corresponds to bits 115:112 of an SSID-to-ORB Request Header Flit
AR_NIC_ORB_CFG_PRF_TRK_COMP_1:CNT_BM	Count/Byte-mask. Corresponds to bits 111:108 of an SSID-to-ORB Request Header Flit
AR_NIC_ORB_CFG_PRF_TRK_COMP_1:P2	Parity Bit 2. Corresponds to bit 107 of an SSID-to-ORB Request Header Flit
AR_NIC_ORB_CFG_PRF_TRK_COMP_1:RC0	Routing Control Bit 0. Corresponds to bit 106 of an SSID-to-ORB Request Header Flit
AR_NIC_ORB_CFG_PRF_TRK_COMP_1:RESERVED_105_94	Reserved[105:94]. Corresponds to bits 105:94 of an SSID-to-ORB Request Header Flit

Name	Description
AR_NIC_ORB_CFG_PRF_TRK_COMP_1:MDH	Memory Domain Handle. Corresponds to bits 93:82 of an SSID-to-ORB Request Header Flit
AR_NIC_ORB_CFG_PRF_TRK_COMP_1:RC2	Routing Control Bit 2. Corresponds to bit 81 of an SSID-to-ORB Request Header Flit
AR_NIC_ORB_CFG_PRF_TRK_COMP_1:REQCMD	Request Command. Corresponds to bits 80:72 of an SSID-to-ORB Request Header Flit
AR_NIC_ORB_CFG_PRF_TRK_COMP_1:P1	Parity Bit 1. Corresponds to bit 71 of an SSID-to-ORB Request Header Flit
AR_NIC_ORB_CFG_PRF_TRK_COMP_1:UV	Upper Valid. Corresponds to bit 70 of an SSID-to-ORB Request Header Flit
AR_NIC_ORB_CFG_PRF_TRK_COMP_1:ADDR_69_64	Address[69:64]. Corresponds to bits 69:64 of an SSID-to-ORB Request Header Flit
AR_NIC_ORB_CFG_PRF_TRK_COMP_2	Set to zero to configure default values
AR_NIC_ORB_CFG_PRF_TRK_COMP_2:EN	Enable performance tracking comparisons for SSID-to-ORB Local Address Flit or Request Header Flit
AR_NIC_ORB_CFG_PRF_TRK_COMP_2:P3	Parity Bit 3. Corresponds to bit 143 of an SSID-to-ORB Request Header Flit
AR_NIC_ORB_CFG_PRF_TRK_COMP_2:RC1	Routing Control Bit 1. Corresponds to bit 142 of an SSID-to-ORB Request Header Flit
AR_NIC_ORB_CFG_PRF_TRK_COMP_2:LSTATUS	Local Status. Corresponds to bits 141:136 of an SSID-to-ORB Request Header Flit
AR_NIC_ORB_CFG_PRF_TRK_COMP_2:RESERVED_135_128	Reserved[135:128]. Corresponds to bits 135:128 of an SSID-to-ORB Request Header Flit
AR_NIC_ORB_CFG_PRF_TRK_MASK_0	Set to zero to configure default values
AR_NIC_ORB_CFG_PRF_TRK_MASK_0:ADDR_63_36_MSK	Address[63:36] Mask. Corresponds to bits 63:36 of an SSID-to-ORB Request Header Flit
AR_NIC_ORB_CFG_PRF_TRK_MASK_0:P0_MSK	Parity Bit 0 Mask. Corresponds to bit 35 of an SSID-to-ORB Request Header Flit
AR_NIC_ORB_CFG_PRF_TRK_MASK_0:NTT_MSK	Node Translation Table Enable Mask. Corresponds to bit 34 of an SSID-to-ORB Request Header Flit
AR_NIC_ORB_CFG_PRF_TRK_MASK_0:RESERVED_33_28	Reserved[33:28] Mask. Corresponds to bits 33:28 of an SSID-to-ORB Request Header Flit
AR_NIC_ORB_CFG_PRF_TRK_MASK_0:PKT_SRC_MSK	Packet Source Mask. Corresponds to bits 27:26 of an SSID-to-ORB Request Header Flit

Name	Description
AR_NIC_ORB_CFG_PRF_TRK_MASK_0:PTAG_MSK	Protection Tag Mask. Corresponds to bits 25:18 of an SSID-to-ORB Request Header Flit
AR_NIC_ORB_CFG_PRF_TRK_MASK_0:DSTID_MSK	Destination Core Identifier Mask. Corresponds to bits 17:16 of an SSID-to-ORB Request Header Flit
AR_NIC_ORB_CFG_PRF_TRK_MASK_0:DST	Destination Endpoint Mask. Corresponds to bits 15:0 of an SSID-to-ORB Request Header Flit
AR_NIC_ORB_CFG_PRF_TRK_MASK_1	Set to zero to configure default values
AR_NIC_ORB_CFG_PRF_TRK_MASK_1:RESERVED_127_125	Reserved[127:125] Mask. Corresponds to bits 127:125 of an SSID-to-ORB Request Header Flit
AR_NIC_ORB_CFG_PRF_TRK_MASK_1:SSID_MSK	Synchronization Sequence Identifier Mask. Corresponds to bits 124:116 of an SSID-to-ORB Request Header Flit
AR_NIC_ORB_CFG_PRF_TRK_MASK_1:ADDR_115_112_MSK	Address[115:112] Mask. Corresponds to bits 115:112 of an SSID-to-ORB Request Header Flit
AR_NIC_ORB_CFG_PRF_TRK_MASK_1:CNT_BM_MSK	Count/Byte-mask Mask. Corresponds to bits 111:108 of an SSID-to-ORB Request Header Flit
AR_NIC_ORB_CFG_PRF_TRK_MASK_1:P2_MSK	Parity Bit 2 Mask. Corresponds to bit 107 of an SSID-to-ORB Request Header Flit
AR_NIC_ORB_CFG_PRF_TRK_MASK_1:RC0_MSK	Routing Control Bit 0 Mask. Corresponds to bit 106 of an SSID-to-ORB Request Header Flit
AR_NIC_ORB_CFG_PRF_TRK_MASK_1:RESERVED_105_94	Reserved[105:94] Mask. Corresponds to bits 105:94 of an SSID-to-ORB Request Header Flit
AR_NIC_ORB_CFG_PRF_TRK_MASK_1:MDH_MSK	Memory Domain Handle Mask. Corresponds to bits 93:82 of an SSID-to-ORB Request Header Flit
AR_NIC_ORB_CFG_PRF_TRK_MASK_1:RC2_MSK	Routing Control Bit 2 Mask. Corresponds to bit 81 of an SSID-to-ORB Request Header Flit
AR_NIC_ORB_CFG_PRF_TRK_MASK_1:REQCMD_MSK	Request Command Mask. Corresponds to bits 80:72 of an SSID-to-ORB Request Header Flit
AR_NIC_ORB_CFG_PRF_TRK_MASK_1:P1_MSK	Parity Bit 1 Mask. Corresponds to bit 71 of an SSID-to-ORB Request Header Flit
AR_NIC_ORB_CFG_PRF_TRK_MASK_1:UV_MSK	Upper Valid Mask. Corresponds to bit 70 of an SSID-to-ORB Request Header Flit

Name	Description
AR_NIC_ORB_CFG_PRF_TRK_MASK_1:ADDR_69_64_MSK	Address[69:64] Mask. Corresponds to bits 69:64 of an SSID-to-ORB Request Header Flit
AR_NIC_ORB_CFG_PRF_TRK_MASK_2	Set to zero to configure default values
AR_NIC_ORB_CFG_PRF_TRK_MASK_2:P3_MSK	Parity Bit 3 Mask. Corresponds to bit 143 of an SSID-to-ORB Request Header Flit
AR_NIC_ORB_CFG_PRF_TRK_MASK_2:RC1_MSK	Routing Control Bit 1 Mask. Corresponds to bit 142 HRO CWA of an SSID-to-ORB Request Header Flit
AR_NIC_ORB_CFG_PRF_TRK_MASK_2:LSTATUS_MSK	Local Status Mask. Corresponds to bits 141:136 of HRO CWA an SSID-to-ORB Request Header Flit
AR_NIC_ORB_CFG_PRF_TRK_MASK_2:RESERVED_135_128	Reserved[135:128] Mask. Corresponds to bits HRO CWA 135:128 of an SSID-to-ORB Request Header Flit
AR_NIC_ORB_CFG_PRF_TRK_COMP_3	Set to zero to configure default values
AR_NIC_ORB_CFG_PRF_TRK_COMP_3:LADDR_63_36	Local Address[63:36]. Corresponds to bits 63:36 of an SSID-to-ORB Local Address Flit
AR_NIC_ORB_CFG_PRF_TRK_COMP_3:RESERVED_35_26	Reserved[35:26]. Corresponds to bits 35:26 of an HRO CWA SSID-to-ORB Local Address Flit
AR_NIC_ORB_CFG_PRF_TRK_COMP_3:LPTAG	Local Protection Tag. Corresponds to bits 25:18 of an HRO CWA SSID-to-ORB Local Address Flit
AR_NIC_ORB_CFG_PRF_TRK_COMP_3:RESERVED_17_0	Reserved[17:0]. Corresponds to bits 17:0 of an HRO CWA SSID-to-ORB Request Header Flit
AR_NIC_ORB_CFG_PRF_TRK_COMP_4	Set to zero to configure default values
AR_NIC_ORB_CFG_PRF_TRK_COMP_4:RESERVED_127_112	Reserved[127:112]. Corresponds to bits 127:112 of an SSID-to-ORB Local Address Flit
AR_NIC_ORB_CFG_PRF_TRK_COMP_4:LCNT	Local 32-bit Word Count “minus one”. Corresponds to HRO CWAbits 111:108 of an SSID-to-ORB Local Address Flit
AR_NIC_ORB_CFG_PRF_TRK_COMP_4:RESERVED_107	Reserved[107]. Corresponds to bit 107 of an HRO CWA SSID-to-ORB Local Address Flit
AR_NIC_ORB_CFG_PRF_TRK_COMP_4:FR	Flagged Response. Corresponds to bit 106 of an HRO CWA SSID-to-ORB Local Address Flit

Name	Description
AR_NIC_ORB_CFG_PRF_TRK_COMP_4:LADDR_105_94	Local Address[105:94]. Corresponds to bits 105:94 of an HRO CWASSID-to-ORB Local Address Flit
AR_NIC_ORB_CFG_PRF_TRK_COMP_4:LMDH	Local Memory Domain Handle. Corresponds to bits HRO CWA 93:82 of an SSID-to-ORB Local Address Flit
AR_NIC_ORB_CFG_PRF_TRK_COMP_4:RESERVED_81_72	Reserved[81:72]. Corresponds to bits 81:72 of an SSID-to-ORB Local Address Flit
AR_NIC_ORB_CFG_PRF_TRK_COMP_4:ECC0	Error Correction Code 0. Corresponds to bits 71:64 of an SSID-to-ORB Local Address Flit
AR_NIC_ORB_CFG_PRF_TRK_COMP_5	Set to zero to configure default values
AR_NIC_ORB_CFG_PRF_TRK_COMP_5:EN	Enable performance tracking comparisons for SSID-to-ORB Local Address Flit or Request Header Flit
AR_NIC_ORB_CFG_PRF_TRK_COMP_5:ECC1	Error Correction Code 1. Corresponds to bits 143:136 of an SSID-to-ORB Local Address Flit
AR_NIC_ORB_CFG_PRF_TRK_COMP_5:LWC	Write Combine. Corresponds to bit 135 of and HRO CWA SSID-to-ORB Local Address Flit
AR_NIC_ORB_CFG_PRF_TRK_COMP_5:RESERVED_134_128	Reserved[134:128]. Corresponds to bits 134:128 of an HRO CWA SSID-to-ORB Local Address Flit
AR_NIC_ORB_CFG_PRF_TRK_MASK_3	Set to zero to configure default values
AR_NIC_ORB_CFG_PRF_TRK_MASK_3:LADDR_63_36_MSK	Local Address[63:36] Mask. Corresponds to bits 63:36 of an SSID-to-ORB Local Address Flit
AR_NIC_ORB_CFG_PRF_TRK_MASK_3:RESERVED_35_26	Reserved[35:26] Mask. Corresponds to bits 35:26 of an HRO F CWASSID-to-ORB Local Address Flit
AR_NIC_ORB_CFG_PRF_TRK_MASK_3:LPTAG_MSK	Local Protection Tag Mask. Corresponds to bits 25:18 of HRO CWA an SSID-to-ORB Local Address Flit
AR_NIC_ORB_CFG_PRF_TRK_MASK_3:RESERVED_17_0	Reserved[17:0] Mask. Corresponds to bits 17:0 of an HRO FFF CWASSID-to-ORB Request Header Flit
AR_NIC_ORB_CFG_PRF_TRK_MASK_4	Set to zero to configure default values
AR_NIC_ORB_CFG_PRF_TRK_MASK_4:RESERVED_127_112	Reserved[127:112] Mask. Corresponds to bits 127:112 of an SSID-to-ORB Local Address Flit

Name	Description
AR_NIC_ORB_CFG_PRF_TRK_MASK_4:LCNT_MSK	Local 32-bit Word Count “minus one” Mask. Corresponds to bits 111:108 of an SSID-to-ORB Local Address Flit
AR_NIC_ORB_CFG_PRF_TRK_MASK_4:RESERVED_107	Reserved[107] Mask. Corresponds to bit 107 of an SSID-to-ORB Local Address Flit
AR_NIC_ORB_CFG_PRF_TRK_MASK_4:FR_MSK	Flagged Response Mask. Corresponds to bit 106 of an SSID-to-ORB Local Address Flit
AR_NIC_ORB_CFG_PRF_TRK_MASK_4:LADDR_105_94_MSK	Local Address[105:94] Mask. Corresponds to bits 105:94 of an SSID-to-ORB Local Address Flit
AR_NIC_ORB_CFG_PRF_TRK_MASK_4:LMDH_MSK	Local Memory Domain Handle Mask. Corresponds to bits 93:82 of an SSID-to-ORB Local Address Flit
AR_NIC_ORB_CFG_PRF_TRK_MASK_4:RESERVED_81_72	Reserved[81:72] Mask. Corresponds to bits 81:72 of an SSID-to-ORB Local Address Flit
AR_NIC_ORB_CFG_PRF_TRK_MASK_4:ECC0_MSK	Error Correction Code 0 Mask. Corresponds to bits 71:64 of an SSID-to-ORB Local Address Flit
AR_NIC_ORB_CFG_PRF_TRK_MASK_5	Set to zero to configure default values
AR_NIC_ORB_CFG_PRF_TRK_MASK_5:ECC1_MSK	Error Correction Code 1 Mask. Corresponds to bits 143:136 of an SSID-to-ORB Local Address Flit
AR_NIC_ORB_CFG_PRF_TRK_MASK_5:LWC_MSK	Write Combine. Corresponds to bit 135 of an SSID-to-ORB Local Address Flit
AR_NIC_ORB_CFG_PRF_TRK_MASK_5:RESERVED_134_128	Reserved[134:128] Mask. Corresponds to bits 134:128 of an SSID-to-ORB Local Address Flit
AR_NIC_ORB_PRF_NET_RSP_TRACK_1:MAX_RSP_TIME	Tracks the maximum response time for an outstanding request to the Aries Network
AR_NIC_ORB_PRF_NET_RSP_TRACK_1:MIN_RSP_TIME	Tracks the minimum response time for an outstanding request to the Aries Network
AR_NIC_ORB_PRF_NET_RSP_TRACK_2	Tracks the sum of all response times for outstanding requests to the Aries Network
AR_NIC_ORB_PRF_NET_RSP_HIST_BIN01:BIN1_COUNT	Bin 1 count
AR_NIC_ORB_PRF_NET_RSP_HIST_BIN01:BIN0_COUNT	Bin 0 count
AR_NIC_ORB_PRF_NET_RSP_HIST_BIN23:BIN3_COUNT	Bin 3 count
AR_NIC_ORB_PRF_NET_RSP_HIST_BIN23:BIN2_COUNT	Bin 2 count
AR_NIC_ORB_PRF_NET_RSP_HIST_BIN45:BIN5_COUNT	Bin 5 count

Name	Description
AR_NIC_ORB_PRF_NET_RSP_HIST_BIN45:BIN4_COUNT	Bin 4 count
AR_NIC_ORB_PRF_NET_RSP_HIST_BIN67:BIN7_COUNT	Bin 7 count
AR_NIC_ORB_PRF_NET_RSP_HIST_BIN67:BIN6_COUNT	Bin 6 count
AR_NIC_ORB_PRF_REQ_BYTES_SENT	Count of the number of bytes sent to the network
AR_NIC_ORB_PRF_RSP_BYTES_RCVD	Count of the number of bytes received from the network
AR_NIC_ORB_PRF_STALL_DURATION_EN	Set to zero to configure default values
AR_NIC_ORB_PRF_STALL_DURATION_EN:ENABLE_PKTID	Set to enable AR_NIC_ORB_PRF_REQ_PKTID_STALL_DURATION
AR_NIC_ORB_PRF_STALL_DURATION_EN:ENABLE_CRDTS	Set to enable AR_NIC_ORB_PRF_REQ_CRDTS_STALL_DURATION
AR_NIC_ORB_PRF_REQ_PKTID_STALL_DURATION	Duration of stalled request due to lack of packet identifiers
AR_NIC_ORB_PRF_REQ_CRDTS_STALL_DURATION	Duration of stalled request due to lack of ORB-to-NL credits
AR_NIC_NETMON_ORB_EVENT_CNTR_REQ_FLITS	ORB request flits event count
AR_NIC_NETMON_ORB_EVENT_CNTR_REQ_PKTS	ORB request packets event count
AR_NIC_NETMON_ORB_EVENT_CNTR_REQ_STALLED	ORB request stalled event count
AR_NIC_NETMON_ORB_EVENT_CNTR_RSP_NET_TRACK	ORB response packets tracked count
AR_NIC_NETMON_ORB_EVENT_CNTR_RSP_FLITS	ORB response flits event count
AR_NIC_NETMON_ORB_EVENT_CNTR_RSP_PKTS	ORB response packets event count
AR_NIC_NETMON_ORB_EVENT_CNTR_RSP_STALLED	ORB response stalled event count
AR_NIC_NETMON_ORB_EVENT_CNTR_RSP_BLOCKED	ORB response blocked event count
AR_NIC_NETMON_ORB_EVENT_CNTR_RSP_BLOCKED_PKT_GEN	ORB response blocked due to GET packet generation event count
AR_NIC_NETMON_ORB_EVENT_CNTR_ORD_RAM_READ	ORB response packets read from ORD event count
AR_NIC_NETMON_ORB_EVENT_CNTR_NTT_SBE	ORB NTT SBE event count
AR_NIC_NETMON_ORB_EVENT_CNTR_NTT_MBE	ORB NTT MBE event count
AR_NIC_NETMON_ORB_EVENT_CNTR_PTT_SBE	ORB PTT SBE event count
AR_NIC_NETMON_ORB_EVENT_CNTR_PTT_MBE	ORB PTT MBE event count
AR_NIC_NETMON_ORB_EVENT_CNTR_REQ_FIFO_SBE	ORB request FIFO SBE event count
AR_NIC_NETMON_ORB_EVENT_CNTR_REQ_FIFO_MBE	ORB request FIFO MBE event count
AR_NIC_NETMON_ORB_EVENT_CNTR_REQ_FIFO_PE	ORB request FIFO PE event count



Name	Description
AR_NIC_NETMON_ORB_EVENT_CNTR_RSP_FIFO_SBE	ORB response FIFO SBE event count
AR_NIC_NETMON_ORB_EVENT_CNTR_RSP_FIFO_MBE	ORB response FIFO MBE count
AR_NIC_NETMON_ORB_EVENT_CNTR_RSP_FIFO_PE	ORB response FIFO PE event count
AR_NIC_NETMON_ORB_EVENT_CNTR_RSP_FIFO_SPKT	Response FIFO super packet count
AR_NIC_NETMON_ORB_EVENT_CNTR_ORD_SBE	ORB outstanding request data RAM SBE event count
AR_NIC_NETMON_ORB_EVENT_CNTR_ORD_MBE	ORB outstanding request data RAM MBE event count
AR_NIC_NETMON_ORB_EVENT_CNTR_SCRUB_CNT	ORB Scrub entry count
AR_NIC_NETMON_ORB_EVENT_CNTR_NO_ENTRY	No entry in ORD event count
AR_NIC_NETMON_ORB_EVENT_CNTR_CMD_MISMATCH	Command mismatch event count
AR_NIC_NETMON_ORB_EVENT_CNTR_ILLEGAL_PKTID	Illegal packet ID in response count
AR_NIC_NETMON_ORB_EVENT_CNTR_ILLEGAL_LSTATUS	Illegal local status in response count
AR_NIC_NETMON_RAT_EVENT_CNTR_ORB_PKTS	ORB packet count
AR_NIC_NETMON_RAT_EVENT_CNTR_ORB_FLITS	ORB flit count

**Table 7. Remote Address Translation (RAT) Performance Counters**

Name	Description
AR_NIC_RAT_PRF_REQ_VAL0	Set to zero to configure default values
AR_NIC_RAT_PRF_REQ_VAL0:FLIT_63_0	Bits [63:0] of the flit
AR_NIC_RAT_PRF_REQ_VAL1	Set to zero to configure default values
AR_NIC_RAT_PRF_REQ_VAL1:FLIT_127_64	Bits [127:64] of the flit
AR_NIC_RAT_PRF_REQ_VAL2	Set to zero to configure default values
AR_NIC_RAT_PRF_REQ_VAL2:IOMMU_EN	Enable this performance counter to look at incoming IOMMU requests
AR_NIC_RAT_PRF_REQ_VAL2:NL_EN	Enable this performance counter to look at incoming NL requests
AR_NIC_RAT_PRF_REQ_VAL2:HDR	1 = Flit type is header
AR_NIC_RAT_PRF_REQ_VAL2:TAIL	1 = Flit is a tail flit
AR_NIC_RAT_PRF_REQ_VAL2:FLIT_143_128	Bits [143:128] of the flit
AR_NIC_RAT_PRF_REQ_MASK0	Set to zero to configure default values
AR_NIC_RAT_PRF_REQ_MASK0:FLIT_63_0	Bits [63:0] of the flit
AR_NIC_RAT_PRF_REQ_MASK1	Set to zero to configure default values
AR_NIC_RAT_PRF_REQ_MASK1:FLIT_127_64	Bits [127:64] of the flit

Name	Description
AR_NIC_RAT_PRF_REQ_MASK2	Set to zero to configure default values
AR_NIC_RAT_PRF_REQ_MASK2:HDR	Flit type (header/payload) To ensure atomicity, disable the counter (IOMMU_EN and NL_EN) before modifying the configuration
AR_NIC_RAT_PRF_REQ_MASK2:TAIL	Flit is a tail flit
AR_NIC_RAT_PRF_REQ_MASK2:FLIT_143_128	Bits [143:128] of the flit
AR_NIC_RAT_PRF_RSP_VAL0	Set to zero to configure default values
AR_NIC_RAT_PRF_RSP_VAL0:FLIT_63_0	Bits [63:0] of the flit
AR_NIC_RAT_PRF_RSP_VAL1	Set to zero to configure default values
AR_NIC_RAT_PRF_RSP_VAL1:FLIT_127_64	Bits [127:64] of the flit
AR_NIC_RAT_PRF_RSP_VAL2	Set to zero to configure default values
AR_NIC_RAT_PRF_RSP_VAL2:EN	Enable this performance counter
AR_NIC_RAT_PRF_RSP_VAL2:HDR	1 = Flit type is header
AR_NIC_RAT_PRF_RSP_VAL2:TAIL	1 = Flit is a tail flit
AR_NIC_RAT_PRF_RSP_VAL2:FLIT_143_128	Bits [143:128] of the flit
AR_NIC_RAT_PRF_RSP_MASK0	Set to zero to configure default values
AR_NIC_RAT_PRF_RSP_MASK0:FLIT_63_0	Bits [63:0] of the flit
AR_NIC_RAT_PRF_RSP_MASK1	Set to zero to configure default values
AR_NIC_RAT_PRF_RSP_MASK1:FLIT_127_64	Bits [127:64] of the flit
AR_NIC_RAT_PRF_RSP_MASK2	Set to zero to configure default values
AR_NIC_RAT_PRF_RSP_MASK2:HDR	Flit type (header/payload) To ensure atomicity, disable the counter (EN) before modifying the configuration
AR_NIC_RAT_PRF_RSP_MASK2:TAIL	Flit is a tail flit
AR_NIC_RAT_PRF_RSP_MASK2:FLIT_143_128	Bits [143:128] of the flit
AR_NIC_RAT_PRF_EN	Set to zero to configure default values
AR_NIC_RAT_PRF_EN:PRF_RSP_DURATION_EN	Enable register
	AR_NIC_RAT_PRF_RSP_STALL_DURATION
AR_NIC_RAT_PRF_EN:PRF_REQ_DURATION_EN	Enable register
	AR_NIC_RAT_PRF_REQ_STALL_DURATION
AR_NIC_RAT_PRF_REQ_STALL_DURATION	Request stall duration
AR_NIC_RAT_PRF_RSP_STALL_DURATION	Response stall duration
AR_NIC_RAT_PRF_REQ_BYTES_RCVD	The number of payload bytes received on the request channel
AR_NIC_NETMON_NAT_EVENT_CNTR_RAT_TRANSLATIONS	RAT translation count

<b>Name</b>	<b>Description</b>
AR_NIC_NETMON_RAT_EVENT_CNTR_RAT_STALLED	RAT stalled count
AR_NIC_NETMON_RAT_EVENT_CNTR_RAT_BLOCKED	RAT blocked count
AR_NIC_NETMON_RAT_EVENT_CNTR_NL_PKTS	NL packet count
AR_NIC_NETMON_RAT_EVENT_CNTR_NL_FLITS	NL flit count
AR_NIC_NETMON_RAT_EVENT_CNTR_NL_BLOCKED	NL blocked count
AR_NIC_NETMON_RAT_EVENT_CNTR_IOMMU_PKTS	IOMMU packet count
AR_NIC_NETMON_RAT_EVENT_CNTR_IOMMU_FLITS	IOMMU flit count
AR_NIC_NETMON_RAT_EVENT_CNTR_IOMMU_BLOCKED	IOMMU blocked count
AR_NIC_NETMON_RAT_EVENT_CNTR_REQ_STALLED	Request channel (NL or IOMMU) stalled count
AR_NIC_NETMON_RAT_EVENT_CNTR_ORB_PKTS	ORB packet count
AR_NIC_NETMON_RAT_EVENT_CNTR_ORB_FLITS	ORB flit count
AR_NIC_NETMON_RAT_EVENT_CNTR_RSP_STALLED	ResponseChannel (ORB) stalled count
AR_NIC_NETMON_RAT_EVENT_CNTR_REQ_PARITY_INPUT	Parity error in request packet input (excludes IOMMU)
AR_NIC_NETMON_RAT_EVENT_CNTR_RSP_PARITY_INPUT	Parity error in response packet input
AR_NIC_NETMON_RAT_EVENT_CNTR_REQ_PARITY_OUTPUT	Parity error in request packet output (includes IOMMU)
AR_NIC_NETMON_RAT_EVENT_CNTR_RSP_PARITY_OUTPUT	Parity error in response packet output
AR_NIC_NETMON_RAT_EVENT_CNTR_REQ_XLAT_ERR	Translation error in request (detected in output processing)
AR_NIC_NETMON_RAT_EVENT_CNTR_REQ_PAYLD_ERR	Payload error in request (detected in output processing)
AR_NIC_NETMON_RAT_EVENT_CNTR_RSP_XLAT_ERR	Translation error in response (detected in output processing)
AR_NIC_NETMON_RAT_EVENT_CNTR_RSP_PAYLD_ERR	Payload error in response (detected in output processing)
AR_NIC_NETMON_RAT_EVENT_CNTR_IOMMU_PARITY_INPUT	Parity error in IOMMU request packet input
AR_NIC_NETMON_RAT_EVENT_CNTR_IOMMU_DROP_INPUT	IOMMU request was dropped during input processing (e.g. invalid command)
AR_NIC_NETMON_RAT_EVENT_CNTR_IOMMU_PAYLD_ERR	Payload error in IOMMU request (detected in output processing)
AR_NIC_NETMON_RAT_EVENT_CNTR_REQ_MALFORMED	Malformed request packet (detected in input processing)
AR_NIC_NETMON_RAT_EVENT_CNTR_REQ_INV_CMD	Invalid command in request (detected in input processing)

Name	Description
AR_NIC_NETMON_RAT_EVENT_CNTR_RSP_MALFORMED	Malformed response packet (detected in input processing)
AR_NIC_NETMON_RAT_EVENT_CNTR_RSP_INV_CMD	Invalid command in response (detected in input processing)
AR_NIC_NETMON_RAT_EVENT_CNTR_REQ_FLIT	Configurable performance counter, request channel (includes IOMMU)
AR_NIC_NETMON_RAT_EVENT_CNTR_RSP_FLIT	Configurable performance counter, response channel
AR_NIC_REQMON_WC_EVENT_CNTR_RAT_GETRESP	GetResp or FetchingAMOResp packets received from RAT

**Table 8. Receive Message Table (RMT) Performance Counters**

Name	Description
AR_NIC_RMT_PRF_PUT_BYTES_RX	Count of PUT bytes received
AR_NIC_RMT_PRF_SEND_BYTES_RX	Count of BTE_Send bytes received
AR_NIC_RSPMON_CQ_EVENT_CNTR_RMT_STALLED	Clocks when RMT event is stalled due to not enough CAM entries
AR_NIC_RSPMON_CQ_EVENT_CNTR_RMT_BLOCKED	Clocks when RMTevent is blocked from leaving the RMT input buffer
AR_NIC_RSPMON_CQ_EVENT_CNTR_RMT_DROPPED	Dropped RMT completion queue events
AR_NIC_RSPMON_CQ_EVENT_CNTR_RMT_INPUT_RMT_DATA_PARITY_ERR	RMT input RMT data buffer parity error count
AR_NIC_RSPMON_CQ_EVENT_CNTR_RMT_INPUT_EVENT_DATA_PARITY_ERR	RMT input event data buffer parity error count
AR_NIC_RSPMON_RMT_EVENT_CNTR_INQ_PARITY	Input queue header flit parity error count
AR_NIC_RSPMON_RMT_EVENT_CNTR_PAYLOAD_SBE	Payload flit SBE count
AR_NIC_RSPMON_RMT_EVENT_CNTR_PAYLOAD_MBE	Payload flit MBE count
AR_NIC_RSPMON_RMT_EVENT_CNTR_SEQ_TBL_SBE	Sequence table SBE count
AR_NIC_RSPMON_RMT_EVENT_CNTR_SEQ_TBL_MBE	Sequence table MBE count
AR_NIC_RSPMON_RMT_EVENT_CNTR_DESC_TBL_SBE	Rx descriptor table SBE count
AR_NIC_RSPMON_RMT_EVENT_CNTR_DESC_TBL_MBE	Rx descriptor table MBE count
AR_NIC_RSPMON_RMT_EVENT_CNTR_UNDELIVERABLE_CQE	Undeliverable CQE (due to errored or malformed CQWrite or MsgRcvComplete) count
AR_NIC_RSPMON_RMT_EVENT_CNTR_PUT_CAM_FILLS	PUT/MsgSendComplete CAM fill count

Name	Description
AR_NIC_RSPMON_RMT_EVENT_CNTR_PUT_CAM_MISSES	PUT/MsgSendComplete CAM miss count
AR_NIC_RSPMON_RMT_EVENT_CNTR_PUT_CAM_EVICTS	DSMN Sequence CAM evict count
AR_NIC_RSPMON_RMT_EVENT_CNTR_PUT_CAM_HITS	PUT/MsgSendComplete CAM hit count
AR_NIC_RSPMON_RMT_EVENT_CNTR_PUT_MBE_EVICTS	DSMN Sequence CAM MBE evict count
AR_NIC_RSPMON_RMT_EVENT_CNTR_PUT_TIMEOUTS	DSMN Sequence CAM MBE timeout count
AR_NIC_RSPMON_RMT_EVENT_CNTR_RCV_COMPLETES	MsgRcvComplete count
AR_NIC_RSPMON_RMT_EVENT_CNTR_SEND_CAM_FILLS	BTE_Send/BTE_SendComplete CAM fill count
AR_NIC_RSPMON_RMT_EVENT_CNTR_SEND_CAM_MISSES	BTE_Send/BTE_SendComplete CAM miss count
AR_NIC_RSPMON_RMT_EVENT_CNTR_SEND_CAM_EVICTS	BTE_Send Sequence CAM evict count
AR_NIC_RSPMON_RMT_EVENT_CNTR_SEND_CAM_HITS	BTE_Send/BTE_SendComplete CAM hit count
AR_NIC_RSPMON_RMT_EVENT_CNTR_SEND_MBE_EVICTS	BTE_Send Sequence CAM MBE evict count
AR_NIC_RSPMON_RMT_EVENT_CNTR_SEND_TIMEOUTS	BTE_Send Sequence CAM MBE timeout count
AR_NIC_RSPMON_RMT_EVENT_CNTR_ABORTS	Abort count
AR_NIC_RSPMON_NPT_EVENT_CNTR_RMT_PKTS	RMT input packet count
AR_NIC_RSPMON_NPT_EVENT_CNTR_RMT_FLITS	RMT input flit count
AR_NIC_RSPMON_NPT_EVENT_CNTR_RMT_BLOCKED	RMT input blocked count

**Table 9. Synchronization Sequence Identification (SSID) Performance Counters**

Name	Description
AR_NIC_SSID_CFG_EVENT_CNTR_FILTER	Set to zero to configure default values
AR_NIC_SSID_CFG_EVENT_CNTR_FILTER:CHANNEL	The ID of the FMA descriptor, BTE channel, or virtual CE for which counting is enabled
AR_NIC_SSID_CFG_EVENT_CNTR_FILTER:INITIATOR	Selects whether counting is enabled for a FMA descriptor, a BTEchannel, or a virtual CE
AR_NIC_SSID_CFG_EVENT_CNTR_FILTER:ALL_CHAN_EN	When set to 1, counting is enabled for all FMA descriptors, BTEchannels, and virtual CEs
AR_NIC_SSID_PRF_CNTR_SSIDS_IN_USE:MAX_SSIDS_IN_USE	The maximum number of SSIDs that have been in use simultaneously since this register was last initialized
AR_NIC_SSID_PRF_CNTR_SSIDS_IN_USE:CUR_SSIDS_IN_USE	The number of SSIDs currently in use across all Request Channels

Name	Description
AR_NIC_NETMON_SSID_EVENT_CNTR_ALLOCATIONS	Successful SSID allocations across all source blocks (FMA, BTE, CE)
AR_NIC_NETMON_SSID_EVENT_CNTR_NO_SSID	Out of SSIDs events
AR_NIC_NETMON_SSID_EVENT_CNTR_NO_SSID_DURATN	Out of SSIDs duration
AR_NIC_NETMON_SSID_EVENT_CNTR_REQ_STALLED_DURATN	Stalled requests duration
AR_NIC_NETMON_SSID_EVENT_CNTR_DSMN_OPTIMAL	Optimally completed DSMN transactions
AR_NIC_NETMON_SSID_EVENT_CNTR_DSMN_SUBOPTIMAL	Suboptimally completed DSMN transactions
AR_NIC_NETMON_SSID_EVENT_CNTR_BTESEND_SUCCESSFUL	Successfully completed BTE_Send transactions
AR_NIC_NETMON_SSID_EVENT_CNTR_BTESEND_UNSUCCESSFUL	Unsuccessfully completed BTE_Send transactions
AR_NIC_NETMON_SSID_EVENT_CNTR_NO_DEST_NOTIFY	Transactions completed without destination notification
AR_NIC_NETMON_SSID_EVENT_CNTR_LATE_FIRST_REQRSP	Transactions completed with late response arrival
AR_NIC_NETMON_SSID_EVENT_CNTR_INTENTIONAL_TIMEOUT	Transactions completed with an intentional SSID entry timeout
AR_NIC_NETMON_SSID_EVENT_CNTR_LREQ_ERR_HSS	Local request errors of interest to HSS
AR_NIC_NETMON_SSID_EVENT_CNTR_LREQ_ERR_OS	Local request errors of interest to OS
AR_NIC_NETMON_SSID_EVENT_CNTR_RREQ_HSS	Remote request errors of interest to HSS
AR_NIC_NETMON_SSID_EVENT_CNTR_RREQ_OS	Remote request errors of interest to OS
AR_NIC_NETMON_SSID_EVENT_CNTR_RSP_HSS	Response errors of interest to HSS
AR_NIC_NETMON_SSID_EVENT_CNTR_RSP_OS	Response errors of interest to OS
AR_NIC_NETMON_SSID_EVENT_CNTR_STALE_SSID	Stale SSID entries count
AR_NIC_NETMON_SSID_EVENT_CNTR_SEQ_ERR_HSS	Sequence errors of interest to HSS
AR_NIC_NETMON_SSID_EVENT_CNTR_SEQ_ERR_OS	Sequence errors of interest to OS
AR_NIC_NETMON_SSID_EVENT_CNTR_UNEXPTD_RSP	Unexpected response
AR_NIC_NETMON_SSID_EVENT_CNTR_MEM_ERR_CORR	Correctable memory errors
AR_NIC_NETMON_SSID_EVENT_CNTR_MEM_ERR_UNCORR	Uncorrectable memory errors
AR_NIC_NETMON_SSID_EVENT_CNTR_CORRUPT_REQ_HD	Request packets with corrupt head flits
AR_NIC_NETMON_SSID_EVENT_CNTR_CORR_REQ_PAYLD_ERR	Correctable errors in request packet payload flits

Name	Description
AR_NIC_NETMON_SSID_EVENT_CNTR_UNCORR_REQ_PAYLD_ERR	Uncorrectable errors in request packet payload flits
AR_NIC_NETMON_SSID_EVENT_CNTR_REQ_LEN_ERR	Request packets with incorrect length
AR_NIC_NETMON_SSID_EVENT_CNTR_CORRUPT_RSP	Corrupt response head flits
AR_NIC_NETMON_SSID_EVENT_CNTR_BYPASSED_REQ	Good request packets not forwarded toward network
AR_NIC_NETMON_TARB_EVENT_CNTR_SSID_FLITS	SSID flit count
AR_NIC_NETMON_TARB_EVENT_CNTR_SSID_PKTS	SSID packet count
AR_NIC_NETMON_TARB_EVENT_CNTR_SSID_STALLED	SSID stalled count
AR_NIC_RSPMON_CQ_EVENT_CNTR_SSID_STALLED	Clocks when SSID event is stalled due to not enough CAM entries
AR_NIC_RSPMON_CQ_EVENT_CNTR_SSID_BLOCKED	Clocks when SSID event is blocked from leaving the SSID input buffer
AR_NIC_RSPMON_CQ_EVENT_CNTR_SSID_DROPPED	Dropped SSID completion queue events
AR_NIC_RSPMON_CQ_EVENT_CNTR_SSID_INPUT_EVENT_DATA_PARITY_ERR	SSID input event data buffer parity error count

**Table 10. Transmit Arbiter (TARB) Performance Counters**

Name	Description
AR_NIC_NETMON_TARB_EVENT_CNTR_CE_FLITS	CE flit count
AR_NIC_NETMON_TARB_EVENT_CNTR_CE_PKTS	CE packet count
AR_NIC_NETMON_TARB_EVENT_CNTR_CE_BLOCKED	CE blocked count
AR_NIC_NETMON_TARB_EVENT_CNTR_BTE_FLITS	BTE flit count
AR_NIC_NETMON_TARB_EVENT_CNTR_BTE_PKTS	BTE packet count
AR_NIC_NETMON_TARB_EVENT_CNTR_BTE_BLOCKED	BTE blocked count
AR_NIC_NETMON_TARB_EVENT_CNTR_DLA_FLITS	DLA flit count
AR_NIC_NETMON_TARB_EVENT_CNTR_DLA_PKTS	DLA packet count
AR_NIC_NETMON_TARB_EVENT_CNTR_DLA_BLOCKED	DLA blocked count
AR_NIC_NETMON_TARB_EVENT_CNTR_SSID_FLITS	SSID flit count
AR_NIC_NETMON_TARB_EVENT_CNTR_SSID_PKTS	SSID packet count
AR_NIC_NETMON_TARB_EVENT_CNTR_SSID_STALLED	SSID stalled count

Name	Description
AR_NIC_REQMON_BTE_EVENT_CNTR_TARB_FLT_BLK	TARB flits blocked (ready to send, but no flit credits)
AR_NIC_REQMON_BTE_EVENT_CNTR_TARB_PKT_BLK	TARB packets blocked (ready to send, but no packet credits)
AR_NIC_REQMON_FMA_EVENT_CNTR_TARB_STALLED	FMA to DLA stalls
AR_NIC_REQMON_DLA_EVENT_CNTR_TARB_STALLED	DLA to TARB stalls

## 1.3 Network Router Tile Counters

The Aries network consists of 48 router tiles, arranged in 6 rows of 8 columns. Within each tile there are memory-mapped registers (MMRs) associated with the link control block (LCB) and with the rest of the tile. The local block has shared connections to each row of tiles.

By default, when only the name of the MMR is used, an event is counted on all 48 tiles. To address an individual tile, append the row (0–5) and column (0–7) to the name.

All Aries router tile counter names begin with the prefix `AR_RTR_`. The sheer volume of such counters as listed in the `Counters.papi_aries` file can seem overwhelming at first, but there is a lot of duplication, differentiated only by the row number, column number, and often other values such as the flit number. For example, factoring in the row (*r*), column (*c*), and flit (*f*), there more than three hundred `AR_RTR_r_c_INQ_PRF_FLITf_FILTERING_MASK` counters alone.

## 1.4 Configurable Counters

The list in [Aries Configurable Counters on page 77](#) shows the Aries events that are writable by the user. The qualifiers in these events control various aspects of other counters, i.e., filtering, masks, setup, and other configurable parameters. Some of the events could be considered moot as they enable (EN) counting of events that are enabled by default, but these events do allow users to write PAPI code that would disable counters that would otherwise be active.

The configurable counters are either *events* or *qualifiers*. Qualifiers are used to control the configuration aspect of other counters, while events are used primarily to reset the associated qualifiers to their default initialization values, when the event is set to zero.



For example, the user could configure the outstanding request buffer histograms by setting these qualifiers:

```
AR_NIC_ORB_CFG_NET_RSP_HIST_1:BIN1_MIN=1000
AR_NIC_ORB_CFG_NET_RSP_HIST_1:BIN2_MIN=5000
```

Or return them to their default values by setting the associated event:

```
AR_NIC_ORB_CFG_NET_RSP_HIST_1=0
```

**Note:** Once a configurable counter has been changed, it stays changed for the remainder of the user's session and is not reset after the application finishes execution. If the user does not reset the counter by changing the qualifier or via the event name, the values set will remain in effect.

#### Example 4. Configuring Counters with `PAT_RT_NWPC_FILE`

To configure the histogram bins and collect the histogram counts, create file named `histogram`, with these contents:

```
AR_NIC_ORB_CFG_NET_RSP_HIST_1:BIN1_MIN=1000
AR_NIC_ORB_CFG_NET_RSP_HIST_1:BIN2_MIN=2000
AR_NIC_ORB_CFG_NET_RSP_HIST_1:BIN3_MIN=3000
AR_NIC_ORB_CFG_NET_RSP_HIST_2:BIN4_MIN=4000
AR_NIC_ORB_CFG_NET_RSP_HIST_2:BIN5_MIN=5000
AR_NIC_ORB_CFG_NET_RSP_HIST_2:BIN6_MIN=6000
AR_NIC_ORB_CFG_NET_RSP_HIST_2:BIN7_MIN=7000
AR_NIC_ORB_PRF_NET_RSP_HIST_BIN01:BIN0_COUNT
AR_NIC_ORB_PRF_NET_RSP_HIST_BIN01:BIN1_COUNT
AR_NIC_ORB_PRF_NET_RSP_HIST_BIN23:BIN2_COUNT
AR_NIC_ORB_PRF_NET_RSP_HIST_BIN23:BIN3_COUNT
AR_NIC_ORB_PRF_NET_RSP_HIST_BIN45:BIN4_COUNT
AR_NIC_ORB_PRF_NET_RSP_HIST_BIN45:BIN5_COUNT
AR_NIC_ORB_PRF_NET_RSP_HIST_BIN67:BIN6_COUNT
AR_NIC_ORB_PRF_NET_RSP_HIST_BIN67:BIN7_COUNT
```

Before executing your program, use the CrayPat run time environment variable `PAT_RT_NWPC_FILE` to specify the filename, `histogram`. This configures the counters to collect histogram counts for each of the eight bins.

**Example 5. Configuring Counters with PAT\_RT\_NWPC\_FILE\_GROUP**

Alternately, you can achieve the same effect by creating a counter group—for example, `histgroup`—with the following contents:

```
HISTOGRAM =
AR_NIC_ORB_CFG_NET_RSP_HIST_1:BIN1_MIN=1000
AR_NIC_ORB_CFG_NET_RSP_HIST_1:BIN2_MIN=2000
AR_NIC_ORB_CFG_NET_RSP_HIST_1:BIN3_MIN=3000
AR_NIC_ORB_CFG_NET_RSP_HIST_2:BIN4_MIN=4000
AR_NIC_ORB_CFG_NET_RSP_HIST_2:BIN5_MIN=5000
AR_NIC_ORB_CFG_NET_RSP_HIST_2:BIN6_MIN=6000
AR_NIC_ORB_CFG_NET_RSP_HIST_2:BIN7_MIN=7000
AR_NIC_ORB_PRF_NET_RSP_HIST_BIN01:BIN0_COUNT
AR_NIC_ORB_PRF_NET_RSP_HIST_BIN01:BIN1_COUNT
AR_NIC_ORB_PRF_NET_RSP_HIST_BIN23:BIN2_COUNT
AR_NIC_ORB_PRF_NET_RSP_HIST_BIN23:BIN3_COUNT
AR_NIC_ORB_PRF_NET_RSP_HIST_BIN45:BIN4_COUNT
AR_NIC_ORB_PRF_NET_RSP_HIST_BIN45:BIN5_COUNT
AR_NIC_ORB_PRF_NET_RSP_HIST_BIN67:BIN6_COUNT
AR_NIC_ORB_PRF_NET_RSP_HIST_BIN67:BIN7_COUNT
```

Before executing your program, use the CrayPat run time environment variable `PAT_RT_NWPC_FILE_GROUP` to specify the filename, `histgroup`. When executed, the instrumented program will configure and collect the specified counters.

**Note:** Use care when collecting network performance counters. The overhead to access network performance counters can be as much as ten times greater than the overhead of accessing CPU counters. A program with many entry points instrumented with the `-u` option, or with entry points in long-running loops, can take a significantly longer time to run if network counters are being collected.

## 1.5 Run Time Environment Variables

The following CrayPat run time environment variables control network performance counter data collection. The variables and their behavior are subject to change with each new software release. For the latest information regarding network performance counter-related run time environment variables, see the `intro_craypat(1)` man page.

**Note:** When working with CrayPat run time environment variables, please note that `HWPC` refers to CPU performance counters, `NWPC` refers to network performance counters, and `ACCPC` refers to accelerator (GPU) performance counters.

`PAT_RT_NWPC`

Specifies a comma-separated list of the Aries performance events to be monitored for a program counter tracing experiment.

For a complete list of Aries network performance counters, see the files `$CRAYPAT_ROOT/share/Counters.papi_aries` or `$CRAYPAT_ROOT/share/Counters.papi_aries.xml`, or view the **counters**→**aries** topics in `pat_help`.

Default: unset; no network performance counter events are monitored during tracing experiments

`PAT_RT_NWPC_CONTROL`

The valid arguments are `global` or `local`.

This environment variable is not evaluated by the instrumented program at run time, but instead is used by the `aprun` command in determining how best and when to schedule the instrumented program so that it has exclusive use of the requested network performance counters.

Use `global` only when you have exclusive use of the system. If `global` is specified, the instrumented program has exclusive access to all network performance counters on the system. If there are other users on the system, specifying `global` can result in very long wait times before program execution begins, if it begins at all.

Default: `local`

**PAT\_RT\_NWPC\_FILE**

Specifies a comma-separated list of file names where each file contains individual Aries event names. See `PAT_RT_NWPC` for a description of an event. A line in the file that begins with a # character is interpreted as a comment and ignored.

Default: unset

**PAT\_RT\_NWPC\_FILE\_GROUP**

Specifies a comma-separated list of file names where each file contains specifications of Aries performance counter groups. This allows a user to extend the scope of the Aries performance counters. The format of the file is:

```
group-name=event1,event2...
```

The definition of the group is terminated with a <newline> character. There may be multiple unique group names defined in a single file. Lines that do not match this syntax are ignored.

The file containing the group definitions for the default groups is in `$CRAYPAT_ROOT/share/`.

Default: unset

## 1.6 Reference Lists

For descriptions of the counters listed, use the `papi_native_avail` command or view the contents of the `$CRAYPAT_ROOT/share/Counters.papi_avail` file.

### 1.6.1 Aries Performance Counters That Collect Counts

This list summarizes the Aries performance counters that can be used to collect counts or otherwise increment when a specific event occurs. They are free-running and are not reset, except at boot time.

```
AR_NIC_AMO_PRF_STALL_DURATION_FLUSH
AR_NIC_AMO_PRF_STALL_DURATION_FULL
AR_NIC_AMO_PRF_STALL_DURATION_MATCH
AR_NIC_AMO_PRF_STALL_DURATION_ORDERED
AR_NIC_BTE_PRF_BYTES_0
AR_NIC_BTE_PRF_BYTES_1
AR_NIC_BTE_PRF_BYTES_2
AR_NIC_BTE_PRF_BYTES_3
AR_NIC_NETMON_CE_EVENT_CNTR_BAD_REQS
AR_NIC_NETMON_CE_EVENT_CNTR_CONFIG_MBE
AR_NIC_NETMON_CE_EVENT_CNTR_CONFIG_SBE
AR_NIC_NETMON_CE_EVENT_CNTR_DFLIT_MBE
AR_NIC_NETMON_CE_EVENT_CNTR_DFLIT_SBE
```

AR\_NIC\_NETMON\_CE\_EVENT\_CNTR\_HEADER\_PC  
AR\_NIC\_NETMON\_CE\_EVENT\_CNTR\_REDUCT\_SCAT0  
AR\_NIC\_NETMON\_CE\_EVENT\_CNTR\_REDUCT\_SCAT1  
AR\_NIC\_NETMON\_CE\_EVENT\_CNTR\_REDUCT\_SCAT2  
AR\_NIC\_NETMON\_CE\_EVENT\_CNTR\_REDUCT\_SCAT3  
AR\_NIC\_NETMON\_NAT\_EVENT\_CNTR\_BTE\_BLOCKED  
AR\_NIC\_NETMON\_NAT\_EVENT\_CNTR\_BTE\_STALLED  
AR\_NIC\_NETMON\_NAT\_EVENT\_CNTR\_BTE\_TRANSLATIONS  
AR\_NIC\_NETMON\_NAT\_EVENT\_CNTR\_CQ\_BLOCKED  
AR\_NIC\_NETMON\_NAT\_EVENT\_CNTR\_CQ\_STALLED  
AR\_NIC\_NETMON\_NAT\_EVENT\_CNTR\_CQ\_TRANSLATIONS  
AR\_NIC\_NETMON\_NAT\_EVENT\_CNTR\_MDDT0\_MBE  
AR\_NIC\_NETMON\_NAT\_EVENT\_CNTR\_MDDT0\_SBE  
AR\_NIC\_NETMON\_NAT\_EVENT\_CNTR\_MDDT1\_MBE  
AR\_NIC\_NETMON\_NAT\_EVENT\_CNTR\_MDDT1\_SBE  
AR\_NIC\_NETMON\_NAT\_EVENT\_CNTR\_PIPELINE\_STALLED  
AR\_NIC\_NETMON\_NAT\_EVENT\_CNTR\_PTT\_MBE  
AR\_NIC\_NETMON\_NAT\_EVENT\_CNTR\_PTT\_SBE  
AR\_NIC\_NETMON\_NAT\_EVENT\_CNTR\_RAT\_BLOCKED  
AR\_NIC\_NETMON\_NAT\_EVENT\_CNTR\_RAT\_STALLED  
AR\_NIC\_NETMON\_NAT\_EVENT\_CNTR\_RAT\_TRANSLATIONS  
AR\_NIC\_NETMON\_NETMON\_EVENT\_CNTR\_TRIGGER0  
AR\_NIC\_NETMON\_NETMON\_EVENT\_CNTR\_TRIGGER1  
AR\_NIC\_NETMON\_ORB\_EVENT\_CNTR\_CMD\_MISMATCH  
AR\_NIC\_NETMON\_ORB\_EVENT\_CNTR\_ILLEGAL\_LSTATUS  
AR\_NIC\_NETMON\_ORB\_EVENT\_CNTR\_ILLEGAL\_PKTID  
AR\_NIC\_NETMON\_ORB\_EVENT\_CNTR\_NO\_ENTRY  
AR\_NIC\_NETMON\_ORB\_EVENT\_CNTR\_NTT\_MBE  
AR\_NIC\_NETMON\_ORB\_EVENT\_CNTR\_NTT\_SBE  
AR\_NIC\_NETMON\_ORB\_EVENT\_CNTR\_ORD\_MBE  
AR\_NIC\_NETMON\_ORB\_EVENT\_CNTR\_ORD\_RAM\_READ  
AR\_NIC\_NETMON\_ORB\_EVENT\_CNTR\_ORD\_SBE  
AR\_NIC\_NETMON\_ORB\_EVENT\_CNTR\_PTT\_MBE  
AR\_NIC\_NETMON\_ORB\_EVENT\_CNTR\_PTT\_SBE  
AR\_NIC\_NETMON\_ORB\_EVENT\_CNTR\_REQ\_FIFO\_MBE  
AR\_NIC\_NETMON\_ORB\_EVENT\_CNTR\_REQ\_FIFO\_PE  
AR\_NIC\_NETMON\_ORB\_EVENT\_CNTR\_REQ\_FIFO\_SBE  
AR\_NIC\_NETMON\_ORB\_EVENT\_CNTR\_REQ\_FLITS  
AR\_NIC\_NETMON\_ORB\_EVENT\_CNTR\_REQ\_PKTS  
AR\_NIC\_NETMON\_ORB\_EVENT\_CNTR\_REQ\_STALLED  
AR\_NIC\_NETMON\_ORB\_EVENT\_CNTR\_RSP\_BLOCKED  
AR\_NIC\_NETMON\_ORB\_EVENT\_CNTR\_RSP\_BLOCKED\_PKT\_GEN  
AR\_NIC\_NETMON\_ORB\_EVENT\_CNTR\_RSP\_FIFO\_MBE  
AR\_NIC\_NETMON\_ORB\_EVENT\_CNTR\_RSP\_FIFO\_PE  
AR\_NIC\_NETMON\_ORB\_EVENT\_CNTR\_RSP\_FIFO\_SBE  
AR\_NIC\_NETMON\_ORB\_EVENT\_CNTR\_RSP\_FIFO\_SPKT  
AR\_NIC\_NETMON\_ORB\_EVENT\_CNTR\_RSP\_FLITS  
AR\_NIC\_NETMON\_ORB\_EVENT\_CNTR\_RSP\_NET\_TRACK  
AR\_NIC\_NETMON\_ORB\_EVENT\_CNTR\_RSP\_PKTS  
AR\_NIC\_NETMON\_ORB\_EVENT\_CNTR\_RSP\_STALLED  
AR\_NIC\_NETMON\_ORB\_EVENT\_CNTR\_SCRUB\_CNT  
AR\_NIC\_NETMON\_PRF\_EVENT\_CNTR  
AR\_NIC\_NETMON\_RAT\_EVENT\_CNTR\_IOMMU\_BLOCKED  
AR\_NIC\_NETMON\_RAT\_EVENT\_CNTR\_IOMMU\_DROP\_INPUT  
AR\_NIC\_NETMON\_RAT\_EVENT\_CNTR\_IOMMU\_FLITS  
AR\_NIC\_NETMON\_RAT\_EVENT\_CNTR\_IOMMU\_PARITY\_INPUT  
AR\_NIC\_NETMON\_RAT\_EVENT\_CNTR\_IOMMU\_PAYLD\_ERR  
AR\_NIC\_NETMON\_RAT\_EVENT\_CNTR\_IOMMU\_PKTS

AR\_NIC\_NETMON\_RAT\_EVENT\_CNTR\_NL\_BLOCKED  
AR\_NIC\_NETMON\_RAT\_EVENT\_CNTR\_NL\_FLITS  
AR\_NIC\_NETMON\_RAT\_EVENT\_CNTR\_NL\_PKTS  
AR\_NIC\_NETMON\_RAT\_EVENT\_CNTR\_ORB\_FLITS  
AR\_NIC\_NETMON\_RAT\_EVENT\_CNTR\_ORB\_PKTS  
AR\_NIC\_NETMON\_RAT\_EVENT\_CNTR\_REQ\_FLIT  
AR\_NIC\_NETMON\_RAT\_EVENT\_CNTR\_REQ\_INV\_CMD  
AR\_NIC\_NETMON\_RAT\_EVENT\_CNTR\_REQ\_MALFORMED  
AR\_NIC\_NETMON\_RAT\_EVENT\_CNTR\_REQ\_PARITY\_INPUT  
AR\_NIC\_NETMON\_RAT\_EVENT\_CNTR\_REQ\_PARITY\_OUTPUT  
AR\_NIC\_NETMON\_RAT\_EVENT\_CNTR\_REQ\_PAYLD\_ERR  
AR\_NIC\_NETMON\_RAT\_EVENT\_CNTR\_REQ\_STALLED  
AR\_NIC\_NETMON\_RAT\_EVENT\_CNTR\_REQ\_XLAT\_ERR  
AR\_NIC\_NETMON\_RAT\_EVENT\_CNTR\_RSP\_FLIT  
AR\_NIC\_NETMON\_RAT\_EVENT\_CNTR\_RSP\_INV\_CMD  
AR\_NIC\_NETMON\_RAT\_EVENT\_CNTR\_RSP\_MALFORMED  
AR\_NIC\_NETMON\_RAT\_EVENT\_CNTR\_RSP\_PARITY\_INPUT  
AR\_NIC\_NETMON\_RAT\_EVENT\_CNTR\_RSP\_PARITY\_OUTPUT  
AR\_NIC\_NETMON\_RAT\_EVENT\_CNTR\_RSP\_PAYLD\_ERR  
AR\_NIC\_NETMON\_RAT\_EVENT\_CNTR\_RSP\_STALLED  
AR\_NIC\_NETMON\_RAT\_EVENT\_CNTR\_RSP\_XLAT\_ERR  
AR\_NIC\_NETMON\_SSID\_EVENT\_CNTR\_ALLOCATIONS  
AR\_NIC\_NETMON\_SSID\_EVENT\_CNTR\_BTESEND\_SUCCESSFUL  
AR\_NIC\_NETMON\_SSID\_EVENT\_CNTR\_BTESEND\_UNSUCCESSFUL  
AR\_NIC\_NETMON\_SSID\_EVENT\_CNTR\_BYPASSED\_REQ  
AR\_NIC\_NETMON\_SSID\_EVENT\_CNTR\_CORR\_REQ\_PAYLD\_ERR  
AR\_NIC\_NETMON\_SSID\_EVENT\_CNTR\_CORRUPT\_REQ\_HD  
AR\_NIC\_NETMON\_SSID\_EVENT\_CNTR\_CORRUPT\_RSP  
AR\_NIC\_NETMON\_SSID\_EVENT\_CNTR\_DSMN\_OPTIMAL  
AR\_NIC\_NETMON\_SSID\_EVENT\_CNTR\_DSMN\_SUBOPTIMAL  
AR\_NIC\_NETMON\_SSID\_EVENT\_CNTR\_INTENTIONAL\_TIMEOUT  
AR\_NIC\_NETMON\_SSID\_EVENT\_CNTR\_LATE\_FIRST\_REQRSP  
AR\_NIC\_NETMON\_SSID\_EVENT\_CNTR\_LREQ\_ERR\_HSS  
AR\_NIC\_NETMON\_SSID\_EVENT\_CNTR\_LREQ\_ERR\_OS  
AR\_NIC\_NETMON\_SSID\_EVENT\_CNTR\_MEM\_ERR\_CORR  
AR\_NIC\_NETMON\_SSID\_EVENT\_CNTR\_MEM\_ERR\_UNCORR  
AR\_NIC\_NETMON\_SSID\_EVENT\_CNTR\_NO\_DEST\_NOTIFY  
AR\_NIC\_NETMON\_SSID\_EVENT\_CNTR\_NO\_SSID  
AR\_NIC\_NETMON\_SSID\_EVENT\_CNTR\_NO\_SSID\_DURATN  
AR\_NIC\_NETMON\_SSID\_EVENT\_CNTR\_REQ\_LEN\_ERR  
AR\_NIC\_NETMON\_SSID\_EVENT\_CNTR\_REQ\_STALLED\_DURATN  
AR\_NIC\_NETMON\_SSID\_EVENT\_CNTR\_RREQ\_HSS  
AR\_NIC\_NETMON\_SSID\_EVENT\_CNTR\_RREQ\_OS  
AR\_NIC\_NETMON\_SSID\_EVENT\_CNTR\_RSP\_HSS  
AR\_NIC\_NETMON\_SSID\_EVENT\_CNTR\_RSP\_OS  
AR\_NIC\_NETMON\_SSID\_EVENT\_CNTR\_SEQ\_ERR\_HSS  
AR\_NIC\_NETMON\_SSID\_EVENT\_CNTR\_SEQ\_ERR\_OS  
AR\_NIC\_NETMON\_SSID\_EVENT\_CNTR\_STALE\_SSID  
AR\_NIC\_NETMON\_SSID\_EVENT\_CNTR\_UNCORR\_REQ\_PAYLD\_ERR  
AR\_NIC\_NETMON\_SSID\_EVENT\_CNTR\_UNEXPTD\_RSP  
AR\_NIC\_NETMON\_TARB\_EVENT\_CNTR\_BTE\_BLOCKED  
AR\_NIC\_NETMON\_TARB\_EVENT\_CNTR\_BTE\_FLITS  
AR\_NIC\_NETMON\_TARB\_EVENT\_CNTR\_BTE\_PKTS  
AR\_NIC\_NETMON\_TARB\_EVENT\_CNTR\_CE\_BLOCKED  
AR\_NIC\_NETMON\_TARB\_EVENT\_CNTR\_CE\_FLITS  
AR\_NIC\_NETMON\_TARB\_EVENT\_CNTR\_CE\_PKTS  
AR\_NIC\_NETMON\_TARB\_EVENT\_CNTR\_DLA\_BLOCKED  
AR\_NIC\_NETMON\_TARB\_EVENT\_CNTR\_DLA\_FLITS

```
AR_NIC_NETMON_TARB_EVENT_CNTR_DLA_PKTS
AR_NIC_NETMON_TARB_EVENT_CNTR_SSID_FLITS
AR_NIC_NETMON_TARB_EVENT_CNTR_SSID_PKTS
AR_NIC_NETMON_TARB_EVENT_CNTR_SSID_STALLED
AR_NIC_NPT_PRF_FULL_DURATION
AR_NIC_NPT_PRF_NL_STALL_DURATION
AR_NIC_NSLM_PRF_EVENT_CNTR
AR_NIC_ORB_PRF_NET_RSP_HIST_BIN01:BIN0_COUNT
AR_NIC_ORB_PRF_NET_RSP_HIST_BIN01:BIN1_COUNT
AR_NIC_ORB_PRF_NET_RSP_HIST_BIN23:BIN2_COUNT
AR_NIC_ORB_PRF_NET_RSP_HIST_BIN23:BIN3_COUNT
AR_NIC_ORB_PRF_NET_RSP_HIST_BIN45:BIN4_COUNT
AR_NIC_ORB_PRF_NET_RSP_HIST_BIN45:BIN5_COUNT
AR_NIC_ORB_PRF_NET_RSP_HIST_BIN67:BIN6_COUNT
AR_NIC_ORB_PRF_NET_RSP_HIST_BIN67:BIN7_COUNT
AR_NIC_ORB_PRF_NET_RSP_TRACK_1:MAX_RSP_TIME
AR_NIC_ORB_PRF_NET_RSP_TRACK_1:MIN_RSP_TIME
AR_NIC_ORB_PRF_NET_RSP_TRACK_2
AR_NIC_ORB_PRF_REQ_BYTES_SENT
AR_NIC_ORB_PRF_REQ_CRDTS_STALL_DURATION
AR_NIC_ORB_PRF_REQ_PKTID_STALL_DURATION
AR_NIC_ORB_PRF_RSP_BYTES_RCVD
AR_NIC_PARB_PRF_STALL_DURATION_VC0
AR_NIC_PARB_PRF_STALL_DURATION_VC1
AR_NIC_PARB_PRF_STALL_DURATION_VC2
AR_NIC_RAT_PRF_REQ_BYTES_RCVD
AR_NIC_RAT_PRF_REQ_STALL_DURATION
AR_NIC_RAT_PRF_RSP_STALL_DURATION
AR_NIC_REQMON_AMO_EVENT_CNTR_AMO_DONE
AR_NIC_REQMON_AMO_EVENT_CNTR_AMO_HIT
AR_NIC_REQMON_AMO_EVENT_CNTR_AMO_MISS
AR_NIC_REQMON_AMO_EVENT_CNTR_DATASTORE_RD_POISON
AR_NIC_REQMON_AMO_EVENT_CNTR_DATASTORE_WR_POISON
AR_NIC_REQMON_AMO_EVENT_CNTR_ERR_REQLIST_MBE
AR_NIC_REQMON_AMO_EVENT_CNTR_ERR_REQLIST_SBE
AR_NIC_REQMON_AMO_EVENT_CNTR_ERR_REQ_MBE
AR_NIC_REQMON_AMO_EVENT_CNTR_ERR_REQ_PARITY
AR_NIC_REQMON_AMO_EVENT_CNTR_ERR_REQ_SBE
AR_NIC_REQMON_AMO_EVENT_CNTR_ERR_RSP_PARITY
AR_NIC_REQMON_AMO_EVENT_CNTR_ERR_RSP_SBE
AR_NIC_REQMON_AMO_EVENT_CNTR_FILL_RSP
AR_NIC_REQMON_AMO_EVENT_CNTR_INVALIDATE
AR_NIC_REQMON_AMO_EVENT_CNTR_REQ0_BLOCK
AR_NIC_REQMON_AMO_EVENT_CNTR_REQ0_FLIT
AR_NIC_REQMON_AMO_EVENT_CNTR_REQ0_PKT
AR_NIC_REQMON_AMO_EVENT_CNTR_REQ0_STALL
AR_NIC_REQMON_AMO_EVENT_CNTR_REQ1_BLOCK
AR_NIC_REQMON_AMO_EVENT_CNTR_REQ1_PKT
AR_NIC_REQMON_AMO_EVENT_CNTR_REQ1_STALL
AR_NIC_REQMON_AMO_EVENT_CNTR_RSP0_BLOCK
AR_NIC_REQMON_AMO_EVENT_CNTR_RSP0_FLIT
AR_NIC_REQMON_AMO_EVENT_CNTR_RSP0_PKT
AR_NIC_REQMON_AMO_EVENT_CNTR_RSP1_BLOCK
AR_NIC_REQMON_AMO_EVENT_CNTR_RSP1_FLIT
AR_NIC_REQMON_AMO_EVENT_CNTR_RSP1_PKT
AR_NIC_REQMON_AMO_EVENT_CNTR_STALL_FLUSH
AR_NIC_REQMON_AMO_EVENT_CNTR_STALL_FULL
AR_NIC_REQMON_AMO_EVENT_CNTR_STALL_MATCH
```

AR\_NIC\_REQMON\_AMO\_EVENT\_CNTR\_STALL\_ORDERED  
AR\_NIC\_REQMON\_AMO\_EVENT\_CNTR\_TIMEOUT\_ARM  
AR\_NIC\_REQMON\_BTE\_EVENT\_CNTR\_AGGREGATED\_CQE  
AR\_NIC\_REQMON\_BTE\_EVENT\_CNTR\_DESC0  
AR\_NIC\_REQMON\_BTE\_EVENT\_CNTR\_DESC1  
AR\_NIC\_REQMON\_BTE\_EVENT\_CNTR\_DESC2  
AR\_NIC\_REQMON\_BTE\_EVENT\_CNTR\_DESC3  
AR\_NIC\_REQMON\_BTE\_EVENT\_CNTR\_INV\_CMD0  
AR\_NIC\_REQMON\_BTE\_EVENT\_CNTR\_INV\_CMD1  
AR\_NIC\_REQMON\_BTE\_EVENT\_CNTR\_INV\_CMD2  
AR\_NIC\_REQMON\_BTE\_EVENT\_CNTR\_INV\_CMD3  
AR\_NIC\_REQMON\_BTE\_EVENT\_CNTR\_NAT\_ERR0  
AR\_NIC\_REQMON\_BTE\_EVENT\_CNTR\_NAT\_ERR1  
AR\_NIC\_REQMON\_BTE\_EVENT\_CNTR\_NAT\_ERR2  
AR\_NIC\_REQMON\_BTE\_EVENT\_CNTR\_NAT\_ERR3  
AR\_NIC\_REQMON\_BTE\_EVENT\_CNTR\_NET\_PKT0  
AR\_NIC\_REQMON\_BTE\_EVENT\_CNTR\_NET\_PKT1  
AR\_NIC\_REQMON\_BTE\_EVENT\_CNTR\_NET\_PKT2  
AR\_NIC\_REQMON\_BTE\_EVENT\_CNTR\_NET\_PKT3  
AR\_NIC\_REQMON\_BTE\_EVENT\_CNTR\_PARB\_RD\_FLT\_BLK  
AR\_NIC\_REQMON\_BTE\_EVENT\_CNTR\_PARB\_RD\_PKT\_BLK  
AR\_NIC\_REQMON\_BTE\_EVENT\_CNTR\_PARB\_WR\_FLT\_BLK  
AR\_NIC\_REQMON\_BTE\_EVENT\_CNTR\_PI\_RD\_RSP\_ERR0  
AR\_NIC\_REQMON\_BTE\_EVENT\_CNTR\_PI\_RD\_RSP\_ERR1  
AR\_NIC\_REQMON\_BTE\_EVENT\_CNTR\_PI\_RD\_RSP\_ERR2  
AR\_NIC\_REQMON\_BTE\_EVENT\_CNTR\_PI\_RD\_RSP\_ERR3  
AR\_NIC\_REQMON\_BTE\_EVENT\_CNTR\_PROC\_TIME0  
AR\_NIC\_REQMON\_BTE\_EVENT\_CNTR\_PROC\_TIME1  
AR\_NIC\_REQMON\_BTE\_EVENT\_CNTR\_PROC\_TIME2  
AR\_NIC\_REQMON\_BTE\_EVENT\_CNTR\_PROC\_TIME3  
AR\_NIC\_REQMON\_BTE\_EVENT\_CNTR\_RD\_REQ0  
AR\_NIC\_REQMON\_BTE\_EVENT\_CNTR\_RD\_REQ1  
AR\_NIC\_REQMON\_BTE\_EVENT\_CNTR\_RD\_REQ2  
AR\_NIC\_REQMON\_BTE\_EVENT\_CNTR\_RD\_REQ3  
AR\_NIC\_REQMON\_BTE\_EVENT\_CNTR\_RD\_RSP0  
AR\_NIC\_REQMON\_BTE\_EVENT\_CNTR\_RD\_RSP1  
AR\_NIC\_REQMON\_BTE\_EVENT\_CNTR\_RD\_RSP2  
AR\_NIC\_REQMON\_BTE\_EVENT\_CNTR\_RD\_RSP3  
AR\_NIC\_REQMON\_BTE\_EVENT\_CNTR\_TARB\_FLT\_BLK  
AR\_NIC\_REQMON\_BTE\_EVENT\_CNTR\_TARB\_PKT\_BLK  
AR\_NIC\_REQMON\_BTE\_EVENT\_CNTR\_TIMEOUT\_ERR0  
AR\_NIC\_REQMON\_BTE\_EVENT\_CNTR\_TIMEOUT\_ERR1  
AR\_NIC\_REQMON\_BTE\_EVENT\_CNTR\_TIMEOUT\_ERR2  
AR\_NIC\_REQMON\_BTE\_EVENT\_CNTR\_TIMEOUT\_ERR3  
AR\_NIC\_REQMON\_BTE\_EVENT\_CNTR\_UNCORRECTABLE\_ERR0  
AR\_NIC\_REQMON\_BTE\_EVENT\_CNTR\_UNCORRECTABLE\_ERR1  
AR\_NIC\_REQMON\_BTE\_EVENT\_CNTR\_UNCORRECTABLE\_ERR2  
AR\_NIC\_REQMON\_BTE\_EVENT\_CNTR\_UNCORRECTABLE\_ERR3  
AR\_NIC\_REQMON\_DLA\_EVENT\_CNTR\_DATA\_MBE  
AR\_NIC\_REQMON\_DLA\_EVENT\_CNTR\_FIFO\_ENQ\_PKT  
AR\_NIC\_REQMON\_DLA\_EVENT\_CNTR\_FIFO\_HDR\_MBE  
AR\_NIC\_REQMON\_DLA\_EVENT\_CNTR\_FMA\_PKT\_ERR  
AR\_NIC\_REQMON\_DLA\_EVENT\_CNTR\_HDR\_PERR  
AR\_NIC\_REQMON\_DLA\_EVENT\_CNTR\_HI\_CD\_ALLOC\_FAIL  
AR\_NIC\_REQMON\_DLA\_EVENT\_CNTR\_HI\_CD\_ALLOC\_PASS  
AR\_NIC\_REQMON\_DLA\_EVENT\_CNTR\_HI\_CD\_ENQ  
AR\_NIC\_REQMON\_DLA\_EVENT\_CNTR\_LO\_CD\_ALLOC\_FAIL  
AR\_NIC\_REQMON\_DLA\_EVENT\_CNTR\_LO\_CD\_ALLOC\_PASS



AR\_NIC\_REQMON\_DLA\_EVENT\_CNTR\_LO\_CD\_ENQ  
AR\_NIC\_REQMON\_DLA\_EVENT\_CNTR\_PR\_ALLOC\_FAIL  
AR\_NIC\_REQMON\_DLA\_EVENT\_CNTR\_PR\_ALLOC\_PASS  
AR\_NIC\_REQMON\_DLA\_EVENT\_CNTR\_PR\_ENQ  
AR\_NIC\_REQMON\_DLA\_EVENT\_CNTR\_TARB\_STALLED  
AR\_NIC\_REQMON\_FMA\_EVENT\_CNTR\_CQ  
AR\_NIC\_REQMON\_FMA\_EVENT\_CNTR\_CQ\_STALL  
AR\_NIC\_REQMON\_FMA\_EVENT\_CNTR\_DATA\_MBE  
AR\_NIC\_REQMON\_FMA\_EVENT\_CNTR\_DESC\_MBE  
AR\_NIC\_REQMON\_FMA\_EVENT\_CNTR\_FAMO\_REQ  
AR\_NIC\_REQMON\_FMA\_EVENT\_CNTR\_FLIT  
AR\_NIC\_REQMON\_FMA\_EVENT\_CNTR\_GET\_REQ  
AR\_NIC\_REQMON\_FMA\_EVENT\_CNTR\_HDR\_PERR  
AR\_NIC\_REQMON\_FMA\_EVENT\_CNTR\_INV\_CMD  
AR\_NIC\_REQMON\_FMA\_EVENT\_CNTR\_NFAMO\_REQ  
AR\_NIC\_REQMON\_FMA\_EVENT\_CNTR\_PI\_FLIT  
AR\_NIC\_REQMON\_FMA\_EVENT\_CNTR\_PI\_PKT  
AR\_NIC\_REQMON\_FMA\_EVENT\_CNTR\_PKT  
AR\_NIC\_REQMON\_FMA\_EVENT\_CNTR\_PUT\_REQ  
AR\_NIC\_REQMON\_FMA\_EVENT\_CNTR\_TARB\_STALLED  
AR\_NIC\_REQMON\_FMA\_EVENT\_CNTR\_TDATA\_SBE  
AR\_NIC\_REQMON\_PRF\_EVENT\_CNTR  
AR\_NIC\_REQMON\_REQMON\_EVENT\_CNTR\_TRIGGER0  
AR\_NIC\_REQMON\_REQMON\_EVENT\_CNTR\_TRIGGER1  
AR\_NIC\_REQMON\_WC\_EVENT\_CNTR\_BUF0\_ALLOCATION  
AR\_NIC\_REQMON\_WC\_EVENT\_CNTR\_BUF0\_COMBINE  
AR\_NIC\_REQMON\_WC\_EVENT\_CNTR\_BUF0\_EVICTION  
AR\_NIC\_REQMON\_WC\_EVENT\_CNTR\_BUF1\_ALLOCATION  
AR\_NIC\_REQMON\_WC\_EVENT\_CNTR\_BUF1\_COMBINE  
AR\_NIC\_REQMON\_WC\_EVENT\_CNTR\_BUF1\_EVICTION  
AR\_NIC\_REQMON\_WC\_EVENT\_CNTR\_BUFFER\_TIMEOUT  
AR\_NIC\_REQMON\_WC\_EVENT\_CNTR\_NTWK\_RSP\_HDR\_PBE  
AR\_NIC\_REQMON\_WC\_EVENT\_CNTR\_PARB\_PUT  
AR\_NIC\_REQMON\_WC\_EVENT\_CNTR\_RAT\_GETRESP  
AR\_NIC\_REQMON\_WC\_EVENT\_CNTR\_RSPIN0\_HDR\_PBE  
AR\_NIC\_REQMON\_WC\_EVENT\_CNTR\_RSPIN1\_HDR\_PBE  
AR\_NIC\_REQMON\_WC\_EVENT\_CNTR\_TAG\_PBE  
AR\_NIC\_REQMON\_WC\_EVENT\_CNTR\_WC\_IN  
AR\_NIC\_REQMON\_WC\_EVENT\_CNTR\_WC\_OUT  
AR\_NIC\_RMT\_PRF\_PUT\_BYTES\_RX  
AR\_NIC\_RMT\_PRF\_SEND\_BYTES\_RX  
AR\_NIC\_RSPMON\_CQ\_EVENT\_CNTR\_BTE  
AR\_NIC\_RSPMON\_CQ\_EVENT\_CNTR\_BTE\_BLOCKED  
AR\_NIC\_RSPMON\_CQ\_EVENT\_CNTR\_BTE\_DROPPED  
AR\_NIC\_RSPMON\_CQ\_EVENT\_CNTR\_BTE\_INPUT\_EVENT\_DATA\_PARITY\_ERR  
AR\_NIC\_RSPMON\_CQ\_EVENT\_CNTR\_BTE\_STALLED  
AR\_NIC\_RSPMON\_CQ\_EVENT\_CNTR\_DESC\_TBL\_MBE0  
AR\_NIC\_RSPMON\_CQ\_EVENT\_CNTR\_DESC\_TBL\_MBE1  
AR\_NIC\_RSPMON\_CQ\_EVENT\_CNTR\_DESC\_TBL\_MBE2  
AR\_NIC\_RSPMON\_CQ\_EVENT\_CNTR\_DESC\_TBL\_MBE3  
AR\_NIC\_RSPMON\_CQ\_EVENT\_CNTR\_DESC\_TBL\_SBE0  
AR\_NIC\_RSPMON\_CQ\_EVENT\_CNTR\_DESC\_TBL\_SBE1  
AR\_NIC\_RSPMON\_CQ\_EVENT\_CNTR\_DESC\_TBL\_SBE2  
AR\_NIC\_RSPMON\_CQ\_EVENT\_CNTR\_DESC\_TBL\_SBE3  
AR\_NIC\_RSPMON\_CQ\_EVENT\_CNTR\_DLA  
AR\_NIC\_RSPMON\_CQ\_EVENT\_CNTR\_DLA\_BLOCKED  
AR\_NIC\_RSPMON\_CQ\_EVENT\_CNTR\_DLA\_DROPPED  
AR\_NIC\_RSPMON\_CQ\_EVENT\_CNTR\_DLA\_INPUT\_EVENT\_DATA\_PARITY\_ERR

AR\_NIC\_RSPMON\_CQ\_EVENT\_CNTR\_DLA\_STALLED  
 AR\_NIC\_RSPMON\_CQ\_EVENT\_CNTR\_FMA  
 AR\_NIC\_RSPMON\_CQ\_EVENT\_CNTR\_NPT\_FLIT  
 AR\_NIC\_RSPMON\_CQ\_EVENT\_CNTR\_NPT\_PKT  
 AR\_NIC\_RSPMON\_CQ\_EVENT\_CNTR\_NPT\_STALLED  
 AR\_NIC\_RSPMON\_CQ\_EVENT\_CNTR\_RMT  
 AR\_NIC\_RSPMON\_CQ\_EVENT\_CNTR\_RMT\_BLOCKED  
 AR\_NIC\_RSPMON\_CQ\_EVENT\_CNTR\_RMT\_DROPPED  
 AR\_NIC\_RSPMON\_CQ\_EVENT\_CNTR\_RMT\_INPUT\_EVENT\_DATA\_PARITY\_ERR  
 AR\_NIC\_RSPMON\_CQ\_EVENT\_CNTR\_RMT\_INPUT\_RMT\_DATA\_PARITY\_ERR  
 AR\_NIC\_RSPMON\_CQ\_EVENT\_CNTR\_RMT\_STALLED  
 AR\_NIC\_RSPMON\_CQ\_EVENT\_CNTR\_SSID  
 AR\_NIC\_RSPMON\_CQ\_EVENT\_CNTR\_SSID\_BLOCKED  
 AR\_NIC\_RSPMON\_CQ\_EVENT\_CNTR\_SSID\_DROPPED  
 AR\_NIC\_RSPMON\_CQ\_EVENT\_CNTR\_SSID\_INPUT\_EVENT\_DATA\_PARITY\_ERR  
 AR\_NIC\_RSPMON\_CQ\_EVENT\_CNTR\_SSID\_STALLED  
 AR\_NIC\_RSPMON\_CQ\_EVENT\_CNTR\_TRANS\_CMP\_Q\_PARITY\_ERR  
 AR\_NIC\_RSPMON\_IOMMU\_EVENT\_CNTR\_EVENTS  
 AR\_NIC\_RSPMON\_IOMMU\_EVENT\_CNTR\_IDLE  
 AR\_NIC\_RSPMON\_IOMMU\_EVENT\_CNTR\_IOPF\_ACCESS  
 AR\_NIC\_RSPMON\_IOMMU\_EVENT\_CNTR\_IOPF\_PTE  
 AR\_NIC\_RSPMON\_IOMMU\_EVENT\_CNTR\_IOPF\_TC  
 AR\_NIC\_RSPMON\_IOMMU\_EVENT\_CNTR\_OFB\_CA  
 AR\_NIC\_RSPMON\_IOMMU\_EVENT\_CNTR\_PTEC\_EVICT  
 AR\_NIC\_RSPMON\_IOMMU\_EVENT\_CNTR\_PTEC\_HIT  
 AR\_NIC\_RSPMON\_IOMMU\_EVENT\_CNTR\_PTEC\_INVALID  
 AR\_NIC\_RSPMON\_IOMMU\_EVENT\_CNTR\_PTEC\_MISS  
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 AR\_NIC\_RSPMON\_IOMMU\_EVENT\_CNTR\_PTE\_RF  
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 AR\_NIC\_RSPMON\_IOMMU\_EVENT\_CNTR\_TR\_2LVL\_RSP  
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 AR\_NIC\_RSPMON\_NPT\_EVENT\_CNTR\_ERR\_SBE  
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 AR\_NIC\_RSPMON\_NPT\_EVENT\_CNTR\_INPUT\_STALLED  
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 AR\_NIC\_RSPMON\_NPT\_EVENT\_CNTR\_PKT\_DROP  
 AR\_NIC\_RSPMON\_NPT\_EVENT\_CNTR\_PKT\_IGNORE  
 AR\_NIC\_RSPMON\_NPT\_EVENT\_CNTR\_P\_PKTS  
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AR\_NIC\_WC\_PRF\_RSP\_BYTES\_RCVD

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AR\_PI\_LM1\_LM0\_TRIGGER0\_CNT  
AR\_PI\_LM1\_LM0\_TRIGGER1\_CNT  
AR\_PI\_PII\_F0\_MSIX\_MSG\_CNT  
AR\_PI\_PII\_F0\_MSIX\_TABLE\_MBE\_CNT  
AR\_PI\_PII\_F0\_MSIX\_TABLE\_SBE\_CNT  
AR\_PI\_PII\_F1\_MSIX\_MSG\_CNT  
AR\_PI\_PII\_F1\_MSIX\_TABLE\_MBE\_CNT  
AR\_PI\_PII\_F1\_MSIX\_TABLE\_SBE\_CNT  
AR\_PI\_PII\_F2\_MSIX\_MSG\_CNT  
AR\_PI\_PII\_F2\_MSIX\_TABLE\_MBE\_CNT  
AR\_PI\_PII\_F2\_MSIX\_TABLE\_SBE\_CNT  
AR\_PI\_PII\_F3\_MSIX\_MSG\_CNT  
AR\_PI\_PII\_F3\_MSIX\_TABLE\_MBE\_CNT  
AR\_PI\_PII\_F3\_MSIX\_TABLE\_SBE\_CNT  
AR\_PI\_PII\_PCLK\_MSIX\_MSG\_CNT  
AR\_PI\_PII\_PCLK\_MSIX\_MSG\_FIFO\_SBE\_CNT  
AR\_PI\_PII\_PCLK\_MSIX\_MSG\_STALLED\_CNT  
AR\_PI\_PMI\_IRSP\_PKTS  
AR\_PI\_PMI\_IRSP\_PKTS\_BLKD  
AR\_PI\_PMI\_NREQ\_AMO\_PKTS  
AR\_PI\_PMI\_NREQ\_GET\_PKTS  
AR\_PI\_PMI\_NREQ\_IRSP\_BLKD  
AR\_PI\_PMI\_NREQ\_NIC\_FLITS  
AR\_PI\_PMI\_NREQ\_OTHER\_PKTS  
AR\_PI\_PMI\_NREQ\_PTC\_BLKD0  
AR\_PI\_PMI\_NREQ\_PTC\_BLKD1  
AR\_PI\_PMI\_NREQ\_PTC\_BLKD2  
AR\_PI\_PMI\_NREQ\_PUT\_PKTS  
AR\_PI\_PMI\_NREQ\_RR\_BLKD  
AR\_PI\_PMI\_NREQ\_TCG\_BLKD0  
AR\_PI\_PMI\_NREQ\_TCG\_BLKD1  
AR\_PI\_PMI\_NREQ\_TCG\_BLKD2  
AR\_PI\_PMI\_NRSP\_BP  
AR\_PI\_PMI\_NRSP\_FLITS  
AR\_PI\_PMI\_NRSP\_HWM  
AR\_PI\_PMI\_NRSP\_PKTS  
AR\_PI\_PMI\_NRSP\_PKTS\_BLKD  
AR\_PI\_PMI\_PCLK\_PREQ\_BLKD  
AR\_PI\_PMI\_PCLK\_PREQ\_FLITS  
AR\_PI\_PMI\_PCLK\_PREQ\_PKTS  
AR\_PI\_PMI\_PCLK\_PREQ\_PTC\_BLKD0  
AR\_PI\_PMI\_PCLK\_PREQ\_PTC\_BLKD1  
AR\_PI\_PMI\_PCLK\_PREQ\_PTC\_BLKD2  
AR\_PI\_PMI\_PCLK\_PRSP\_BLKD  
AR\_PI\_PMI\_PCLK\_PRSP\_FLITS  
AR\_PI\_PMI\_PCLK\_PRSP\_PKTS  
AR\_PI\_PMI\_REQTRANS\_BLKD  
AR\_PI\_PTI\_LIF\_REQ\_IFIFO\_MBE\_CNT



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AR_PI_PTI_LIF_REQ_IFIFO_SBE_CNT
AR_PI_PTI_LIF_REQ_LB_STALLED_CNT
AR_PI_PTI_LIF_REQ_ORF_STALLED_CNT
AR_PI_PTI_LIF_REQ_PKT_DISCARD_CNT
AR_PI_PTI_LIF_REQ_RX_PKT_CNT
AR_PI_PTI_LIF_REQ_TX_PKT_CNT
AR_PI_PTI_LIF_RSP_HAL_STALLED_CNT
AR_PI_PTI_LIF_RSP_RX_PKT_CNT
AR_PI_PTI_LIF_RSP_TX_ABORT_CNT
AR_PI_PTI_LIF_RSP_TX_PKT_CNT
AR_PI_PTI_LIF_RSP_TX_UR_CNT
AR_PI_PTI_NIF_IFIFO_MBE_CNT
AR_PI_PTI_NIF_IFIFO_SBE_CNT
AR_PI_PTI_NIF_NIC_DATA_BYTE_CNT
AR_PI_PTI_NIF_NIC_DATA_QW_CNT
AR_PI_PTI_NIF_NIC_STALLED_CNT
AR_PI_PTI_NIF_PKT_DISCARD_CNT
AR_PI_PTI_NIF_RX_PKT_CNT
AR_PI_PTI_NIF_TX_PKT_CNT
AR_PI_PTI_PCLK_LIF_RX_PKT_CNT
AR_PI_PTI_PCLK_NIF_RX_PKT_CNT
AR_PI_PTI_PCLK_TRSP_OFIFO_SBE_CNT
AR_PI_PTI_PCLK_TRSP_TX_PKT_CNT
AR_RTR_0_0_COLBUF_PERF_STALL_RQ:COL_BUF_PERF_STALL_RQ
AR_RTR_0_0_COLBUF_PERF_STALL_RQ:VC_PTR
AR_RTR_0_0_COLBUF_PERF_STALL_RS:COL_BUF_PERF_STALL_RS
AR_RTR_0_0_COLBUF_PERF_STALL_RS:VC_PTR
AR_RTR_0_0_INQ_PRF_INCOMING_FLIT_VC0
AR_RTR_0_0_INQ_PRF_INCOMING_FLIT_VC1
AR_RTR_0_0_INQ_PRF_INCOMING_FLIT_VC2
AR_RTR_0_0_INQ_PRF_INCOMING_FLIT_VC3
AR_RTR_0_0_INQ_PRF_INCOMING_FLIT_VC4
AR_RTR_0_0_INQ_PRF_INCOMING_FLIT_VC5
AR_RTR_0_0_INQ_PRF_INCOMING_FLIT_VC6
AR_RTR_0_0_INQ_PRF_INCOMING_FLIT_VC7
AR_RTR_0_0_INQ_PRF_INCOMING_PKT_VC0_FILTER_FLIT0_CNT
AR_RTR_0_0_INQ_PRF_INCOMING_PKT_VC1_FILTER_FLIT1_CNT
AR_RTR_0_0_INQ_PRF_INCOMING_PKT_VC2_FILTER_FLIT2_CNT
AR_RTR_0_0_INQ_PRF_INCOMING_PKT_VC3_FILTER_FLIT3_CNT
AR_RTR_0_0_INQ_PRF_INCOMING_PKT_VC4_FILTER_FLIT4_CNT
AR_RTR_0_0_INQ_PRF_INCOMING_PKT_VC5_FILTER_FLIT5_CNT
AR_RTR_0_0_INQ_PRF_INCOMING_PKT_VC6_FILTER_FLIT6_CNT
AR_RTR_0_0_INQ_PRF_INCOMING_PKT_VC7_FILTER_FLIT7_CNT
AR_RTR_0_0_INQ_PRF_MATCH_FLIT_3_TO_0_FILTERING_CNT
AR_RTR_0_0_INQ_PRF_MATCH_FLIT_7_TO_4_FILTERING_CNT
AR_RTR_0_0_INQ_PRF_PKT_TO_DEAD_LINK_CNT
AR_RTR_0_0_INQ_PRF_ROWBUS_2X_USAGE_CNT
AR_RTR_0_0_INQ_PRF_ROWBUS_STALL_CNT
AR_RTR_0_1_COLBUF_PERF_STALL_RQ:COL_BUF_PERF_STALL_RQ
AR_RTR_0_1_COLBUF_PERF_STALL_RQ:VC_PTR
AR_RTR_0_1_COLBUF_PERF_STALL_RS:COL_BUF_PERF_STALL_RS
AR_RTR_0_1_COLBUF_PERF_STALL_RS:VC_PTR
AR_RTR_0_1_INQ_PRF_INCOMING_FLIT_VC0
AR_RTR_0_1_INQ_PRF_INCOMING_FLIT_VC1
AR_RTR_0_1_INQ_PRF_INCOMING_FLIT_VC2
AR_RTR_0_1_INQ_PRF_INCOMING_FLIT_VC3
AR_RTR_0_1_INQ_PRF_INCOMING_FLIT_VC4
AR_RTR_0_1_INQ_PRF_INCOMING_FLIT_VC5
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AR_RTR_0_1_INQ_PRF_INCOMING_FLIT_VC6
AR_RTR_0_1_INQ_PRF_INCOMING_FLIT_VC7
AR_RTR_0_1_INQ_PRF_INCOMING_PKT_VC0_FILTER_FLIT0_CNT
AR_RTR_0_1_INQ_PRF_INCOMING_PKT_VC1_FILTER_FLIT1_CNT
AR_RTR_0_1_INQ_PRF_INCOMING_PKT_VC2_FILTER_FLIT2_CNT
AR_RTR_0_1_INQ_PRF_INCOMING_PKT_VC3_FILTER_FLIT3_CNT
AR_RTR_0_1_INQ_PRF_INCOMING_PKT_VC4_FILTER_FLIT4_CNT
AR_RTR_0_1_INQ_PRF_INCOMING_PKT_VC5_FILTER_FLIT5_CNT
AR_RTR_0_1_INQ_PRF_INCOMING_PKT_VC6_FILTER_FLIT6_CNT
AR_RTR_0_1_INQ_PRF_INCOMING_PKT_VC7_FILTER_FLIT7_CNT
AR_RTR_0_1_INQ_PRF_MATCH_FLIT_3_TO_0_FILTERING_CNT
AR_RTR_0_1_INQ_PRF_MATCH_FLIT_7_TO_4_FILTERING_CNT
AR_RTR_0_1_INQ_PRF_PKT_TO_DEAD_LINK_CNT
AR_RTR_0_1_INQ_PRF_ROWBUS_2X_USAGE_CNT
AR_RTR_0_1_INQ_PRF_ROWBUS_STALL_CNT
AR_RTR_0_2_COLBUF_PERF_STALL_RQ:COL_BUF_PERF_STALL_RQ
AR_RTR_0_2_COLBUF_PERF_STALL_RQ:VC_PTR
AR_RTR_0_2_COLBUF_PERF_STALL_RS:COL_BUF_PERF_STALL_RS
AR_RTR_0_2_COLBUF_PERF_STALL_RS:VC_PTR
AR_RTR_0_2_INQ_PRF_INCOMING_FLIT_VC0
AR_RTR_0_2_INQ_PRF_INCOMING_FLIT_VC1
AR_RTR_0_2_INQ_PRF_INCOMING_FLIT_VC2
AR_RTR_0_2_INQ_PRF_INCOMING_FLIT_VC3
AR_RTR_0_2_INQ_PRF_INCOMING_FLIT_VC4
AR_RTR_0_2_INQ_PRF_INCOMING_FLIT_VC5
AR_RTR_0_2_INQ_PRF_INCOMING_FLIT_VC6
AR_RTR_0_2_INQ_PRF_INCOMING_FLIT_VC7
AR_RTR_0_2_INQ_PRF_INCOMING_PKT_VC0_FILTER_FLIT0_CNT
AR_RTR_0_2_INQ_PRF_INCOMING_PKT_VC1_FILTER_FLIT1_CNT
AR_RTR_0_2_INQ_PRF_INCOMING_PKT_VC2_FILTER_FLIT2_CNT
AR_RTR_0_2_INQ_PRF_INCOMING_PKT_VC3_FILTER_FLIT3_CNT
AR_RTR_0_2_INQ_PRF_INCOMING_PKT_VC4_FILTER_FLIT4_CNT
AR_RTR_0_2_INQ_PRF_INCOMING_PKT_VC5_FILTER_FLIT5_CNT
AR_RTR_0_2_INQ_PRF_INCOMING_PKT_VC6_FILTER_FLIT6_CNT
AR_RTR_0_2_INQ_PRF_INCOMING_PKT_VC7_FILTER_FLIT7_CNT
AR_RTR_0_2_INQ_PRF_MATCH_FLIT_3_TO_0_FILTERING_CNT
AR_RTR_0_2_INQ_PRF_MATCH_FLIT_7_TO_4_FILTERING_CNT
AR_RTR_0_2_INQ_PRF_PKT_TO_DEAD_LINK_CNT
AR_RTR_0_2_INQ_PRF_ROWBUS_2X_USAGE_CNT
AR_RTR_0_2_INQ_PRF_ROWBUS_STALL_CNT
AR_RTR_0_3_COLBUF_PERF_STALL_RQ:COL_BUF_PERF_STALL_RQ
AR_RTR_0_3_COLBUF_PERF_STALL_RQ:VC_PTR
AR_RTR_0_3_COLBUF_PERF_STALL_RS:COL_BUF_PERF_STALL_RS
AR_RTR_0_3_COLBUF_PERF_STALL_RS:VC_PTR
AR_RTR_0_3_INQ_PRF_INCOMING_FLIT_VC0
AR_RTR_0_3_INQ_PRF_INCOMING_FLIT_VC1
AR_RTR_0_3_INQ_PRF_INCOMING_FLIT_VC2
AR_RTR_0_3_INQ_PRF_INCOMING_FLIT_VC3
AR_RTR_0_3_INQ_PRF_INCOMING_FLIT_VC4
AR_RTR_0_3_INQ_PRF_INCOMING_FLIT_VC5
AR_RTR_0_3_INQ_PRF_INCOMING_FLIT_VC6
AR_RTR_0_3_INQ_PRF_INCOMING_FLIT_VC7
AR_RTR_0_3_INQ_PRF_INCOMING_PKT_VC0_FILTER_FLIT0_CNT
AR_RTR_0_3_INQ_PRF_INCOMING_PKT_VC1_FILTER_FLIT1_CNT
AR_RTR_0_3_INQ_PRF_INCOMING_PKT_VC2_FILTER_FLIT2_CNT
AR_RTR_0_3_INQ_PRF_INCOMING_PKT_VC3_FILTER_FLIT3_CNT
AR_RTR_0_3_INQ_PRF_INCOMING_PKT_VC4_FILTER_FLIT4_CNT
AR_RTR_0_3_INQ_PRF_INCOMING_PKT_VC5_FILTER_FLIT5_CNT
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AR\_RTR\_0\_3\_INQ\_PRF\_INCOMING\_PKT\_VC6\_FILTER\_FLIT6\_CNT  
AR\_RTR\_0\_3\_INQ\_PRF\_INCOMING\_PKT\_VC7\_FILTER\_FLIT7\_CNT  
AR\_RTR\_0\_3\_INQ\_PRF\_MATCH\_FLIT\_3\_TO\_0\_FILTERING\_CNT  
AR\_RTR\_0\_3\_INQ\_PRF\_MATCH\_FLIT\_7\_TO\_4\_FILTERING\_CNT  
AR\_RTR\_0\_3\_INQ\_PRF\_PKT\_TO\_DEAD\_LINK\_CNT  
AR\_RTR\_0\_3\_INQ\_PRF\_ROWBUS\_2X\_USAGE\_CNT  
AR\_RTR\_0\_3\_INQ\_PRF\_ROWBUS\_STALL\_CNT  
AR\_RTR\_0\_4\_COLBUF\_PERF\_STALL\_RQ:COL\_BUF\_PERF\_STALL\_RQ  
AR\_RTR\_0\_4\_COLBUF\_PERF\_STALL\_RQ:VC\_PTR  
AR\_RTR\_0\_4\_COLBUF\_PERF\_STALL\_RS:COL\_BUF\_PERF\_STALL\_RS  
AR\_RTR\_0\_4\_COLBUF\_PERF\_STALL\_RS:VC\_PTR  
AR\_RTR\_0\_4\_INQ\_PRF\_INCOMING\_FLIT\_VC0  
AR\_RTR\_0\_4\_INQ\_PRF\_INCOMING\_FLIT\_VC1  
AR\_RTR\_0\_4\_INQ\_PRF\_INCOMING\_FLIT\_VC2  
AR\_RTR\_0\_4\_INQ\_PRF\_INCOMING\_FLIT\_VC3  
AR\_RTR\_0\_4\_INQ\_PRF\_INCOMING\_FLIT\_VC4  
AR\_RTR\_0\_4\_INQ\_PRF\_INCOMING\_FLIT\_VC5  
AR\_RTR\_0\_4\_INQ\_PRF\_INCOMING\_FLIT\_VC6  
AR\_RTR\_0\_4\_INQ\_PRF\_INCOMING\_FLIT\_VC7  
AR\_RTR\_0\_4\_INQ\_PRF\_INCOMING\_PKT\_VC0\_FILTER\_FLIT0\_CNT  
AR\_RTR\_0\_4\_INQ\_PRF\_INCOMING\_PKT\_VC1\_FILTER\_FLIT1\_CNT  
AR\_RTR\_0\_4\_INQ\_PRF\_INCOMING\_PKT\_VC2\_FILTER\_FLIT2\_CNT  
AR\_RTR\_0\_4\_INQ\_PRF\_INCOMING\_PKT\_VC3\_FILTER\_FLIT3\_CNT  
AR\_RTR\_0\_4\_INQ\_PRF\_INCOMING\_PKT\_VC4\_FILTER\_FLIT4\_CNT  
AR\_RTR\_0\_4\_INQ\_PRF\_INCOMING\_PKT\_VC5\_FILTER\_FLIT5\_CNT  
AR\_RTR\_0\_4\_INQ\_PRF\_INCOMING\_PKT\_VC6\_FILTER\_FLIT6\_CNT  
AR\_RTR\_0\_4\_INQ\_PRF\_INCOMING\_PKT\_VC7\_FILTER\_FLIT7\_CNT  
AR\_RTR\_0\_4\_INQ\_PRF\_MATCH\_FLIT\_3\_TO\_0\_FILTERING\_CNT  
AR\_RTR\_0\_4\_INQ\_PRF\_MATCH\_FLIT\_7\_TO\_4\_FILTERING\_CNT  
AR\_RTR\_0\_4\_INQ\_PRF\_PKT\_TO\_DEAD\_LINK\_CNT  
AR\_RTR\_0\_4\_INQ\_PRF\_ROWBUS\_2X\_USAGE\_CNT  
AR\_RTR\_0\_4\_INQ\_PRF\_ROWBUS\_STALL\_CNT  
AR\_RTR\_0\_5\_COLBUF\_PERF\_STALL\_RQ:COL\_BUF\_PERF\_STALL\_RQ  
AR\_RTR\_0\_5\_COLBUF\_PERF\_STALL\_RQ:VC\_PTR  
AR\_RTR\_0\_5\_COLBUF\_PERF\_STALL\_RS:COL\_BUF\_PERF\_STALL\_RS  
AR\_RTR\_0\_5\_COLBUF\_PERF\_STALL\_RS:VC\_PTR  
AR\_RTR\_0\_5\_INQ\_PRF\_INCOMING\_FLIT\_VC0  
AR\_RTR\_0\_5\_INQ\_PRF\_INCOMING\_FLIT\_VC1  
AR\_RTR\_0\_5\_INQ\_PRF\_INCOMING\_FLIT\_VC2  
AR\_RTR\_0\_5\_INQ\_PRF\_INCOMING\_FLIT\_VC3  
AR\_RTR\_0\_5\_INQ\_PRF\_INCOMING\_FLIT\_VC4  
AR\_RTR\_0\_5\_INQ\_PRF\_INCOMING\_FLIT\_VC5  
AR\_RTR\_0\_5\_INQ\_PRF\_INCOMING\_FLIT\_VC6  
AR\_RTR\_0\_5\_INQ\_PRF\_INCOMING\_FLIT\_VC7  
AR\_RTR\_0\_5\_INQ\_PRF\_INCOMING\_PKT\_VC0\_FILTER\_FLIT0\_CNT  
AR\_RTR\_0\_5\_INQ\_PRF\_INCOMING\_PKT\_VC1\_FILTER\_FLIT1\_CNT  
AR\_RTR\_0\_5\_INQ\_PRF\_INCOMING\_PKT\_VC2\_FILTER\_FLIT2\_CNT  
AR\_RTR\_0\_5\_INQ\_PRF\_INCOMING\_PKT\_VC3\_FILTER\_FLIT3\_CNT  
AR\_RTR\_0\_5\_INQ\_PRF\_INCOMING\_PKT\_VC4\_FILTER\_FLIT4\_CNT  
AR\_RTR\_0\_5\_INQ\_PRF\_INCOMING\_PKT\_VC5\_FILTER\_FLIT5\_CNT  
AR\_RTR\_0\_5\_INQ\_PRF\_INCOMING\_PKT\_VC6\_FILTER\_FLIT6\_CNT  
AR\_RTR\_0\_5\_INQ\_PRF\_INCOMING\_PKT\_VC7\_FILTER\_FLIT7\_CNT  
AR\_RTR\_0\_5\_INQ\_PRF\_MATCH\_FLIT\_3\_TO\_0\_FILTERING\_CNT  
AR\_RTR\_0\_5\_INQ\_PRF\_MATCH\_FLIT\_7\_TO\_4\_FILTERING\_CNT  
AR\_RTR\_0\_5\_INQ\_PRF\_PKT\_TO\_DEAD\_LINK\_CNT  
AR\_RTR\_0\_5\_INQ\_PRF\_ROWBUS\_2X\_USAGE\_CNT  
AR\_RTR\_0\_5\_INQ\_PRF\_ROWBUS\_STALL\_CNT  
AR\_RTR\_0\_6\_COLBUF\_PERF\_STALL\_RQ:COL\_BUF\_PERF\_STALL\_RQ

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AR_RTR_0_6_COLBUF_PERF_STALL_RQ:VC_PTR
AR_RTR_0_6_COLBUF_PERF_STALL_RS:COL_BUF_PERF_STALL_RS
AR_RTR_0_6_COLBUF_PERF_STALL_RS:VC_PTR
AR_RTR_0_6_INQ_PRF_INCOMING_FLIT_VC0
AR_RTR_0_6_INQ_PRF_INCOMING_FLIT_VC1
AR_RTR_0_6_INQ_PRF_INCOMING_FLIT_VC2
AR_RTR_0_6_INQ_PRF_INCOMING_FLIT_VC3
AR_RTR_0_6_INQ_PRF_INCOMING_FLIT_VC4
AR_RTR_0_6_INQ_PRF_INCOMING_FLIT_VC5
AR_RTR_0_6_INQ_PRF_INCOMING_FLIT_VC6
AR_RTR_0_6_INQ_PRF_INCOMING_FLIT_VC7
AR_RTR_0_6_INQ_PRF_INCOMING_PKT_VC0_FILTER_FLIT0_CNT
AR_RTR_0_6_INQ_PRF_INCOMING_PKT_VC1_FILTER_FLIT1_CNT
AR_RTR_0_6_INQ_PRF_INCOMING_PKT_VC2_FILTER_FLIT2_CNT
AR_RTR_0_6_INQ_PRF_INCOMING_PKT_VC3_FILTER_FLIT3_CNT
AR_RTR_0_6_INQ_PRF_INCOMING_PKT_VC4_FILTER_FLIT4_CNT
AR_RTR_0_6_INQ_PRF_INCOMING_PKT_VC5_FILTER_FLIT5_CNT
AR_RTR_0_6_INQ_PRF_INCOMING_PKT_VC6_FILTER_FLIT6_CNT
AR_RTR_0_6_INQ_PRF_INCOMING_PKT_VC7_FILTER_FLIT7_CNT
AR_RTR_0_6_INQ_PRF_MATCH_FLIT_3_TO_0_FILTERING_CNT
AR_RTR_0_6_INQ_PRF_MATCH_FLIT_7_TO_4_FILTERING_CNT
AR_RTR_0_6_INQ_PRF_PKT_TO_DEAD_LINK_CNT
AR_RTR_0_6_INQ_PRF_ROWBUS_2X_USAGE_CNT
AR_RTR_0_6_INQ_PRF_ROWBUS_STALL_CNT
AR_RTR_0_7_COLBUF_PERF_STALL_RQ:COL_BUF_PERF_STALL_RQ
AR_RTR_0_7_COLBUF_PERF_STALL_RQ:VC_PTR
AR_RTR_0_7_COLBUF_PERF_STALL_RS:COL_BUF_PERF_STALL_RS
AR_RTR_0_7_COLBUF_PERF_STALL_RS:VC_PTR
AR_RTR_0_7_INQ_PRF_INCOMING_FLIT_VC0
AR_RTR_0_7_INQ_PRF_INCOMING_FLIT_VC1
AR_RTR_0_7_INQ_PRF_INCOMING_FLIT_VC2
AR_RTR_0_7_INQ_PRF_INCOMING_FLIT_VC3
AR_RTR_0_7_INQ_PRF_INCOMING_FLIT_VC4
AR_RTR_0_7_INQ_PRF_INCOMING_FLIT_VC5
AR_RTR_0_7_INQ_PRF_INCOMING_FLIT_VC6
AR_RTR_0_7_INQ_PRF_INCOMING_FLIT_VC7
AR_RTR_0_7_INQ_PRF_INCOMING_PKT_VC0_FILTER_FLIT0_CNT
AR_RTR_0_7_INQ_PRF_INCOMING_PKT_VC1_FILTER_FLIT1_CNT
AR_RTR_0_7_INQ_PRF_INCOMING_PKT_VC2_FILTER_FLIT2_CNT
AR_RTR_0_7_INQ_PRF_INCOMING_PKT_VC3_FILTER_FLIT3_CNT
AR_RTR_0_7_INQ_PRF_INCOMING_PKT_VC4_FILTER_FLIT4_CNT
AR_RTR_0_7_INQ_PRF_INCOMING_PKT_VC5_FILTER_FLIT5_CNT
AR_RTR_0_7_INQ_PRF_INCOMING_PKT_VC6_FILTER_FLIT6_CNT
AR_RTR_0_7_INQ_PRF_INCOMING_PKT_VC7_FILTER_FLIT7_CNT
AR_RTR_0_7_INQ_PRF_MATCH_FLIT_3_TO_0_FILTERING_CNT
AR_RTR_0_7_INQ_PRF_MATCH_FLIT_7_TO_4_FILTERING_CNT
AR_RTR_0_7_INQ_PRF_PKT_TO_DEAD_LINK_CNT
AR_RTR_0_7_INQ_PRF_ROWBUS_2X_USAGE_CNT
AR_RTR_0_7_INQ_PRF_ROWBUS_STALL_CNT
AR_RTR_1_0_COLBUF_PERF_STALL_RQ:COL_BUF_PERF_STALL_RQ
AR_RTR_1_0_COLBUF_PERF_STALL_RQ:VC_PTR
AR_RTR_1_0_COLBUF_PERF_STALL_RS:COL_BUF_PERF_STALL_RS
AR_RTR_1_0_COLBUF_PERF_STALL_RS:VC_PTR
AR_RTR_1_0_INQ_PRF_INCOMING_FLIT_VC0
AR_RTR_1_0_INQ_PRF_INCOMING_FLIT_VC1
AR_RTR_1_0_INQ_PRF_INCOMING_FLIT_VC2
AR_RTR_1_0_INQ_PRF_INCOMING_FLIT_VC3
AR_RTR_1_0_INQ_PRF_INCOMING_FLIT_VC4
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AR_RTR_1_0_INQ_PRF_INCOMING_FLIT_VC5
AR_RTR_1_0_INQ_PRF_INCOMING_FLIT_VC6
AR_RTR_1_0_INQ_PRF_INCOMING_FLIT_VC7
AR_RTR_1_0_INQ_PRF_INCOMING_PKT_VC0_FILTER_FLIT0_CNT
AR_RTR_1_0_INQ_PRF_INCOMING_PKT_VC1_FILTER_FLIT1_CNT
AR_RTR_1_0_INQ_PRF_INCOMING_PKT_VC2_FILTER_FLIT2_CNT
AR_RTR_1_0_INQ_PRF_INCOMING_PKT_VC3_FILTER_FLIT3_CNT
AR_RTR_1_0_INQ_PRF_INCOMING_PKT_VC4_FILTER_FLIT4_CNT
AR_RTR_1_0_INQ_PRF_INCOMING_PKT_VC5_FILTER_FLIT5_CNT
AR_RTR_1_0_INQ_PRF_INCOMING_PKT_VC6_FILTER_FLIT6_CNT
AR_RTR_1_0_INQ_PRF_INCOMING_PKT_VC7_FILTER_FLIT7_CNT
AR_RTR_1_0_INQ_PRF_MATCH_FLIT_3_TO_0_FILTERING_CNT
AR_RTR_1_0_INQ_PRF_MATCH_FLIT_7_TO_4_FILTERING_CNT
AR_RTR_1_0_INQ_PRF_PKT_TO_DEAD_LINK_CNT
AR_RTR_1_0_INQ_PRF_ROWBUS_2X_USAGE_CNT
AR_RTR_1_0_INQ_PRF_ROWBUS_STALL_CNT
AR_RTR_1_1_COLBUF_PERF_STALL_RQ:COL_BUF_PERF_STALL_RQ
AR_RTR_1_1_COLBUF_PERF_STALL_RQ:VC_PTR
AR_RTR_1_1_COLBUF_PERF_STALL_RS:COL_BUF_PERF_STALL_RS
AR_RTR_1_1_COLBUF_PERF_STALL_RS:VC_PTR
AR_RTR_1_1_INQ_PRF_INCOMING_FLIT_VC0
AR_RTR_1_1_INQ_PRF_INCOMING_FLIT_VC1
AR_RTR_1_1_INQ_PRF_INCOMING_FLIT_VC2
AR_RTR_1_1_INQ_PRF_INCOMING_FLIT_VC3
AR_RTR_1_1_INQ_PRF_INCOMING_FLIT_VC4
AR_RTR_1_1_INQ_PRF_INCOMING_FLIT_VC5
AR_RTR_1_1_INQ_PRF_INCOMING_FLIT_VC6
AR_RTR_1_1_INQ_PRF_INCOMING_FLIT_VC7
AR_RTR_1_1_INQ_PRF_INCOMING_PKT_VC0_FILTER_FLIT0_CNT
AR_RTR_1_1_INQ_PRF_INCOMING_PKT_VC1_FILTER_FLIT1_CNT
AR_RTR_1_1_INQ_PRF_INCOMING_PKT_VC2_FILTER_FLIT2_CNT
AR_RTR_1_1_INQ_PRF_INCOMING_PKT_VC3_FILTER_FLIT3_CNT
AR_RTR_1_1_INQ_PRF_INCOMING_PKT_VC4_FILTER_FLIT4_CNT
AR_RTR_1_1_INQ_PRF_INCOMING_PKT_VC5_FILTER_FLIT5_CNT
AR_RTR_1_1_INQ_PRF_INCOMING_PKT_VC6_FILTER_FLIT6_CNT
AR_RTR_1_1_INQ_PRF_INCOMING_PKT_VC7_FILTER_FLIT7_CNT
AR_RTR_1_1_INQ_PRF_MATCH_FLIT_3_TO_0_FILTERING_CNT
AR_RTR_1_1_INQ_PRF_MATCH_FLIT_7_TO_4_FILTERING_CNT
AR_RTR_1_1_INQ_PRF_PKT_TO_DEAD_LINK_CNT
AR_RTR_1_1_INQ_PRF_ROWBUS_2X_USAGE_CNT
AR_RTR_1_1_INQ_PRF_ROWBUS_STALL_CNT
AR_RTR_1_2_COLBUF_PERF_STALL_RQ:COL_BUF_PERF_STALL_RQ
AR_RTR_1_2_COLBUF_PERF_STALL_RQ:VC_PTR
AR_RTR_1_2_COLBUF_PERF_STALL_RS:COL_BUF_PERF_STALL_RS
AR_RTR_1_2_COLBUF_PERF_STALL_RS:VC_PTR
AR_RTR_1_2_INQ_PRF_INCOMING_FLIT_VC0
AR_RTR_1_2_INQ_PRF_INCOMING_FLIT_VC1
AR_RTR_1_2_INQ_PRF_INCOMING_FLIT_VC2
AR_RTR_1_2_INQ_PRF_INCOMING_FLIT_VC3
AR_RTR_1_2_INQ_PRF_INCOMING_FLIT_VC4
AR_RTR_1_2_INQ_PRF_INCOMING_FLIT_VC5
AR_RTR_1_2_INQ_PRF_INCOMING_FLIT_VC6
AR_RTR_1_2_INQ_PRF_INCOMING_FLIT_VC7
AR_RTR_1_2_INQ_PRF_INCOMING_PKT_VC0_FILTER_FLIT0_CNT
AR_RTR_1_2_INQ_PRF_INCOMING_PKT_VC1_FILTER_FLIT1_CNT
AR_RTR_1_2_INQ_PRF_INCOMING_PKT_VC2_FILTER_FLIT2_CNT
AR_RTR_1_2_INQ_PRF_INCOMING_PKT_VC3_FILTER_FLIT3_CNT
AR_RTR_1_2_INQ_PRF_INCOMING_PKT_VC4_FILTER_FLIT4_CNT
```

AR\_RTR\_1\_2\_INQ\_PRF\_INCOMING\_PKT\_VC5\_FILTER\_FLIT5\_CNT  
AR\_RTR\_1\_2\_INQ\_PRF\_INCOMING\_PKT\_VC6\_FILTER\_FLIT6\_CNT  
AR\_RTR\_1\_2\_INQ\_PRF\_INCOMING\_PKT\_VC7\_FILTER\_FLIT7\_CNT  
AR\_RTR\_1\_2\_INQ\_PRF\_MATCH\_FLIT\_3\_TO\_0\_FILTERING\_CNT  
AR\_RTR\_1\_2\_INQ\_PRF\_MATCH\_FLIT\_7\_TO\_4\_FILTERING\_CNT  
AR\_RTR\_1\_2\_INQ\_PRF\_PKT\_TO\_DEAD\_LINK\_CNT  
AR\_RTR\_1\_2\_INQ\_PRF\_ROWBUS\_2X\_USAGE\_CNT  
AR\_RTR\_1\_2\_INQ\_PRF\_ROWBUS\_STALL\_CNT  
AR\_RTR\_1\_3\_COLBUF\_PERF\_STALL\_RQ:COL\_BUF\_PERF\_STALL\_RQ  
AR\_RTR\_1\_3\_COLBUF\_PERF\_STALL\_RQ:VC\_PTR  
AR\_RTR\_1\_3\_COLBUF\_PERF\_STALL\_RS:COL\_BUF\_PERF\_STALL\_RS  
AR\_RTR\_1\_3\_COLBUF\_PERF\_STALL\_RS:VC\_PTR  
AR\_RTR\_1\_3\_INQ\_PRF\_INCOMING\_FLIT\_VC0  
AR\_RTR\_1\_3\_INQ\_PRF\_INCOMING\_FLIT\_VC1  
AR\_RTR\_1\_3\_INQ\_PRF\_INCOMING\_FLIT\_VC2  
AR\_RTR\_1\_3\_INQ\_PRF\_INCOMING\_FLIT\_VC3  
AR\_RTR\_1\_3\_INQ\_PRF\_INCOMING\_FLIT\_VC4  
AR\_RTR\_1\_3\_INQ\_PRF\_INCOMING\_FLIT\_VC5  
AR\_RTR\_1\_3\_INQ\_PRF\_INCOMING\_FLIT\_VC6  
AR\_RTR\_1\_3\_INQ\_PRF\_INCOMING\_FLIT\_VC7  
AR\_RTR\_1\_3\_INQ\_PRF\_INCOMING\_PKT\_VC0\_FILTER\_FLIT0\_CNT  
AR\_RTR\_1\_3\_INQ\_PRF\_INCOMING\_PKT\_VC1\_FILTER\_FLIT1\_CNT  
AR\_RTR\_1\_3\_INQ\_PRF\_INCOMING\_PKT\_VC2\_FILTER\_FLIT2\_CNT  
AR\_RTR\_1\_3\_INQ\_PRF\_INCOMING\_PKT\_VC3\_FILTER\_FLIT3\_CNT  
AR\_RTR\_1\_3\_INQ\_PRF\_INCOMING\_PKT\_VC4\_FILTER\_FLIT4\_CNT  
AR\_RTR\_1\_3\_INQ\_PRF\_INCOMING\_PKT\_VC5\_FILTER\_FLIT5\_CNT  
AR\_RTR\_1\_3\_INQ\_PRF\_INCOMING\_PKT\_VC6\_FILTER\_FLIT6\_CNT  
AR\_RTR\_1\_3\_INQ\_PRF\_INCOMING\_PKT\_VC7\_FILTER\_FLIT7\_CNT  
AR\_RTR\_1\_3\_INQ\_PRF\_MATCH\_FLIT\_3\_TO\_0\_FILTERING\_CNT  
AR\_RTR\_1\_3\_INQ\_PRF\_MATCH\_FLIT\_7\_TO\_4\_FILTERING\_CNT  
AR\_RTR\_1\_3\_INQ\_PRF\_PKT\_TO\_DEAD\_LINK\_CNT  
AR\_RTR\_1\_3\_INQ\_PRF\_ROWBUS\_2X\_USAGE\_CNT  
AR\_RTR\_1\_3\_INQ\_PRF\_ROWBUS\_STALL\_CNT  
AR\_RTR\_1\_4\_COLBUF\_PERF\_STALL\_RQ:COL\_BUF\_PERF\_STALL\_RQ  
AR\_RTR\_1\_4\_COLBUF\_PERF\_STALL\_RQ:VC\_PTR  
AR\_RTR\_1\_4\_COLBUF\_PERF\_STALL\_RS:COL\_BUF\_PERF\_STALL\_RS  
AR\_RTR\_1\_4\_COLBUF\_PERF\_STALL\_RS:VC\_PTR  
AR\_RTR\_1\_4\_INQ\_PRF\_INCOMING\_FLIT\_VC0  
AR\_RTR\_1\_4\_INQ\_PRF\_INCOMING\_FLIT\_VC1  
AR\_RTR\_1\_4\_INQ\_PRF\_INCOMING\_FLIT\_VC2  
AR\_RTR\_1\_4\_INQ\_PRF\_INCOMING\_FLIT\_VC3  
AR\_RTR\_1\_4\_INQ\_PRF\_INCOMING\_FLIT\_VC4  
AR\_RTR\_1\_4\_INQ\_PRF\_INCOMING\_FLIT\_VC5  
AR\_RTR\_1\_4\_INQ\_PRF\_INCOMING\_FLIT\_VC6  
AR\_RTR\_1\_4\_INQ\_PRF\_INCOMING\_FLIT\_VC7  
AR\_RTR\_1\_4\_INQ\_PRF\_INCOMING\_PKT\_VC0\_FILTER\_FLIT0\_CNT  
AR\_RTR\_1\_4\_INQ\_PRF\_INCOMING\_PKT\_VC1\_FILTER\_FLIT1\_CNT  
AR\_RTR\_1\_4\_INQ\_PRF\_INCOMING\_PKT\_VC2\_FILTER\_FLIT2\_CNT  
AR\_RTR\_1\_4\_INQ\_PRF\_INCOMING\_PKT\_VC3\_FILTER\_FLIT3\_CNT  
AR\_RTR\_1\_4\_INQ\_PRF\_INCOMING\_PKT\_VC4\_FILTER\_FLIT4\_CNT  
AR\_RTR\_1\_4\_INQ\_PRF\_INCOMING\_PKT\_VC5\_FILTER\_FLIT5\_CNT  
AR\_RTR\_1\_4\_INQ\_PRF\_INCOMING\_PKT\_VC6\_FILTER\_FLIT6\_CNT  
AR\_RTR\_1\_4\_INQ\_PRF\_INCOMING\_PKT\_VC7\_FILTER\_FLIT7\_CNT  
AR\_RTR\_1\_4\_INQ\_PRF\_MATCH\_FLIT\_3\_TO\_0\_FILTERING\_CNT  
AR\_RTR\_1\_4\_INQ\_PRF\_MATCH\_FLIT\_7\_TO\_4\_FILTERING\_CNT  
AR\_RTR\_1\_4\_INQ\_PRF\_PKT\_TO\_DEAD\_LINK\_CNT  
AR\_RTR\_1\_4\_INQ\_PRF\_ROWBUS\_2X\_USAGE\_CNT  
AR\_RTR\_1\_4\_INQ\_PRF\_ROWBUS\_STALL\_CNT

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AR_RTR_1_5_COLBUF_PERF_STALL_RQ:COL_BUF_PERF_STALL_RQ
AR_RTR_1_5_COLBUF_PERF_STALL_RQ:VC_PTR
AR_RTR_1_5_COLBUF_PERF_STALL_RS:COL_BUF_PERF_STALL_RS
AR_RTR_1_5_COLBUF_PERF_STALL_RS:VC_PTR
AR_RTR_1_5_INQ_PRF_INCOMING_FLIT_VC0
AR_RTR_1_5_INQ_PRF_INCOMING_FLIT_VC1
AR_RTR_1_5_INQ_PRF_INCOMING_FLIT_VC2
AR_RTR_1_5_INQ_PRF_INCOMING_FLIT_VC3
AR_RTR_1_5_INQ_PRF_INCOMING_FLIT_VC4
AR_RTR_1_5_INQ_PRF_INCOMING_FLIT_VC5
AR_RTR_1_5_INQ_PRF_INCOMING_FLIT_VC6
AR_RTR_1_5_INQ_PRF_INCOMING_FLIT_VC7
AR_RTR_1_5_INQ_PRF_INCOMING_PKT_VC0_FILTER_FLIT0_CNT
AR_RTR_1_5_INQ_PRF_INCOMING_PKT_VC1_FILTER_FLIT1_CNT
AR_RTR_1_5_INQ_PRF_INCOMING_PKT_VC2_FILTER_FLIT2_CNT
AR_RTR_1_5_INQ_PRF_INCOMING_PKT_VC3_FILTER_FLIT3_CNT
AR_RTR_1_5_INQ_PRF_INCOMING_PKT_VC4_FILTER_FLIT4_CNT
AR_RTR_1_5_INQ_PRF_INCOMING_PKT_VC5_FILTER_FLIT5_CNT
AR_RTR_1_5_INQ_PRF_INCOMING_PKT_VC6_FILTER_FLIT6_CNT
AR_RTR_1_5_INQ_PRF_INCOMING_PKT_VC7_FILTER_FLIT7_CNT
AR_RTR_1_5_INQ_PRF_MATCH_FLIT_3_TO_0_FILTERING_CNT
AR_RTR_1_5_INQ_PRF_MATCH_FLIT_7_TO_4_FILTERING_CNT
AR_RTR_1_5_INQ_PRF_PKT_TO_DEAD_LINK_CNT
AR_RTR_1_5_INQ_PRF_ROWBUS_2X_USAGE_CNT
AR_RTR_1_5_INQ_PRF_ROWBUS_STALL_CNT
AR_RTR_1_6_COLBUF_PERF_STALL_RQ:COL_BUF_PERF_STALL_RQ
AR_RTR_1_6_COLBUF_PERF_STALL_RQ:VC_PTR
AR_RTR_1_6_COLBUF_PERF_STALL_RS:COL_BUF_PERF_STALL_RS
AR_RTR_1_6_COLBUF_PERF_STALL_RS:VC_PTR
AR_RTR_1_6_INQ_PRF_INCOMING_FLIT_VC0
AR_RTR_1_6_INQ_PRF_INCOMING_FLIT_VC1
AR_RTR_1_6_INQ_PRF_INCOMING_FLIT_VC2
AR_RTR_1_6_INQ_PRF_INCOMING_FLIT_VC3
AR_RTR_1_6_INQ_PRF_INCOMING_FLIT_VC4
AR_RTR_1_6_INQ_PRF_INCOMING_FLIT_VC5
AR_RTR_1_6_INQ_PRF_INCOMING_FLIT_VC6
AR_RTR_1_6_INQ_PRF_INCOMING_FLIT_VC7
AR_RTR_1_6_INQ_PRF_INCOMING_PKT_VC0_FILTER_FLIT0_CNT
AR_RTR_1_6_INQ_PRF_INCOMING_PKT_VC1_FILTER_FLIT1_CNT
AR_RTR_1_6_INQ_PRF_INCOMING_PKT_VC2_FILTER_FLIT2_CNT
AR_RTR_1_6_INQ_PRF_INCOMING_PKT_VC3_FILTER_FLIT3_CNT
AR_RTR_1_6_INQ_PRF_INCOMING_PKT_VC4_FILTER_FLIT4_CNT
AR_RTR_1_6_INQ_PRF_INCOMING_PKT_VC5_FILTER_FLIT5_CNT
AR_RTR_1_6_INQ_PRF_INCOMING_PKT_VC6_FILTER_FLIT6_CNT
AR_RTR_1_6_INQ_PRF_INCOMING_PKT_VC7_FILTER_FLIT7_CNT
AR_RTR_1_6_INQ_PRF_MATCH_FLIT_3_TO_0_FILTERING_CNT
AR_RTR_1_6_INQ_PRF_MATCH_FLIT_7_TO_4_FILTERING_CNT
AR_RTR_1_6_INQ_PRF_PKT_TO_DEAD_LINK_CNT
AR_RTR_1_6_INQ_PRF_ROWBUS_2X_USAGE_CNT
AR_RTR_1_6_INQ_PRF_ROWBUS_STALL_CNT
AR_RTR_1_7_COLBUF_PERF_STALL_RQ:COL_BUF_PERF_STALL_RQ
AR_RTR_1_7_COLBUF_PERF_STALL_RQ:VC_PTR
AR_RTR_1_7_COLBUF_PERF_STALL_RS:COL_BUF_PERF_STALL_RS
AR_RTR_1_7_COLBUF_PERF_STALL_RS:VC_PTR
AR_RTR_1_7_INQ_PRF_INCOMING_FLIT_VC0
AR_RTR_1_7_INQ_PRF_INCOMING_FLIT_VC1
AR_RTR_1_7_INQ_PRF_INCOMING_FLIT_VC2
AR_RTR_1_7_INQ_PRF_INCOMING_FLIT_VC3
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AR_RTR_1_7_INQ_PRF_INCOMING_FLIT_VC4
AR_RTR_1_7_INQ_PRF_INCOMING_FLIT_VC5
AR_RTR_1_7_INQ_PRF_INCOMING_FLIT_VC6
AR_RTR_1_7_INQ_PRF_INCOMING_FLIT_VC7
AR_RTR_1_7_INQ_PRF_INCOMING_PKT_VC0_FILTER_FLIT0_CNT
AR_RTR_1_7_INQ_PRF_INCOMING_PKT_VC1_FILTER_FLIT1_CNT
AR_RTR_1_7_INQ_PRF_INCOMING_PKT_VC2_FILTER_FLIT2_CNT
AR_RTR_1_7_INQ_PRF_INCOMING_PKT_VC3_FILTER_FLIT3_CNT
AR_RTR_1_7_INQ_PRF_INCOMING_PKT_VC4_FILTER_FLIT4_CNT
AR_RTR_1_7_INQ_PRF_INCOMING_PKT_VC5_FILTER_FLIT5_CNT
AR_RTR_1_7_INQ_PRF_INCOMING_PKT_VC6_FILTER_FLIT6_CNT
AR_RTR_1_7_INQ_PRF_INCOMING_PKT_VC7_FILTER_FLIT7_CNT
AR_RTR_1_7_INQ_PRF_MATCH_FLIT_3_TO_0_FILTERING_CNT
AR_RTR_1_7_INQ_PRF_MATCH_FLIT_7_TO_4_FILTERING_CNT
AR_RTR_1_7_INQ_PRF_PKT_TO_DEAD_LINK_CNT
AR_RTR_1_7_INQ_PRF_ROWBUS_2X_USAGE_CNT
AR_RTR_1_7_INQ_PRF_ROWBUS_STALL_CNT
AR_RTR_2_0_COLBUF_PERF_STALL_RQ:COL_BUF_PERF_STALL_RQ
AR_RTR_2_0_COLBUF_PERF_STALL_RQ:VC_PTR
AR_RTR_2_0_COLBUF_PERF_STALL_RS:COL_BUF_PERF_STALL_RS
AR_RTR_2_0_COLBUF_PERF_STALL_RS:VC_PTR
AR_RTR_2_0_INQ_PRF_INCOMING_FLIT_VC0
AR_RTR_2_0_INQ_PRF_INCOMING_FLIT_VC1
AR_RTR_2_0_INQ_PRF_INCOMING_FLIT_VC2
AR_RTR_2_0_INQ_PRF_INCOMING_FLIT_VC3
AR_RTR_2_0_INQ_PRF_INCOMING_FLIT_VC4
AR_RTR_2_0_INQ_PRF_INCOMING_FLIT_VC5
AR_RTR_2_0_INQ_PRF_INCOMING_FLIT_VC6
AR_RTR_2_0_INQ_PRF_INCOMING_FLIT_VC7
AR_RTR_2_0_INQ_PRF_INCOMING_PKT_VC0_FILTER_FLIT0_CNT
AR_RTR_2_0_INQ_PRF_INCOMING_PKT_VC1_FILTER_FLIT1_CNT
AR_RTR_2_0_INQ_PRF_INCOMING_PKT_VC2_FILTER_FLIT2_CNT
AR_RTR_2_0_INQ_PRF_INCOMING_PKT_VC3_FILTER_FLIT3_CNT
AR_RTR_2_0_INQ_PRF_INCOMING_PKT_VC4_FILTER_FLIT4_CNT
AR_RTR_2_0_INQ_PRF_INCOMING_PKT_VC5_FILTER_FLIT5_CNT
AR_RTR_2_0_INQ_PRF_INCOMING_PKT_VC6_FILTER_FLIT6_CNT
AR_RTR_2_0_INQ_PRF_INCOMING_PKT_VC7_FILTER_FLIT7_CNT
AR_RTR_2_0_INQ_PRF_MATCH_FLIT_3_TO_0_FILTERING_CNT
AR_RTR_2_0_INQ_PRF_MATCH_FLIT_7_TO_4_FILTERING_CNT
AR_RTR_2_0_INQ_PRF_PKT_TO_DEAD_LINK_CNT
AR_RTR_2_0_INQ_PRF_ROWBUS_2X_USAGE_CNT
AR_RTR_2_0_INQ_PRF_ROWBUS_STALL_CNT
AR_RTR_2_1_COLBUF_PERF_STALL_RQ:COL_BUF_PERF_STALL_RQ
AR_RTR_2_1_COLBUF_PERF_STALL_RQ:VC_PTR
AR_RTR_2_1_COLBUF_PERF_STALL_RS:COL_BUF_PERF_STALL_RS
AR_RTR_2_1_COLBUF_PERF_STALL_RS:VC_PTR
AR_RTR_2_1_INQ_PRF_INCOMING_FLIT_VC0
AR_RTR_2_1_INQ_PRF_INCOMING_FLIT_VC1
AR_RTR_2_1_INQ_PRF_INCOMING_FLIT_VC2
AR_RTR_2_1_INQ_PRF_INCOMING_FLIT_VC3
AR_RTR_2_1_INQ_PRF_INCOMING_FLIT_VC4
AR_RTR_2_1_INQ_PRF_INCOMING_FLIT_VC5
AR_RTR_2_1_INQ_PRF_INCOMING_FLIT_VC6
AR_RTR_2_1_INQ_PRF_INCOMING_FLIT_VC7
AR_RTR_2_1_INQ_PRF_INCOMING_PKT_VC0_FILTER_FLIT0_CNT
AR_RTR_2_1_INQ_PRF_INCOMING_PKT_VC1_FILTER_FLIT1_CNT
AR_RTR_2_1_INQ_PRF_INCOMING_PKT_VC2_FILTER_FLIT2_CNT
AR_RTR_2_1_INQ_PRF_INCOMING_PKT_VC3_FILTER_FLIT3_CNT
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AR_RTR_2_1_INQ_PRF_INCOMING_PKT_VC4_FILTER_FLIT4_CNT
AR_RTR_2_1_INQ_PRF_INCOMING_PKT_VC5_FILTER_FLIT5_CNT
AR_RTR_2_1_INQ_PRF_INCOMING_PKT_VC6_FILTER_FLIT6_CNT
AR_RTR_2_1_INQ_PRF_INCOMING_PKT_VC7_FILTER_FLIT7_CNT
AR_RTR_2_1_INQ_PRF_MATCH_FLIT_3_TO_0_FILTERING_CNT
AR_RTR_2_1_INQ_PRF_MATCH_FLIT_7_TO_4_FILTERING_CNT
AR_RTR_2_1_INQ_PRF_PKT_TO_DEAD_LINK_CNT
AR_RTR_2_1_INQ_PRF_ROWBUS_2X_USAGE_CNT
AR_RTR_2_1_INQ_PRF_ROWBUS_STALL_CNT
AR_RTR_2_2_COLBUF_PERF_STALL_RQ:COL_BUF_PERF_STALL_RQ
AR_RTR_2_2_COLBUF_PERF_STALL_RQ:VC_PTR
AR_RTR_2_2_COLBUF_PERF_STALL_RS:COL_BUF_PERF_STALL_RS
AR_RTR_2_2_COLBUF_PERF_STALL_RS:VC_PTR
AR_RTR_2_2_INQ_PRF_INCOMING_FLIT_VC0
AR_RTR_2_2_INQ_PRF_INCOMING_FLIT_VC1
AR_RTR_2_2_INQ_PRF_INCOMING_FLIT_VC2
AR_RTR_2_2_INQ_PRF_INCOMING_FLIT_VC3
AR_RTR_2_2_INQ_PRF_INCOMING_FLIT_VC4
AR_RTR_2_2_INQ_PRF_INCOMING_FLIT_VC5
AR_RTR_2_2_INQ_PRF_INCOMING_FLIT_VC6
AR_RTR_2_2_INQ_PRF_INCOMING_FLIT_VC7
AR_RTR_2_2_INQ_PRF_INCOMING_PKT_VC0_FILTER_FLIT0_CNT
AR_RTR_2_2_INQ_PRF_INCOMING_PKT_VC1_FILTER_FLIT1_CNT
AR_RTR_2_2_INQ_PRF_INCOMING_PKT_VC2_FILTER_FLIT2_CNT
AR_RTR_2_2_INQ_PRF_INCOMING_PKT_VC3_FILTER_FLIT3_CNT
AR_RTR_2_2_INQ_PRF_INCOMING_PKT_VC4_FILTER_FLIT4_CNT
AR_RTR_2_2_INQ_PRF_INCOMING_PKT_VC5_FILTER_FLIT5_CNT
AR_RTR_2_2_INQ_PRF_INCOMING_PKT_VC6_FILTER_FLIT6_CNT
AR_RTR_2_2_INQ_PRF_INCOMING_PKT_VC7_FILTER_FLIT7_CNT
AR_RTR_2_2_INQ_PRF_MATCH_FLIT_3_TO_0_FILTERING_CNT
AR_RTR_2_2_INQ_PRF_MATCH_FLIT_7_TO_4_FILTERING_CNT
AR_RTR_2_2_INQ_PRF_PKT_TO_DEAD_LINK_CNT
AR_RTR_2_2_INQ_PRF_ROWBUS_2X_USAGE_CNT
AR_RTR_2_2_INQ_PRF_ROWBUS_STALL_CNT
AR_RTR_2_3_COLBUF_PERF_STALL_RQ:COL_BUF_PERF_STALL_RQ
AR_RTR_2_3_COLBUF_PERF_STALL_RQ:VC_PTR
AR_RTR_2_3_COLBUF_PERF_STALL_RS:COL_BUF_PERF_STALL_RS
AR_RTR_2_3_COLBUF_PERF_STALL_RS:VC_PTR
AR_RTR_2_3_INQ_PRF_INCOMING_FLIT_VC0
AR_RTR_2_3_INQ_PRF_INCOMING_FLIT_VC1
AR_RTR_2_3_INQ_PRF_INCOMING_FLIT_VC2
AR_RTR_2_3_INQ_PRF_INCOMING_FLIT_VC3
AR_RTR_2_3_INQ_PRF_INCOMING_FLIT_VC4
AR_RTR_2_3_INQ_PRF_INCOMING_FLIT_VC5
AR_RTR_2_3_INQ_PRF_INCOMING_FLIT_VC6
AR_RTR_2_3_INQ_PRF_INCOMING_FLIT_VC7
AR_RTR_2_3_INQ_PRF_INCOMING_PKT_VC0_FILTER_FLIT0_CNT
AR_RTR_2_3_INQ_PRF_INCOMING_PKT_VC1_FILTER_FLIT1_CNT
AR_RTR_2_3_INQ_PRF_INCOMING_PKT_VC2_FILTER_FLIT2_CNT
AR_RTR_2_3_INQ_PRF_INCOMING_PKT_VC3_FILTER_FLIT3_CNT
AR_RTR_2_3_INQ_PRF_INCOMING_PKT_VC4_FILTER_FLIT4_CNT
AR_RTR_2_3_INQ_PRF_INCOMING_PKT_VC5_FILTER_FLIT5_CNT
AR_RTR_2_3_INQ_PRF_INCOMING_PKT_VC6_FILTER_FLIT6_CNT
AR_RTR_2_3_INQ_PRF_INCOMING_PKT_VC7_FILTER_FLIT7_CNT
AR_RTR_2_3_INQ_PRF_MATCH_FLIT_3_TO_0_FILTERING_CNT
AR_RTR_2_3_INQ_PRF_MATCH_FLIT_7_TO_4_FILTERING_CNT
AR_RTR_2_3_INQ_PRF_PKT_TO_DEAD_LINK_CNT
AR_RTR_2_3_INQ_PRF_ROWBUS_2X_USAGE_CNT
```

```
AR_RTR_2_3_INQ_PRF_ROWBUS_STALL_CNT
AR_RTR_2_4_COLBUF_PERF_STALL_RQ:COL_BUF_PERF_STALL_RQ
AR_RTR_2_4_COLBUF_PERF_STALL_RQ:VC_PTR
AR_RTR_2_4_COLBUF_PERF_STALL_RS:COL_BUF_PERF_STALL_RS
AR_RTR_2_4_COLBUF_PERF_STALL_RS:VC_PTR
AR_RTR_2_4_INQ_PRF_INCOMING_FLIT_VC0
AR_RTR_2_4_INQ_PRF_INCOMING_FLIT_VC1
AR_RTR_2_4_INQ_PRF_INCOMING_FLIT_VC2
AR_RTR_2_4_INQ_PRF_INCOMING_FLIT_VC3
AR_RTR_2_4_INQ_PRF_INCOMING_FLIT_VC4
AR_RTR_2_4_INQ_PRF_INCOMING_FLIT_VC5
AR_RTR_2_4_INQ_PRF_INCOMING_FLIT_VC6
AR_RTR_2_4_INQ_PRF_INCOMING_FLIT_VC7
AR_RTR_2_4_INQ_PRF_INCOMING_PKT_VC0_FILTER_FLIT0_CNT
AR_RTR_2_4_INQ_PRF_INCOMING_PKT_VC1_FILTER_FLIT1_CNT
AR_RTR_2_4_INQ_PRF_INCOMING_PKT_VC2_FILTER_FLIT2_CNT
AR_RTR_2_4_INQ_PRF_INCOMING_PKT_VC3_FILTER_FLIT3_CNT
AR_RTR_2_4_INQ_PRF_INCOMING_PKT_VC4_FILTER_FLIT4_CNT
AR_RTR_2_4_INQ_PRF_INCOMING_PKT_VC5_FILTER_FLIT5_CNT
AR_RTR_2_4_INQ_PRF_INCOMING_PKT_VC6_FILTER_FLIT6_CNT
AR_RTR_2_4_INQ_PRF_INCOMING_PKT_VC7_FILTER_FLIT7_CNT
AR_RTR_2_4_INQ_PRF_MATCH_FLIT_3_TO_0_FILTERING_CNT
AR_RTR_2_4_INQ_PRF_MATCH_FLIT_7_TO_4_FILTERING_CNT
AR_RTR_2_4_INQ_PRF_PKT_TO_DEAD_LINK_CNT
AR_RTR_2_4_INQ_PRF_ROWBUS_2X_USAGE_CNT
AR_RTR_2_4_INQ_PRF_ROWBUS_STALL_CNT
AR_RTR_2_5_COLBUF_PERF_STALL_RQ:COL_BUF_PERF_STALL_RQ
AR_RTR_2_5_COLBUF_PERF_STALL_RQ:VC_PTR
AR_RTR_2_5_COLBUF_PERF_STALL_RS:COL_BUF_PERF_STALL_RS
AR_RTR_2_5_COLBUF_PERF_STALL_RS:VC_PTR
AR_RTR_2_5_INQ_PRF_INCOMING_FLIT_VC0
AR_RTR_2_5_INQ_PRF_INCOMING_FLIT_VC1
AR_RTR_2_5_INQ_PRF_INCOMING_FLIT_VC2
AR_RTR_2_5_INQ_PRF_INCOMING_FLIT_VC3
AR_RTR_2_5_INQ_PRF_INCOMING_FLIT_VC4
AR_RTR_2_5_INQ_PRF_INCOMING_FLIT_VC5
AR_RTR_2_5_INQ_PRF_INCOMING_FLIT_VC6
AR_RTR_2_5_INQ_PRF_INCOMING_FLIT_VC7
AR_RTR_2_5_INQ_PRF_INCOMING_PKT_VC0_FILTER_FLIT0_CNT
AR_RTR_2_5_INQ_PRF_INCOMING_PKT_VC1_FILTER_FLIT1_CNT
AR_RTR_2_5_INQ_PRF_INCOMING_PKT_VC2_FILTER_FLIT2_CNT
AR_RTR_2_5_INQ_PRF_INCOMING_PKT_VC3_FILTER_FLIT3_CNT
AR_RTR_2_5_INQ_PRF_INCOMING_PKT_VC4_FILTER_FLIT4_CNT
AR_RTR_2_5_INQ_PRF_INCOMING_PKT_VC5_FILTER_FLIT5_CNT
AR_RTR_2_5_INQ_PRF_INCOMING_PKT_VC6_FILTER_FLIT6_CNT
AR_RTR_2_5_INQ_PRF_INCOMING_PKT_VC7_FILTER_FLIT7_CNT
AR_RTR_2_5_INQ_PRF_MATCH_FLIT_3_TO_0_FILTERING_CNT
AR_RTR_2_5_INQ_PRF_MATCH_FLIT_7_TO_4_FILTERING_CNT
AR_RTR_2_5_INQ_PRF_PKT_TO_DEAD_LINK_CNT
AR_RTR_2_5_INQ_PRF_ROWBUS_2X_USAGE_CNT
AR_RTR_2_5_INQ_PRF_ROWBUS_STALL_CNT
AR_RTR_2_6_COLBUF_PERF_STALL_RQ:COL_BUF_PERF_STALL_RQ
AR_RTR_2_6_COLBUF_PERF_STALL_RQ:VC_PTR
AR_RTR_2_6_COLBUF_PERF_STALL_RS:COL_BUF_PERF_STALL_RS
AR_RTR_2_6_COLBUF_PERF_STALL_RS:VC_PTR
AR_RTR_2_6_INQ_PRF_INCOMING_FLIT_VC0
AR_RTR_2_6_INQ_PRF_INCOMING_FLIT_VC1
AR_RTR_2_6_INQ_PRF_INCOMING_FLIT_VC2
```

```
AR_RTR_2_6_INQ_PRF_INCOMING_FLIT_VC3
AR_RTR_2_6_INQ_PRF_INCOMING_FLIT_VC4
AR_RTR_2_6_INQ_PRF_INCOMING_FLIT_VC5
AR_RTR_2_6_INQ_PRF_INCOMING_FLIT_VC6
AR_RTR_2_6_INQ_PRF_INCOMING_FLIT_VC7
AR_RTR_2_6_INQ_PRF_INCOMING_PKT_VC0_FILTER_FLIT0_CNT
AR_RTR_2_6_INQ_PRF_INCOMING_PKT_VC1_FILTER_FLIT1_CNT
AR_RTR_2_6_INQ_PRF_INCOMING_PKT_VC2_FILTER_FLIT2_CNT
AR_RTR_2_6_INQ_PRF_INCOMING_PKT_VC3_FILTER_FLIT3_CNT
AR_RTR_2_6_INQ_PRF_INCOMING_PKT_VC4_FILTER_FLIT4_CNT
AR_RTR_2_6_INQ_PRF_INCOMING_PKT_VC5_FILTER_FLIT5_CNT
AR_RTR_2_6_INQ_PRF_INCOMING_PKT_VC6_FILTER_FLIT6_CNT
AR_RTR_2_6_INQ_PRF_INCOMING_PKT_VC7_FILTER_FLIT7_CNT
AR_RTR_2_6_INQ_PRF_MATCH_FLIT_3_TO_0_FILTERING_CNT
AR_RTR_2_6_INQ_PRF_MATCH_FLIT_7_TO_4_FILTERING_CNT
AR_RTR_2_6_INQ_PRF_PKT_TO_DEAD_LINK_CNT
AR_RTR_2_6_INQ_PRF_ROWBUS_2X_USAGE_CNT
AR_RTR_2_6_INQ_PRF_ROWBUS_STALL_CNT
AR_RTR_2_7_COLBUF_PERF_STALL_RQ:COL_BUF_PERF_STALL_RQ
AR_RTR_2_7_COLBUF_PERF_STALL_RQ:VC_PTR
AR_RTR_2_7_COLBUF_PERF_STALL_RS:COL_BUF_PERF_STALL_RS
AR_RTR_2_7_COLBUF_PERF_STALL_RS:VC_PTR
AR_RTR_2_7_INQ_PRF_INCOMING_FLIT_VC0
AR_RTR_2_7_INQ_PRF_INCOMING_FLIT_VC1
AR_RTR_2_7_INQ_PRF_INCOMING_FLIT_VC2
AR_RTR_2_7_INQ_PRF_INCOMING_FLIT_VC3
AR_RTR_2_7_INQ_PRF_INCOMING_FLIT_VC4
AR_RTR_2_7_INQ_PRF_INCOMING_FLIT_VC5
AR_RTR_2_7_INQ_PRF_INCOMING_FLIT_VC6
AR_RTR_2_7_INQ_PRF_INCOMING_FLIT_VC7
AR_RTR_2_7_INQ_PRF_INCOMING_PKT_VC0_FILTER_FLIT0_CNT
AR_RTR_2_7_INQ_PRF_INCOMING_PKT_VC1_FILTER_FLIT1_CNT
AR_RTR_2_7_INQ_PRF_INCOMING_PKT_VC2_FILTER_FLIT2_CNT
AR_RTR_2_7_INQ_PRF_INCOMING_PKT_VC3_FILTER_FLIT3_CNT
AR_RTR_2_7_INQ_PRF_INCOMING_PKT_VC4_FILTER_FLIT4_CNT
AR_RTR_2_7_INQ_PRF_INCOMING_PKT_VC5_FILTER_FLIT5_CNT
AR_RTR_2_7_INQ_PRF_INCOMING_PKT_VC6_FILTER_FLIT6_CNT
AR_RTR_2_7_INQ_PRF_INCOMING_PKT_VC7_FILTER_FLIT7_CNT
AR_RTR_2_7_INQ_PRF_MATCH_FLIT_3_TO_0_FILTERING_CNT
AR_RTR_2_7_INQ_PRF_MATCH_FLIT_7_TO_4_FILTERING_CNT
AR_RTR_2_7_INQ_PRF_PKT_TO_DEAD_LINK_CNT
AR_RTR_2_7_INQ_PRF_ROWBUS_2X_USAGE_CNT
AR_RTR_2_7_INQ_PRF_ROWBUS_STALL_CNT
AR_RTR_3_0_COLBUF_PERF_STALL_RQ:COL_BUF_PERF_STALL_RQ
AR_RTR_3_0_COLBUF_PERF_STALL_RQ:VC_PTR
AR_RTR_3_0_COLBUF_PERF_STALL_RS:COL_BUF_PERF_STALL_RS
AR_RTR_3_0_COLBUF_PERF_STALL_RS:VC_PTR
AR_RTR_3_0_INQ_PRF_INCOMING_FLIT_VC0
AR_RTR_3_0_INQ_PRF_INCOMING_FLIT_VC1
AR_RTR_3_0_INQ_PRF_INCOMING_FLIT_VC2
AR_RTR_3_0_INQ_PRF_INCOMING_FLIT_VC3
AR_RTR_3_0_INQ_PRF_INCOMING_FLIT_VC4
AR_RTR_3_0_INQ_PRF_INCOMING_FLIT_VC5
AR_RTR_3_0_INQ_PRF_INCOMING_FLIT_VC6
AR_RTR_3_0_INQ_PRF_INCOMING_FLIT_VC7
AR_RTR_3_0_INQ_PRF_INCOMING_PKT_VC0_FILTER_FLIT0_CNT
AR_RTR_3_0_INQ_PRF_INCOMING_PKT_VC1_FILTER_FLIT1_CNT
AR_RTR_3_0_INQ_PRF_INCOMING_PKT_VC2_FILTER_FLIT2_CNT
```

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AR_RTR_3_0_INQ_PRF_INCOMING_PKT_VC3_FILTER_FLIT3_CNT
AR_RTR_3_0_INQ_PRF_INCOMING_PKT_VC4_FILTER_FLIT4_CNT
AR_RTR_3_0_INQ_PRF_INCOMING_PKT_VC5_FILTER_FLIT5_CNT
AR_RTR_3_0_INQ_PRF_INCOMING_PKT_VC6_FILTER_FLIT6_CNT
AR_RTR_3_0_INQ_PRF_INCOMING_PKT_VC7_FILTER_FLIT7_CNT
AR_RTR_3_0_INQ_PRF_MATCH_FLIT_3_TO_0_FILTERING_CNT
AR_RTR_3_0_INQ_PRF_MATCH_FLIT_7_TO_4_FILTERING_CNT
AR_RTR_3_0_INQ_PRF_PKT_TO_DEAD_LINK_CNT
AR_RTR_3_0_INQ_PRF_ROWBUS_2X_USAGE_CNT
AR_RTR_3_0_INQ_PRF_ROWBUS_STALL_CNT
AR_RTR_3_1_COLBUF_PERF_STALL_RQ:COL_BUF_PERF_STALL_RQ
AR_RTR_3_1_COLBUF_PERF_STALL_RQ:VC_PTR
AR_RTR_3_1_COLBUF_PERF_STALL_RS:COL_BUF_PERF_STALL_RS
AR_RTR_3_1_COLBUF_PERF_STALL_RS:VC_PTR
AR_RTR_3_1_INQ_PRF_INCOMING_FLIT_VC0
AR_RTR_3_1_INQ_PRF_INCOMING_FLIT_VC1
AR_RTR_3_1_INQ_PRF_INCOMING_FLIT_VC2
AR_RTR_3_1_INQ_PRF_INCOMING_FLIT_VC3
AR_RTR_3_1_INQ_PRF_INCOMING_FLIT_VC4
AR_RTR_3_1_INQ_PRF_INCOMING_FLIT_VC5
AR_RTR_3_1_INQ_PRF_INCOMING_FLIT_VC6
AR_RTR_3_1_INQ_PRF_INCOMING_FLIT_VC7
AR_RTR_3_1_INQ_PRF_INCOMING_PKT_VC0_FILTER_FLIT0_CNT
AR_RTR_3_1_INQ_PRF_INCOMING_PKT_VC1_FILTER_FLIT1_CNT
AR_RTR_3_1_INQ_PRF_INCOMING_PKT_VC2_FILTER_FLIT2_CNT
AR_RTR_3_1_INQ_PRF_INCOMING_PKT_VC3_FILTER_FLIT3_CNT
AR_RTR_3_1_INQ_PRF_INCOMING_PKT_VC4_FILTER_FLIT4_CNT
AR_RTR_3_1_INQ_PRF_INCOMING_PKT_VC5_FILTER_FLIT5_CNT
AR_RTR_3_1_INQ_PRF_INCOMING_PKT_VC6_FILTER_FLIT6_CNT
AR_RTR_3_1_INQ_PRF_INCOMING_PKT_VC7_FILTER_FLIT7_CNT
AR_RTR_3_1_INQ_PRF_MATCH_FLIT_3_TO_0_FILTERING_CNT
AR_RTR_3_1_INQ_PRF_MATCH_FLIT_7_TO_4_FILTERING_CNT
AR_RTR_3_1_INQ_PRF_PKT_TO_DEAD_LINK_CNT
AR_RTR_3_1_INQ_PRF_ROWBUS_2X_USAGE_CNT
AR_RTR_3_1_INQ_PRF_ROWBUS_STALL_CNT
AR_RTR_3_2_COLBUF_PERF_STALL_RQ:COL_BUF_PERF_STALL_RQ
AR_RTR_3_2_COLBUF_PERF_STALL_RQ:VC_PTR
AR_RTR_3_2_COLBUF_PERF_STALL_RS:COL_BUF_PERF_STALL_RS
AR_RTR_3_2_COLBUF_PERF_STALL_RS:VC_PTR
AR_RTR_3_2_INQ_PRF_INCOMING_FLIT_VC0
AR_RTR_3_2_INQ_PRF_INCOMING_FLIT_VC1
AR_RTR_3_2_INQ_PRF_INCOMING_FLIT_VC2
AR_RTR_3_2_INQ_PRF_INCOMING_FLIT_VC3
AR_RTR_3_2_INQ_PRF_INCOMING_FLIT_VC4
AR_RTR_3_2_INQ_PRF_INCOMING_FLIT_VC5
AR_RTR_3_2_INQ_PRF_INCOMING_FLIT_VC6
AR_RTR_3_2_INQ_PRF_INCOMING_FLIT_VC7
AR_RTR_3_2_INQ_PRF_INCOMING_PKT_VC0_FILTER_FLIT0_CNT
AR_RTR_3_2_INQ_PRF_INCOMING_PKT_VC1_FILTER_FLIT1_CNT
AR_RTR_3_2_INQ_PRF_INCOMING_PKT_VC2_FILTER_FLIT2_CNT
AR_RTR_3_2_INQ_PRF_INCOMING_PKT_VC3_FILTER_FLIT3_CNT
AR_RTR_3_2_INQ_PRF_INCOMING_PKT_VC4_FILTER_FLIT4_CNT
AR_RTR_3_2_INQ_PRF_INCOMING_PKT_VC5_FILTER_FLIT5_CNT
AR_RTR_3_2_INQ_PRF_INCOMING_PKT_VC6_FILTER_FLIT6_CNT
AR_RTR_3_2_INQ_PRF_INCOMING_PKT_VC7_FILTER_FLIT7_CNT
AR_RTR_3_2_INQ_PRF_MATCH_FLIT_3_TO_0_FILTERING_CNT
AR_RTR_3_2_INQ_PRF_MATCH_FLIT_7_TO_4_FILTERING_CNT
AR_RTR_3_2_INQ_PRF_PKT_TO_DEAD_LINK_CNT
```

AR\_RTR\_3\_2\_INQ\_PRF\_ROWBUS\_2X\_USAGE\_CNT  
AR\_RTR\_3\_2\_INQ\_PRF\_ROWBUS\_STALL\_CNT  
AR\_RTR\_3\_3\_COLBUF\_PERF\_STALL\_RQ:COL\_BUF\_PERF\_STALL\_RQ  
AR\_RTR\_3\_3\_COLBUF\_PERF\_STALL\_RQ:VC\_PTR  
AR\_RTR\_3\_3\_COLBUF\_PERF\_STALL\_RS:COL\_BUF\_PERF\_STALL\_RS  
AR\_RTR\_3\_3\_COLBUF\_PERF\_STALL\_RS:VC\_PTR  
AR\_RTR\_3\_3\_INQ\_PRF\_INCOMING\_FLIT\_VC0  
AR\_RTR\_3\_3\_INQ\_PRF\_INCOMING\_FLIT\_VC1  
AR\_RTR\_3\_3\_INQ\_PRF\_INCOMING\_FLIT\_VC2  
AR\_RTR\_3\_3\_INQ\_PRF\_INCOMING\_FLIT\_VC3  
AR\_RTR\_3\_3\_INQ\_PRF\_INCOMING\_FLIT\_VC4  
AR\_RTR\_3\_3\_INQ\_PRF\_INCOMING\_FLIT\_VC5  
AR\_RTR\_3\_3\_INQ\_PRF\_INCOMING\_FLIT\_VC6  
AR\_RTR\_3\_3\_INQ\_PRF\_INCOMING\_FLIT\_VC7  
AR\_RTR\_3\_3\_INQ\_PRF\_INCOMING\_PKT\_VC0\_FILTER\_FLIT0\_CNT  
AR\_RTR\_3\_3\_INQ\_PRF\_INCOMING\_PKT\_VC1\_FILTER\_FLIT1\_CNT  
AR\_RTR\_3\_3\_INQ\_PRF\_INCOMING\_PKT\_VC2\_FILTER\_FLIT2\_CNT  
AR\_RTR\_3\_3\_INQ\_PRF\_INCOMING\_PKT\_VC3\_FILTER\_FLIT3\_CNT  
AR\_RTR\_3\_3\_INQ\_PRF\_INCOMING\_PKT\_VC4\_FILTER\_FLIT4\_CNT  
AR\_RTR\_3\_3\_INQ\_PRF\_INCOMING\_PKT\_VC5\_FILTER\_FLIT5\_CNT  
AR\_RTR\_3\_3\_INQ\_PRF\_INCOMING\_PKT\_VC6\_FILTER\_FLIT6\_CNT  
AR\_RTR\_3\_3\_INQ\_PRF\_INCOMING\_PKT\_VC7\_FILTER\_FLIT7\_CNT  
AR\_RTR\_3\_3\_INQ\_PRF\_MATCH\_FLIT\_3\_TO\_0\_FILTERING\_CNT  
AR\_RTR\_3\_3\_INQ\_PRF\_MATCH\_FLIT\_7\_TO\_4\_FILTERING\_CNT  
AR\_RTR\_3\_3\_INQ\_PRF\_PKT\_TO\_DEAD\_LINK\_CNT  
AR\_RTR\_3\_3\_INQ\_PRF\_ROWBUS\_2X\_USAGE\_CNT  
AR\_RTR\_3\_3\_INQ\_PRF\_ROWBUS\_STALL\_CNT  
AR\_RTR\_3\_4\_COLBUF\_PERF\_STALL\_RQ:COL\_BUF\_PERF\_STALL\_RQ  
AR\_RTR\_3\_4\_COLBUF\_PERF\_STALL\_RQ:VC\_PTR  
AR\_RTR\_3\_4\_COLBUF\_PERF\_STALL\_RS:COL\_BUF\_PERF\_STALL\_RS  
AR\_RTR\_3\_4\_COLBUF\_PERF\_STALL\_RS:VC\_PTR  
AR\_RTR\_3\_4\_INQ\_PRF\_INCOMING\_FLIT\_VC0  
AR\_RTR\_3\_4\_INQ\_PRF\_INCOMING\_FLIT\_VC1  
AR\_RTR\_3\_4\_INQ\_PRF\_INCOMING\_FLIT\_VC2  
AR\_RTR\_3\_4\_INQ\_PRF\_INCOMING\_FLIT\_VC3  
AR\_RTR\_3\_4\_INQ\_PRF\_INCOMING\_FLIT\_VC4  
AR\_RTR\_3\_4\_INQ\_PRF\_INCOMING\_FLIT\_VC5  
AR\_RTR\_3\_4\_INQ\_PRF\_INCOMING\_FLIT\_VC6  
AR\_RTR\_3\_4\_INQ\_PRF\_INCOMING\_FLIT\_VC7  
AR\_RTR\_3\_4\_INQ\_PRF\_INCOMING\_PKT\_VC0\_FILTER\_FLIT0\_CNT  
AR\_RTR\_3\_4\_INQ\_PRF\_INCOMING\_PKT\_VC1\_FILTER\_FLIT1\_CNT  
AR\_RTR\_3\_4\_INQ\_PRF\_INCOMING\_PKT\_VC2\_FILTER\_FLIT2\_CNT  
AR\_RTR\_3\_4\_INQ\_PRF\_INCOMING\_PKT\_VC3\_FILTER\_FLIT3\_CNT  
AR\_RTR\_3\_4\_INQ\_PRF\_INCOMING\_PKT\_VC4\_FILTER\_FLIT4\_CNT  
AR\_RTR\_3\_4\_INQ\_PRF\_INCOMING\_PKT\_VC5\_FILTER\_FLIT5\_CNT  
AR\_RTR\_3\_4\_INQ\_PRF\_INCOMING\_PKT\_VC6\_FILTER\_FLIT6\_CNT  
AR\_RTR\_3\_4\_INQ\_PRF\_INCOMING\_PKT\_VC7\_FILTER\_FLIT7\_CNT  
AR\_RTR\_3\_4\_INQ\_PRF\_MATCH\_FLIT\_3\_TO\_0\_FILTERING\_CNT  
AR\_RTR\_3\_4\_INQ\_PRF\_MATCH\_FLIT\_7\_TO\_4\_FILTERING\_CNT  
AR\_RTR\_3\_4\_INQ\_PRF\_PKT\_TO\_DEAD\_LINK\_CNT  
AR\_RTR\_3\_4\_INQ\_PRF\_ROWBUS\_2X\_USAGE\_CNT  
AR\_RTR\_3\_4\_INQ\_PRF\_ROWBUS\_STALL\_CNT  
AR\_RTR\_3\_5\_COLBUF\_PERF\_STALL\_RQ:COL\_BUF\_PERF\_STALL\_RQ  
AR\_RTR\_3\_5\_COLBUF\_PERF\_STALL\_RQ:VC\_PTR  
AR\_RTR\_3\_5\_COLBUF\_PERF\_STALL\_RS:COL\_BUF\_PERF\_STALL\_RS  
AR\_RTR\_3\_5\_COLBUF\_PERF\_STALL\_RS:VC\_PTR  
AR\_RTR\_3\_5\_INQ\_PRF\_INCOMING\_FLIT\_VC0  
AR\_RTR\_3\_5\_INQ\_PRF\_INCOMING\_FLIT\_VC1

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AR_RTR_3_5_INQ_PRF_INCOMING_FLIT_VC2
AR_RTR_3_5_INQ_PRF_INCOMING_FLIT_VC3
AR_RTR_3_5_INQ_PRF_INCOMING_FLIT_VC4
AR_RTR_3_5_INQ_PRF_INCOMING_FLIT_VC5
AR_RTR_3_5_INQ_PRF_INCOMING_FLIT_VC6
AR_RTR_3_5_INQ_PRF_INCOMING_FLIT_VC7
AR_RTR_3_5_INQ_PRF_INCOMING_PKT_VC0_FILTER_FLIT0_CNT
AR_RTR_3_5_INQ_PRF_INCOMING_PKT_VC1_FILTER_FLIT1_CNT
AR_RTR_3_5_INQ_PRF_INCOMING_PKT_VC2_FILTER_FLIT2_CNT
AR_RTR_3_5_INQ_PRF_INCOMING_PKT_VC3_FILTER_FLIT3_CNT
AR_RTR_3_5_INQ_PRF_INCOMING_PKT_VC4_FILTER_FLIT4_CNT
AR_RTR_3_5_INQ_PRF_INCOMING_PKT_VC5_FILTER_FLIT5_CNT
AR_RTR_3_5_INQ_PRF_INCOMING_PKT_VC6_FILTER_FLIT6_CNT
AR_RTR_3_5_INQ_PRF_INCOMING_PKT_VC7_FILTER_FLIT7_CNT
AR_RTR_3_5_INQ_PRF_MATCH_FLIT_3_TO_0_FILTERING_CNT
AR_RTR_3_5_INQ_PRF_MATCH_FLIT_7_TO_4_FILTERING_CNT
AR_RTR_3_5_INQ_PRF_PKT_TO_DEAD_LINK_CNT
AR_RTR_3_5_INQ_PRF_ROWBUS_2X_USAGE_CNT
AR_RTR_3_5_INQ_PRF_ROWBUS_STALL_CNT
AR_RTR_3_6_COLBUF_PERF_STALL_RQ:COL_BUF_PERF_STALL_RQ
AR_RTR_3_6_COLBUF_PERF_STALL_RQ:VC_PTR
AR_RTR_3_6_COLBUF_PERF_STALL_RS:COL_BUF_PERF_STALL_RS
AR_RTR_3_6_COLBUF_PERF_STALL_RS:VC_PTR
AR_RTR_3_6_INQ_PRF_INCOMING_FLIT_VC0
AR_RTR_3_6_INQ_PRF_INCOMING_FLIT_VC1
AR_RTR_3_6_INQ_PRF_INCOMING_FLIT_VC2
AR_RTR_3_6_INQ_PRF_INCOMING_FLIT_VC3
AR_RTR_3_6_INQ_PRF_INCOMING_FLIT_VC4
AR_RTR_3_6_INQ_PRF_INCOMING_FLIT_VC5
AR_RTR_3_6_INQ_PRF_INCOMING_FLIT_VC6
AR_RTR_3_6_INQ_PRF_INCOMING_FLIT_VC7
AR_RTR_3_6_INQ_PRF_INCOMING_PKT_VC0_FILTER_FLIT0_CNT
AR_RTR_3_6_INQ_PRF_INCOMING_PKT_VC1_FILTER_FLIT1_CNT
AR_RTR_3_6_INQ_PRF_INCOMING_PKT_VC2_FILTER_FLIT2_CNT
AR_RTR_3_6_INQ_PRF_INCOMING_PKT_VC3_FILTER_FLIT3_CNT
AR_RTR_3_6_INQ_PRF_INCOMING_PKT_VC4_FILTER_FLIT4_CNT
AR_RTR_3_6_INQ_PRF_INCOMING_PKT_VC5_FILTER_FLIT5_CNT
AR_RTR_3_6_INQ_PRF_INCOMING_PKT_VC6_FILTER_FLIT6_CNT
AR_RTR_3_6_INQ_PRF_INCOMING_PKT_VC7_FILTER_FLIT7_CNT
AR_RTR_3_6_INQ_PRF_MATCH_FLIT_3_TO_0_FILTERING_CNT
AR_RTR_3_6_INQ_PRF_MATCH_FLIT_7_TO_4_FILTERING_CNT
AR_RTR_3_6_INQ_PRF_PKT_TO_DEAD_LINK_CNT
AR_RTR_3_6_INQ_PRF_ROWBUS_2X_USAGE_CNT
AR_RTR_3_6_INQ_PRF_ROWBUS_STALL_CNT
AR_RTR_3_7_COLBUF_PERF_STALL_RQ:COL_BUF_PERF_STALL_RQ
AR_RTR_3_7_COLBUF_PERF_STALL_RQ:VC_PTR
AR_RTR_3_7_COLBUF_PERF_STALL_RS:COL_BUF_PERF_STALL_RS
AR_RTR_3_7_COLBUF_PERF_STALL_RS:VC_PTR
AR_RTR_3_7_INQ_PRF_INCOMING_FLIT_VC0
AR_RTR_3_7_INQ_PRF_INCOMING_FLIT_VC1
AR_RTR_3_7_INQ_PRF_INCOMING_FLIT_VC2
AR_RTR_3_7_INQ_PRF_INCOMING_FLIT_VC3
AR_RTR_3_7_INQ_PRF_INCOMING_FLIT_VC4
AR_RTR_3_7_INQ_PRF_INCOMING_FLIT_VC5
AR_RTR_3_7_INQ_PRF_INCOMING_FLIT_VC6
AR_RTR_3_7_INQ_PRF_INCOMING_FLIT_VC7
AR_RTR_3_7_INQ_PRF_INCOMING_PKT_VC0_FILTER_FLIT0_CNT
AR_RTR_3_7_INQ_PRF_INCOMING_PKT_VC1_FILTER_FLIT1_CNT
```

AR\_RTR\_3\_7\_INQ\_PRF\_INCOMING\_PKT\_VC2\_FILTER\_FLIT2\_CNT  
AR\_RTR\_3\_7\_INQ\_PRF\_INCOMING\_PKT\_VC3\_FILTER\_FLIT3\_CNT  
AR\_RTR\_3\_7\_INQ\_PRF\_INCOMING\_PKT\_VC4\_FILTER\_FLIT4\_CNT  
AR\_RTR\_3\_7\_INQ\_PRF\_INCOMING\_PKT\_VC5\_FILTER\_FLIT5\_CNT  
AR\_RTR\_3\_7\_INQ\_PRF\_INCOMING\_PKT\_VC6\_FILTER\_FLIT6\_CNT  
AR\_RTR\_3\_7\_INQ\_PRF\_INCOMING\_PKT\_VC7\_FILTER\_FLIT7\_CNT  
AR\_RTR\_3\_7\_INQ\_PRF\_MATCH\_FLIT\_3\_TO\_0\_FILTERING\_CNT  
AR\_RTR\_3\_7\_INQ\_PRF\_MATCH\_FLIT\_7\_TO\_4\_FILTERING\_CNT  
AR\_RTR\_3\_7\_INQ\_PRF\_PKT\_TO\_DEAD\_LINK\_CNT  
AR\_RTR\_3\_7\_INQ\_PRF\_ROWBUS\_2X\_USAGE\_CNT  
AR\_RTR\_3\_7\_INQ\_PRF\_ROWBUS\_STALL\_CNT  
AR\_RTR\_4\_0\_COLBUF\_PERF\_STALL\_RQ:COL\_BUF\_PERF\_STALL\_RQ  
AR\_RTR\_4\_0\_COLBUF\_PERF\_STALL\_RQ:VC\_PTR  
AR\_RTR\_4\_0\_COLBUF\_PERF\_STALL\_RS:COL\_BUF\_PERF\_STALL\_RS  
AR\_RTR\_4\_0\_COLBUF\_PERF\_STALL\_RS:VC\_PTR  
AR\_RTR\_4\_0\_INQ\_PRF\_INCOMING\_FLIT\_VC0  
AR\_RTR\_4\_0\_INQ\_PRF\_INCOMING\_FLIT\_VC1  
AR\_RTR\_4\_0\_INQ\_PRF\_INCOMING\_FLIT\_VC2  
AR\_RTR\_4\_0\_INQ\_PRF\_INCOMING\_FLIT\_VC3  
AR\_RTR\_4\_0\_INQ\_PRF\_INCOMING\_FLIT\_VC4  
AR\_RTR\_4\_0\_INQ\_PRF\_INCOMING\_FLIT\_VC5  
AR\_RTR\_4\_0\_INQ\_PRF\_INCOMING\_FLIT\_VC6  
AR\_RTR\_4\_0\_INQ\_PRF\_INCOMING\_FLIT\_VC7  
AR\_RTR\_4\_0\_INQ\_PRF\_INCOMING\_PKT\_VC0\_FILTER\_FLIT0\_CNT  
AR\_RTR\_4\_0\_INQ\_PRF\_INCOMING\_PKT\_VC1\_FILTER\_FLIT1\_CNT  
AR\_RTR\_4\_0\_INQ\_PRF\_INCOMING\_PKT\_VC2\_FILTER\_FLIT2\_CNT  
AR\_RTR\_4\_0\_INQ\_PRF\_INCOMING\_PKT\_VC3\_FILTER\_FLIT3\_CNT  
AR\_RTR\_4\_0\_INQ\_PRF\_INCOMING\_PKT\_VC4\_FILTER\_FLIT4\_CNT  
AR\_RTR\_4\_0\_INQ\_PRF\_INCOMING\_PKT\_VC5\_FILTER\_FLIT5\_CNT  
AR\_RTR\_4\_0\_INQ\_PRF\_INCOMING\_PKT\_VC6\_FILTER\_FLIT6\_CNT  
AR\_RTR\_4\_0\_INQ\_PRF\_INCOMING\_PKT\_VC7\_FILTER\_FLIT7\_CNT  
AR\_RTR\_4\_0\_INQ\_PRF\_MATCH\_FLIT\_3\_TO\_0\_FILTERING\_CNT  
AR\_RTR\_4\_0\_INQ\_PRF\_MATCH\_FLIT\_7\_TO\_4\_FILTERING\_CNT  
AR\_RTR\_4\_0\_INQ\_PRF\_PKT\_TO\_DEAD\_LINK\_CNT  
AR\_RTR\_4\_0\_INQ\_PRF\_ROWBUS\_2X\_USAGE\_CNT  
AR\_RTR\_4\_0\_INQ\_PRF\_ROWBUS\_STALL\_CNT  
AR\_RTR\_4\_1\_COLBUF\_PERF\_STALL\_RQ:COL\_BUF\_PERF\_STALL\_RQ  
AR\_RTR\_4\_1\_COLBUF\_PERF\_STALL\_RQ:VC\_PTR  
AR\_RTR\_4\_1\_COLBUF\_PERF\_STALL\_RS:COL\_BUF\_PERF\_STALL\_RS  
AR\_RTR\_4\_1\_COLBUF\_PERF\_STALL\_RS:VC\_PTR  
AR\_RTR\_4\_1\_INQ\_PRF\_INCOMING\_FLIT\_VC0  
AR\_RTR\_4\_1\_INQ\_PRF\_INCOMING\_FLIT\_VC1  
AR\_RTR\_4\_1\_INQ\_PRF\_INCOMING\_FLIT\_VC2  
AR\_RTR\_4\_1\_INQ\_PRF\_INCOMING\_FLIT\_VC3  
AR\_RTR\_4\_1\_INQ\_PRF\_INCOMING\_FLIT\_VC4  
AR\_RTR\_4\_1\_INQ\_PRF\_INCOMING\_FLIT\_VC5  
AR\_RTR\_4\_1\_INQ\_PRF\_INCOMING\_FLIT\_VC6  
AR\_RTR\_4\_1\_INQ\_PRF\_INCOMING\_FLIT\_VC7  
AR\_RTR\_4\_1\_INQ\_PRF\_INCOMING\_PKT\_VC0\_FILTER\_FLIT0\_CNT  
AR\_RTR\_4\_1\_INQ\_PRF\_INCOMING\_PKT\_VC1\_FILTER\_FLIT1\_CNT  
AR\_RTR\_4\_1\_INQ\_PRF\_INCOMING\_PKT\_VC2\_FILTER\_FLIT2\_CNT  
AR\_RTR\_4\_1\_INQ\_PRF\_INCOMING\_PKT\_VC3\_FILTER\_FLIT3\_CNT  
AR\_RTR\_4\_1\_INQ\_PRF\_INCOMING\_PKT\_VC4\_FILTER\_FLIT4\_CNT  
AR\_RTR\_4\_1\_INQ\_PRF\_INCOMING\_PKT\_VC5\_FILTER\_FLIT5\_CNT  
AR\_RTR\_4\_1\_INQ\_PRF\_INCOMING\_PKT\_VC6\_FILTER\_FLIT6\_CNT  
AR\_RTR\_4\_1\_INQ\_PRF\_INCOMING\_PKT\_VC7\_FILTER\_FLIT7\_CNT  
AR\_RTR\_4\_1\_INQ\_PRF\_MATCH\_FLIT\_3\_TO\_0\_FILTERING\_CNT  
AR\_RTR\_4\_1\_INQ\_PRF\_MATCH\_FLIT\_7\_TO\_4\_FILTERING\_CNT

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AR_RTR_4_1_INQ_PRF_PKT_TO_DEAD_LINK_CNT
AR_RTR_4_1_INQ_PRF_ROWBUS_2X_USAGE_CNT
AR_RTR_4_1_INQ_PRF_ROWBUS_STALL_CNT
AR_RTR_4_2_COLBUF_PERF_STALL_RQ:COL_BUF_PERF_STALL_RQ
AR_RTR_4_2_COLBUF_PERF_STALL_RQ:VC_PTR
AR_RTR_4_2_COLBUF_PERF_STALL_RS:COL_BUF_PERF_STALL_RS
AR_RTR_4_2_COLBUF_PERF_STALL_RS:VC_PTR
AR_RTR_4_2_INQ_PRF_INCOMING_FLIT_VC0
AR_RTR_4_2_INQ_PRF_INCOMING_FLIT_VC1
AR_RTR_4_2_INQ_PRF_INCOMING_FLIT_VC2
AR_RTR_4_2_INQ_PRF_INCOMING_FLIT_VC3
AR_RTR_4_2_INQ_PRF_INCOMING_FLIT_VC4
AR_RTR_4_2_INQ_PRF_INCOMING_FLIT_VC5
AR_RTR_4_2_INQ_PRF_INCOMING_FLIT_VC6
AR_RTR_4_2_INQ_PRF_INCOMING_FLIT_VC7
AR_RTR_4_2_INQ_PRF_INCOMING_PKT_VC0_FILTER_FLIT0_CNT
AR_RTR_4_2_INQ_PRF_INCOMING_PKT_VC1_FILTER_FLIT1_CNT
AR_RTR_4_2_INQ_PRF_INCOMING_PKT_VC2_FILTER_FLIT2_CNT
AR_RTR_4_2_INQ_PRF_INCOMING_PKT_VC3_FILTER_FLIT3_CNT
AR_RTR_4_2_INQ_PRF_INCOMING_PKT_VC4_FILTER_FLIT4_CNT
AR_RTR_4_2_INQ_PRF_INCOMING_PKT_VC5_FILTER_FLIT5_CNT
AR_RTR_4_2_INQ_PRF_INCOMING_PKT_VC6_FILTER_FLIT6_CNT
AR_RTR_4_2_INQ_PRF_INCOMING_PKT_VC7_FILTER_FLIT7_CNT
AR_RTR_4_2_INQ_PRF_MATCH_FLIT_3_TO_0_FILTERING_CNT
AR_RTR_4_2_INQ_PRF_MATCH_FLIT_7_TO_4_FILTERING_CNT
AR_RTR_4_2_INQ_PRF_PKT_TO_DEAD_LINK_CNT
AR_RTR_4_2_INQ_PRF_ROWBUS_2X_USAGE_CNT
AR_RTR_4_2_INQ_PRF_ROWBUS_STALL_CNT
AR_RTR_4_3_COLBUF_PERF_STALL_RQ:COL_BUF_PERF_STALL_RQ
AR_RTR_4_3_COLBUF_PERF_STALL_RQ:VC_PTR
AR_RTR_4_3_COLBUF_PERF_STALL_RS:COL_BUF_PERF_STALL_RS
AR_RTR_4_3_COLBUF_PERF_STALL_RS:VC_PTR
AR_RTR_4_3_INQ_PRF_INCOMING_FLIT_VC0
AR_RTR_4_3_INQ_PRF_INCOMING_FLIT_VC1
AR_RTR_4_3_INQ_PRF_INCOMING_FLIT_VC2
AR_RTR_4_3_INQ_PRF_INCOMING_FLIT_VC3
AR_RTR_4_3_INQ_PRF_INCOMING_FLIT_VC4
AR_RTR_4_3_INQ_PRF_INCOMING_FLIT_VC5
AR_RTR_4_3_INQ_PRF_INCOMING_FLIT_VC6
AR_RTR_4_3_INQ_PRF_INCOMING_FLIT_VC7
AR_RTR_4_3_INQ_PRF_INCOMING_PKT_VC0_FILTER_FLIT0_CNT
AR_RTR_4_3_INQ_PRF_INCOMING_PKT_VC1_FILTER_FLIT1_CNT
AR_RTR_4_3_INQ_PRF_INCOMING_PKT_VC2_FILTER_FLIT2_CNT
AR_RTR_4_3_INQ_PRF_INCOMING_PKT_VC3_FILTER_FLIT3_CNT
AR_RTR_4_3_INQ_PRF_INCOMING_PKT_VC4_FILTER_FLIT4_CNT
AR_RTR_4_3_INQ_PRF_INCOMING_PKT_VC5_FILTER_FLIT5_CNT
AR_RTR_4_3_INQ_PRF_INCOMING_PKT_VC6_FILTER_FLIT6_CNT
AR_RTR_4_3_INQ_PRF_INCOMING_PKT_VC7_FILTER_FLIT7_CNT
AR_RTR_4_3_INQ_PRF_MATCH_FLIT_3_TO_0_FILTERING_CNT
AR_RTR_4_3_INQ_PRF_MATCH_FLIT_7_TO_4_FILTERING_CNT
AR_RTR_4_3_INQ_PRF_PKT_TO_DEAD_LINK_CNT
AR_RTR_4_3_INQ_PRF_ROWBUS_2X_USAGE_CNT
AR_RTR_4_3_INQ_PRF_ROWBUS_STALL_CNT
AR_RTR_4_4_COLBUF_PERF_STALL_RQ:COL_BUF_PERF_STALL_RQ
AR_RTR_4_4_COLBUF_PERF_STALL_RQ:VC_PTR
AR_RTR_4_4_COLBUF_PERF_STALL_RS:COL_BUF_PERF_STALL_RS
AR_RTR_4_4_COLBUF_PERF_STALL_RS:VC_PTR
AR_RTR_4_4_INQ_PRF_INCOMING_FLIT_VC0
```



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AR_RTR_4_4_INQ_PRF_INCOMING_FLIT_VC1
AR_RTR_4_4_INQ_PRF_INCOMING_FLIT_VC2
AR_RTR_4_4_INQ_PRF_INCOMING_FLIT_VC3
AR_RTR_4_4_INQ_PRF_INCOMING_FLIT_VC4
AR_RTR_4_4_INQ_PRF_INCOMING_FLIT_VC5
AR_RTR_4_4_INQ_PRF_INCOMING_FLIT_VC6
AR_RTR_4_4_INQ_PRF_INCOMING_FLIT_VC7
AR_RTR_4_4_INQ_PRF_INCOMING_PKT_VC0_FILTER_FLIT0_CNT
AR_RTR_4_4_INQ_PRF_INCOMING_PKT_VC1_FILTER_FLIT1_CNT
AR_RTR_4_4_INQ_PRF_INCOMING_PKT_VC2_FILTER_FLIT2_CNT
AR_RTR_4_4_INQ_PRF_INCOMING_PKT_VC3_FILTER_FLIT3_CNT
AR_RTR_4_4_INQ_PRF_INCOMING_PKT_VC4_FILTER_FLIT4_CNT
AR_RTR_4_4_INQ_PRF_INCOMING_PKT_VC5_FILTER_FLIT5_CNT
AR_RTR_4_4_INQ_PRF_INCOMING_PKT_VC6_FILTER_FLIT6_CNT
AR_RTR_4_4_INQ_PRF_INCOMING_PKT_VC7_FILTER_FLIT7_CNT
AR_RTR_4_4_INQ_PRF_MATCH_FLIT_3_TO_0_FILTERING_CNT
AR_RTR_4_4_INQ_PRF_MATCH_FLIT_7_TO_4_FILTERING_CNT
AR_RTR_4_4_INQ_PRF_PKT_TO_DEAD_LINK_CNT
AR_RTR_4_4_INQ_PRF_ROWBUS_2X_USAGE_CNT
AR_RTR_4_4_INQ_PRF_ROWBUS_STALL_CNT
AR_RTR_4_5_COLBUF_PERF_STALL_RQ:COL_BUF_PERF_STALL_RQ
AR_RTR_4_5_COLBUF_PERF_STALL_RQ:VC_PTR
AR_RTR_4_5_COLBUF_PERF_STALL_RS:COL_BUF_PERF_STALL_RS
AR_RTR_4_5_COLBUF_PERF_STALL_RS:VC_PTR
AR_RTR_4_5_INQ_PRF_INCOMING_FLIT_VC0
AR_RTR_4_5_INQ_PRF_INCOMING_FLIT_VC1
AR_RTR_4_5_INQ_PRF_INCOMING_FLIT_VC2
AR_RTR_4_5_INQ_PRF_INCOMING_FLIT_VC3
AR_RTR_4_5_INQ_PRF_INCOMING_FLIT_VC4
AR_RTR_4_5_INQ_PRF_INCOMING_FLIT_VC5
AR_RTR_4_5_INQ_PRF_INCOMING_FLIT_VC6
AR_RTR_4_5_INQ_PRF_INCOMING_FLIT_VC7
AR_RTR_4_5_INQ_PRF_INCOMING_PKT_VC0_FILTER_FLIT0_CNT
AR_RTR_4_5_INQ_PRF_INCOMING_PKT_VC1_FILTER_FLIT1_CNT
AR_RTR_4_5_INQ_PRF_INCOMING_PKT_VC2_FILTER_FLIT2_CNT
AR_RTR_4_5_INQ_PRF_INCOMING_PKT_VC3_FILTER_FLIT3_CNT
AR_RTR_4_5_INQ_PRF_INCOMING_PKT_VC4_FILTER_FLIT4_CNT
AR_RTR_4_5_INQ_PRF_INCOMING_PKT_VC5_FILTER_FLIT5_CNT
AR_RTR_4_5_INQ_PRF_INCOMING_PKT_VC6_FILTER_FLIT6_CNT
AR_RTR_4_5_INQ_PRF_INCOMING_PKT_VC7_FILTER_FLIT7_CNT
AR_RTR_4_5_INQ_PRF_MATCH_FLIT_3_TO_0_FILTERING_CNT
AR_RTR_4_5_INQ_PRF_MATCH_FLIT_7_TO_4_FILTERING_CNT
AR_RTR_4_5_INQ_PRF_PKT_TO_DEAD_LINK_CNT
AR_RTR_4_5_INQ_PRF_ROWBUS_2X_USAGE_CNT
AR_RTR_4_5_INQ_PRF_ROWBUS_STALL_CNT
AR_RTR_4_6_COLBUF_PERF_STALL_RQ:COL_BUF_PERF_STALL_RQ
AR_RTR_4_6_COLBUF_PERF_STALL_RQ:VC_PTR
AR_RTR_4_6_COLBUF_PERF_STALL_RS:COL_BUF_PERF_STALL_RS
AR_RTR_4_6_COLBUF_PERF_STALL_RS:VC_PTR
AR_RTR_4_6_INQ_PRF_INCOMING_FLIT_VC0
AR_RTR_4_6_INQ_PRF_INCOMING_FLIT_VC1
AR_RTR_4_6_INQ_PRF_INCOMING_FLIT_VC2
AR_RTR_4_6_INQ_PRF_INCOMING_FLIT_VC3
AR_RTR_4_6_INQ_PRF_INCOMING_FLIT_VC4
AR_RTR_4_6_INQ_PRF_INCOMING_FLIT_VC5
AR_RTR_4_6_INQ_PRF_INCOMING_FLIT_VC6
AR_RTR_4_6_INQ_PRF_INCOMING_FLIT_VC7
AR_RTR_4_6_INQ_PRF_INCOMING_PKT_VC0_FILTER_FLIT0_CNT
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AR_RTR_4_6_INQ_PRF_INCOMING_PKT_VC1_FILTER_FLIT1_CNT
AR_RTR_4_6_INQ_PRF_INCOMING_PKT_VC2_FILTER_FLIT2_CNT
AR_RTR_4_6_INQ_PRF_INCOMING_PKT_VC3_FILTER_FLIT3_CNT
AR_RTR_4_6_INQ_PRF_INCOMING_PKT_VC4_FILTER_FLIT4_CNT
AR_RTR_4_6_INQ_PRF_INCOMING_PKT_VC5_FILTER_FLIT5_CNT
AR_RTR_4_6_INQ_PRF_INCOMING_PKT_VC6_FILTER_FLIT6_CNT
AR_RTR_4_6_INQ_PRF_INCOMING_PKT_VC7_FILTER_FLIT7_CNT
AR_RTR_4_6_INQ_PRF_MATCH_FLIT_3_TO_0_FILTERING_CNT
AR_RTR_4_6_INQ_PRF_MATCH_FLIT_7_TO_4_FILTERING_CNT
AR_RTR_4_6_INQ_PRF_PKT_TO_DEAD_LINK_CNT
AR_RTR_4_6_INQ_PRF_ROWBUS_2X_USAGE_CNT
AR_RTR_4_6_INQ_PRF_ROWBUS_STALL_CNT
AR_RTR_4_7_COLBUF_PERF_STALL_RQ:COL_BUF_PERF_STALL_RQ
AR_RTR_4_7_COLBUF_PERF_STALL_RQ:VC_PTR
AR_RTR_4_7_COLBUF_PERF_STALL_RS:COL_BUF_PERF_STALL_RS
AR_RTR_4_7_COLBUF_PERF_STALL_RS:VC_PTR
AR_RTR_4_7_INQ_PRF_INCOMING_FLIT_VC0
AR_RTR_4_7_INQ_PRF_INCOMING_FLIT_VC1
AR_RTR_4_7_INQ_PRF_INCOMING_FLIT_VC2
AR_RTR_4_7_INQ_PRF_INCOMING_FLIT_VC3
AR_RTR_4_7_INQ_PRF_INCOMING_FLIT_VC4
AR_RTR_4_7_INQ_PRF_INCOMING_FLIT_VC5
AR_RTR_4_7_INQ_PRF_INCOMING_FLIT_VC6
AR_RTR_4_7_INQ_PRF_INCOMING_FLIT_VC7
AR_RTR_4_7_INQ_PRF_INCOMING_PKT_VC0_FILTER_FLIT0_CNT
AR_RTR_4_7_INQ_PRF_INCOMING_PKT_VC1_FILTER_FLIT1_CNT
AR_RTR_4_7_INQ_PRF_INCOMING_PKT_VC2_FILTER_FLIT2_CNT
AR_RTR_4_7_INQ_PRF_INCOMING_PKT_VC3_FILTER_FLIT3_CNT
AR_RTR_4_7_INQ_PRF_INCOMING_PKT_VC4_FILTER_FLIT4_CNT
AR_RTR_4_7_INQ_PRF_INCOMING_PKT_VC5_FILTER_FLIT5_CNT
AR_RTR_4_7_INQ_PRF_INCOMING_PKT_VC6_FILTER_FLIT6_CNT
AR_RTR_4_7_INQ_PRF_INCOMING_PKT_VC7_FILTER_FLIT7_CNT
AR_RTR_4_7_INQ_PRF_MATCH_FLIT_3_TO_0_FILTERING_CNT
AR_RTR_4_7_INQ_PRF_MATCH_FLIT_7_TO_4_FILTERING_CNT
AR_RTR_4_7_INQ_PRF_PKT_TO_DEAD_LINK_CNT
AR_RTR_4_7_INQ_PRF_ROWBUS_2X_USAGE_CNT
AR_RTR_4_7_INQ_PRF_ROWBUS_STALL_CNT
AR_RTR_PT_5_0_COLBUF_PERF_STALL_RQ
AR_RTR_PT_5_0_COLBUF_PERF_STALL_RS
AR_RTR_PT_5_0_INQ_PRF_INCOMING_FLIT_VC0
AR_RTR_PT_5_0_INQ_PRF_INCOMING_FLIT_VC4
AR_RTR_PT_5_0_INQ_PRF_INCOMING_PKT_VC0_FILTER_FLIT0_CNT
AR_RTR_PT_5_0_INQ_PRF_INCOMING_PKT_VC1_FILTER_FLIT1_CNT
AR_RTR_PT_5_0_INQ_PRF_INCOMING_PKT_VC2_FILTER_FLIT2_CNT
AR_RTR_PT_5_0_INQ_PRF_INCOMING_PKT_VC3_FILTER_FLIT3_CNT
AR_RTR_PT_5_0_INQ_PRF_INCOMING_PKT_VC4_FILTER_FLIT4_CNT
AR_RTR_PT_5_0_INQ_PRF_INCOMING_PKT_VC5_FILTER_FLIT5_CNT
AR_RTR_PT_5_0_INQ_PRF_INCOMING_PKT_VC6_FILTER_FLIT6_CNT
AR_RTR_PT_5_0_INQ_PRF_INCOMING_PKT_VC7_FILTER_FLIT7_CNT
AR_RTR_PT_5_0_INQ_PRF_MATCH_FLIT_3_TO_0_FILTERING_CNT
AR_RTR_PT_5_0_INQ_PRF_MATCH_FLIT_7_TO_4_FILTERING_CNT
AR_RTR_PT_5_0_INQ_PRF_PKT_TO_DEAD_LINK_CNT
AR_RTR_PT_5_0_INQ_PRF_REQ_ROWBUS_STALL_CNT
AR_RTR_PT_5_0_INQ_PRF_ROWBUS_2X_USAGE_CNT
AR_RTR_PT_5_0_INQ_PRF_RSP_ROWBUS_STALL_CNT
AR_RTR_PT_5_1_COLBUF_PERF_STALL_RQ
AR_RTR_PT_5_1_COLBUF_PERF_STALL_RS
AR_RTR_PT_5_1_INQ_PRF_INCOMING_FLIT_VC0
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AR\_RTR\_PT\_5\_1\_INQ\_PRF\_INCOMING\_FLIT\_VC4  
AR\_RTR\_PT\_5\_1\_INQ\_PRF\_INCOMING\_PKT\_VC0\_FILTER\_FLIT0\_CNT  
AR\_RTR\_PT\_5\_1\_INQ\_PRF\_INCOMING\_PKT\_VC1\_FILTER\_FLIT1\_CNT  
AR\_RTR\_PT\_5\_1\_INQ\_PRF\_INCOMING\_PKT\_VC2\_FILTER\_FLIT2\_CNT  
AR\_RTR\_PT\_5\_1\_INQ\_PRF\_INCOMING\_PKT\_VC3\_FILTER\_FLIT3\_CNT  
AR\_RTR\_PT\_5\_1\_INQ\_PRF\_INCOMING\_PKT\_VC4\_FILTER\_FLIT4\_CNT  
AR\_RTR\_PT\_5\_1\_INQ\_PRF\_INCOMING\_PKT\_VC5\_FILTER\_FLIT5\_CNT  
AR\_RTR\_PT\_5\_1\_INQ\_PRF\_INCOMING\_PKT\_VC6\_FILTER\_FLIT6\_CNT  
AR\_RTR\_PT\_5\_1\_INQ\_PRF\_INCOMING\_PKT\_VC7\_FILTER\_FLIT7\_CNT  
AR\_RTR\_PT\_5\_1\_INQ\_PRF\_MATCH\_FLIT\_3\_TO\_0\_FILTERING\_CNT  
AR\_RTR\_PT\_5\_1\_INQ\_PRF\_MATCH\_FLIT\_7\_TO\_4\_FILTERING\_CNT  
AR\_RTR\_PT\_5\_1\_INQ\_PRF\_PKT\_TO\_DEAD\_LINK\_CNT  
AR\_RTR\_PT\_5\_1\_INQ\_PRF\_REQ\_ROWBUS\_STALL\_CNT  
AR\_RTR\_PT\_5\_1\_INQ\_PRF\_ROWBUS\_2X\_USAGE\_CNT  
AR\_RTR\_PT\_5\_1\_INQ\_PRF\_RSP\_ROWBUS\_STALL\_CNT  
AR\_RTR\_PT\_5\_2\_COLBUF\_PERF\_STALL\_RQ  
AR\_RTR\_PT\_5\_2\_COLBUF\_PERF\_STALL\_RS  
AR\_RTR\_PT\_5\_2\_INQ\_PRF\_INCOMING\_FLIT\_VC0  
AR\_RTR\_PT\_5\_2\_INQ\_PRF\_INCOMING\_FLIT\_VC4  
AR\_RTR\_PT\_5\_2\_INQ\_PRF\_INCOMING\_PKT\_VC0\_FILTER\_FLIT0\_CNT  
AR\_RTR\_PT\_5\_2\_INQ\_PRF\_INCOMING\_PKT\_VC1\_FILTER\_FLIT1\_CNT  
AR\_RTR\_PT\_5\_2\_INQ\_PRF\_INCOMING\_PKT\_VC2\_FILTER\_FLIT2\_CNT  
AR\_RTR\_PT\_5\_2\_INQ\_PRF\_INCOMING\_PKT\_VC3\_FILTER\_FLIT3\_CNT  
AR\_RTR\_PT\_5\_2\_INQ\_PRF\_INCOMING\_PKT\_VC4\_FILTER\_FLIT4\_CNT  
AR\_RTR\_PT\_5\_2\_INQ\_PRF\_INCOMING\_PKT\_VC5\_FILTER\_FLIT5\_CNT  
AR\_RTR\_PT\_5\_2\_INQ\_PRF\_INCOMING\_PKT\_VC6\_FILTER\_FLIT6\_CNT  
AR\_RTR\_PT\_5\_2\_INQ\_PRF\_INCOMING\_PKT\_VC7\_FILTER\_FLIT7\_CNT  
AR\_RTR\_PT\_5\_2\_INQ\_PRF\_MATCH\_FLIT\_3\_TO\_0\_FILTERING\_CNT  
AR\_RTR\_PT\_5\_2\_INQ\_PRF\_MATCH\_FLIT\_7\_TO\_4\_FILTERING\_CNT  
AR\_RTR\_PT\_5\_2\_INQ\_PRF\_PKT\_TO\_DEAD\_LINK\_CNT  
AR\_RTR\_PT\_5\_2\_INQ\_PRF\_REQ\_ROWBUS\_STALL\_CNT  
AR\_RTR\_PT\_5\_2\_INQ\_PRF\_ROWBUS\_2X\_USAGE\_CNT  
AR\_RTR\_PT\_5\_2\_INQ\_PRF\_RSP\_ROWBUS\_STALL\_CNT  
AR\_RTR\_PT\_5\_3\_COLBUF\_PERF\_STALL\_RQ  
AR\_RTR\_PT\_5\_3\_COLBUF\_PERF\_STALL\_RS  
AR\_RTR\_PT\_5\_3\_INQ\_PRF\_INCOMING\_FLIT\_VC0  
AR\_RTR\_PT\_5\_3\_INQ\_PRF\_INCOMING\_FLIT\_VC4  
AR\_RTR\_PT\_5\_3\_INQ\_PRF\_INCOMING\_PKT\_VC0\_FILTER\_FLIT0\_CNT  
AR\_RTR\_PT\_5\_3\_INQ\_PRF\_INCOMING\_PKT\_VC1\_FILTER\_FLIT1\_CNT  
AR\_RTR\_PT\_5\_3\_INQ\_PRF\_INCOMING\_PKT\_VC2\_FILTER\_FLIT2\_CNT  
AR\_RTR\_PT\_5\_3\_INQ\_PRF\_INCOMING\_PKT\_VC3\_FILTER\_FLIT3\_CNT  
AR\_RTR\_PT\_5\_3\_INQ\_PRF\_INCOMING\_PKT\_VC4\_FILTER\_FLIT4\_CNT  
AR\_RTR\_PT\_5\_3\_INQ\_PRF\_INCOMING\_PKT\_VC5\_FILTER\_FLIT5\_CNT  
AR\_RTR\_PT\_5\_3\_INQ\_PRF\_INCOMING\_PKT\_VC6\_FILTER\_FLIT6\_CNT  
AR\_RTR\_PT\_5\_3\_INQ\_PRF\_INCOMING\_PKT\_VC7\_FILTER\_FLIT7\_CNT  
AR\_RTR\_PT\_5\_3\_INQ\_PRF\_MATCH\_FLIT\_3\_TO\_0\_FILTERING\_CNT  
AR\_RTR\_PT\_5\_3\_INQ\_PRF\_MATCH\_FLIT\_7\_TO\_4\_FILTERING\_CNT  
AR\_RTR\_PT\_5\_3\_INQ\_PRF\_PKT\_TO\_DEAD\_LINK\_CNT  
AR\_RTR\_PT\_5\_3\_INQ\_PRF\_REQ\_ROWBUS\_STALL\_CNT  
AR\_RTR\_PT\_5\_3\_INQ\_PRF\_ROWBUS\_2X\_USAGE\_CNT  
AR\_RTR\_PT\_5\_3\_INQ\_PRF\_RSP\_ROWBUS\_STALL\_CNT  
AR\_RTR\_PT\_5\_4\_COLBUF\_PERF\_STALL\_RQ  
AR\_RTR\_PT\_5\_4\_COLBUF\_PERF\_STALL\_RS  
AR\_RTR\_PT\_5\_4\_INQ\_PRF\_INCOMING\_FLIT\_VC0  
AR\_RTR\_PT\_5\_4\_INQ\_PRF\_INCOMING\_FLIT\_VC4  
AR\_RTR\_PT\_5\_4\_INQ\_PRF\_INCOMING\_PKT\_VC0\_FILTER\_FLIT0\_CNT  
AR\_RTR\_PT\_5\_4\_INQ\_PRF\_INCOMING\_PKT\_VC1\_FILTER\_FLIT1\_CNT  
AR\_RTR\_PT\_5\_4\_INQ\_PRF\_INCOMING\_PKT\_VC2\_FILTER\_FLIT2\_CNT

AR\_RTR\_PT\_5\_4\_INQ\_PRF\_INCOMING\_PKT\_VC3\_FILTER\_FLIT3\_CNT  
AR\_RTR\_PT\_5\_4\_INQ\_PRF\_INCOMING\_PKT\_VC4\_FILTER\_FLIT4\_CNT  
AR\_RTR\_PT\_5\_4\_INQ\_PRF\_INCOMING\_PKT\_VC5\_FILTER\_FLIT5\_CNT  
AR\_RTR\_PT\_5\_4\_INQ\_PRF\_INCOMING\_PKT\_VC6\_FILTER\_FLIT6\_CNT  
AR\_RTR\_PT\_5\_4\_INQ\_PRF\_INCOMING\_PKT\_VC7\_FILTER\_FLIT7\_CNT  
AR\_RTR\_PT\_5\_4\_INQ\_PRF\_MATCH\_FLIT\_3\_TO\_0\_FILTERING\_CNT  
AR\_RTR\_PT\_5\_4\_INQ\_PRF\_MATCH\_FLIT\_7\_TO\_4\_FILTERING\_CNT  
AR\_RTR\_PT\_5\_4\_INQ\_PRF\_PKT\_TO\_DEAD\_LINK\_CNT  
AR\_RTR\_PT\_5\_4\_INQ\_PRF\_REQ\_ROWBUS\_STALL\_CNT  
AR\_RTR\_PT\_5\_4\_INQ\_PRF\_ROWBUS\_2X\_USAGE\_CNT  
AR\_RTR\_PT\_5\_4\_INQ\_PRF\_RSP\_ROWBUS\_STALL\_CNT  
AR\_RTR\_PT\_5\_5\_COLBUF\_PERF\_STALL\_RQ  
AR\_RTR\_PT\_5\_5\_COLBUF\_PERF\_STALL\_RS  
AR\_RTR\_PT\_5\_5\_INQ\_PRF\_INCOMING\_FLIT\_VC0  
AR\_RTR\_PT\_5\_5\_INQ\_PRF\_INCOMING\_FLIT\_VC4  
AR\_RTR\_PT\_5\_5\_INQ\_PRF\_INCOMING\_PKT\_VC0\_FILTER\_FLIT0\_CNT  
AR\_RTR\_PT\_5\_5\_INQ\_PRF\_INCOMING\_PKT\_VC1\_FILTER\_FLIT1\_CNT  
AR\_RTR\_PT\_5\_5\_INQ\_PRF\_INCOMING\_PKT\_VC2\_FILTER\_FLIT2\_CNT  
AR\_RTR\_PT\_5\_5\_INQ\_PRF\_INCOMING\_PKT\_VC3\_FILTER\_FLIT3\_CNT  
AR\_RTR\_PT\_5\_5\_INQ\_PRF\_INCOMING\_PKT\_VC4\_FILTER\_FLIT4\_CNT  
AR\_RTR\_PT\_5\_5\_INQ\_PRF\_INCOMING\_PKT\_VC5\_FILTER\_FLIT5\_CNT  
AR\_RTR\_PT\_5\_5\_INQ\_PRF\_INCOMING\_PKT\_VC6\_FILTER\_FLIT6\_CNT  
AR\_RTR\_PT\_5\_5\_INQ\_PRF\_INCOMING\_PKT\_VC7\_FILTER\_FLIT7\_CNT  
AR\_RTR\_PT\_5\_5\_INQ\_PRF\_MATCH\_FLIT\_3\_TO\_0\_FILTERING\_CNT  
AR\_RTR\_PT\_5\_5\_INQ\_PRF\_MATCH\_FLIT\_7\_TO\_4\_FILTERING\_CNT  
AR\_RTR\_PT\_5\_5\_INQ\_PRF\_PKT\_TO\_DEAD\_LINK\_CNT  
AR\_RTR\_PT\_5\_5\_INQ\_PRF\_REQ\_ROWBUS\_STALL\_CNT  
AR\_RTR\_PT\_5\_5\_INQ\_PRF\_ROWBUS\_2X\_USAGE\_CNT  
AR\_RTR\_PT\_5\_5\_INQ\_PRF\_RSP\_ROWBUS\_STALL\_CNT  
AR\_RTR\_PT\_5\_6\_COLBUF\_PERF\_STALL\_RQ  
AR\_RTR\_PT\_5\_6\_COLBUF\_PERF\_STALL\_RS  
AR\_RTR\_PT\_5\_6\_INQ\_PRF\_INCOMING\_FLIT\_VC0  
AR\_RTR\_PT\_5\_6\_INQ\_PRF\_INCOMING\_FLIT\_VC4  
AR\_RTR\_PT\_5\_6\_INQ\_PRF\_INCOMING\_PKT\_VC0\_FILTER\_FLIT0\_CNT  
AR\_RTR\_PT\_5\_6\_INQ\_PRF\_INCOMING\_PKT\_VC1\_FILTER\_FLIT1\_CNT  
AR\_RTR\_PT\_5\_6\_INQ\_PRF\_INCOMING\_PKT\_VC2\_FILTER\_FLIT2\_CNT  
AR\_RTR\_PT\_5\_6\_INQ\_PRF\_INCOMING\_PKT\_VC3\_FILTER\_FLIT3\_CNT  
AR\_RTR\_PT\_5\_6\_INQ\_PRF\_INCOMING\_PKT\_VC4\_FILTER\_FLIT4\_CNT  
AR\_RTR\_PT\_5\_6\_INQ\_PRF\_INCOMING\_PKT\_VC5\_FILTER\_FLIT5\_CNT  
AR\_RTR\_PT\_5\_6\_INQ\_PRF\_INCOMING\_PKT\_VC6\_FILTER\_FLIT6\_CNT  
AR\_RTR\_PT\_5\_6\_INQ\_PRF\_INCOMING\_PKT\_VC7\_FILTER\_FLIT7\_CNT  
AR\_RTR\_PT\_5\_6\_INQ\_PRF\_MATCH\_FLIT\_3\_TO\_0\_FILTERING\_CNT  
AR\_RTR\_PT\_5\_6\_INQ\_PRF\_MATCH\_FLIT\_7\_TO\_4\_FILTERING\_CNT  
AR\_RTR\_PT\_5\_6\_INQ\_PRF\_PKT\_TO\_DEAD\_LINK\_CNT  
AR\_RTR\_PT\_5\_6\_INQ\_PRF\_REQ\_ROWBUS\_STALL\_CNT  
AR\_RTR\_PT\_5\_6\_INQ\_PRF\_ROWBUS\_2X\_USAGE\_CNT  
AR\_RTR\_PT\_5\_6\_INQ\_PRF\_RSP\_ROWBUS\_STALL\_CNT  
AR\_RTR\_PT\_5\_7\_COLBUF\_PERF\_STALL\_RQ  
AR\_RTR\_PT\_5\_7\_COLBUF\_PERF\_STALL\_RS  
AR\_RTR\_PT\_5\_7\_INQ\_PRF\_INCOMING\_FLIT\_VC0  
AR\_RTR\_PT\_5\_7\_INQ\_PRF\_INCOMING\_FLIT\_VC4  
AR\_RTR\_PT\_5\_7\_INQ\_PRF\_INCOMING\_PKT\_VC0\_FILTER\_FLIT0\_CNT  
AR\_RTR\_PT\_5\_7\_INQ\_PRF\_INCOMING\_PKT\_VC1\_FILTER\_FLIT1\_CNT  
AR\_RTR\_PT\_5\_7\_INQ\_PRF\_INCOMING\_PKT\_VC2\_FILTER\_FLIT2\_CNT  
AR\_RTR\_PT\_5\_7\_INQ\_PRF\_INCOMING\_PKT\_VC3\_FILTER\_FLIT3\_CNT  
AR\_RTR\_PT\_5\_7\_INQ\_PRF\_INCOMING\_PKT\_VC4\_FILTER\_FLIT4\_CNT  
AR\_RTR\_PT\_5\_7\_INQ\_PRF\_INCOMING\_PKT\_VC5\_FILTER\_FLIT5\_CNT  
AR\_RTR\_PT\_5\_7\_INQ\_PRF\_INCOMING\_PKT\_VC6\_FILTER\_FLIT6\_CNT

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AR_RTR_PT_5_7_INQ_PRF_INCOMING_PKT_VC7_FILTER_FLIT7_CNT
AR_RTR_PT_5_7_INQ_PRF_MATCH_FLIT_3_TO_0_FILTERING_CNT
AR_RTR_PT_5_7_INQ_PRF_MATCH_FLIT_7_TO_4_FILTERING_CNT
AR_RTR_PT_5_7_INQ_PRF_PKT_TO_DEAD_LINK_CNT
AR_RTR_PT_5_7_INQ_PRF_REQ_ROWBUS_STALL_CNT
AR_RTR_PT_5_7_INQ_PRF_ROWBUS_2X_USAGE_CNT
AR_RTR_PT_5_7_INQ_PRF_RSP_ROWBUS_STALL_CNT

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## 1.6.2 Aries Configurable Counters

This list summarizes the unique configurable Aries events, taking into account mask and value numbers, row and column numbers, and flit numbers. Aside from what the cardinal number presents, the descriptions of each of the events that are grouped is the same.

Each event at a given tile row and column position, however, is individually selected by the user. A user may, for example, define their own group of filtering events for row 1.

Number	Description
1	AR_NIC_AMO_PRF_EN
1	AR_NIC_AMO_PRF_EN:PRF_FLUSH_EN
1	AR_NIC_AMO_PRF_EN:PRF_FULL_EN
1	AR_NIC_AMO_PRF_EN:PRF_MATCH_EN
1	AR_NIC_AMO_PRF_EN:PRF_ORDERED_EN
1	AR_NIC_NETMON_PRF_EVENT_CNTR_CTRL
1	AR_NIC_NETMON_PRF_EVENT_CNTR_CTRL: CLEAR
1	AR_NIC_NETMON_PRF_EVENT_CNTR_CTRL: COUNTERS_CLEAR
1	AR_NIC_NETMON_PRF_EVENT_CNTR_CTRL: DISABLE_AUTO_UPDATE
1	AR_NIC_NETMON_PRF_EVENT_CNTR_CTRL: ENABLE
1	AR_NIC_NPT_PRF_EN
1	AR_NIC_NPT_PRF_EN:PRF_FULL_EN
1	AR_NIC_NPT_PRF_EN:PRF_STALL_EN
1	AR_NIC_NSLM_PRF_EVENT_CNTR_CTRL
1	AR_NIC_NSLM_PRF_EVENT_CNTR_CTRL: CLEAR
1	AR_NIC_NSLM_PRF_EVENT_CNTR_CTRL: COUNTERS_CLEAR
1	AR_NIC_NSLM_PRF_EVENT_CNTR_CTRL: DISABLE_AUTO_UPDATE
1	AR_NIC_NSLM_PRF_EVENT_CNTR_CTRL: ENABLE
1	AR_NIC_ORB_CFG_NET_RSP_HIST_1
1	AR_NIC_ORB_CFG_NET_RSP_HIST_1:BIN1_MIN
1	AR_NIC_ORB_CFG_NET_RSP_HIST_1:BIN2_MIN
1	AR_NIC_ORB_CFG_NET_RSP_HIST_1:BIN3_MIN
1	AR_NIC_ORB_CFG_NET_RSP_HIST_2
1	AR_NIC_ORB_CFG_NET_RSP_HIST_2:BIN4_MIN
1	AR_NIC_ORB_CFG_NET_RSP_HIST_2:BIN5_MIN
1	AR_NIC_ORB_CFG_NET_RSP_HIST_2:BIN6_MIN
1	AR_NIC_ORB_CFG_NET_RSP_HIST_2:BIN7_MIN
1	AR_NIC_ORB_CFG_PRF_TRK_COMP_0
1	AR_NIC_ORB_CFG_PRF_TRK_COMP_0:ADDR_63_36
1	AR_NIC_ORB_CFG_PRF_TRK_COMP_0: DST
1	AR_NIC_ORB_CFG_PRF_TRK_COMP_0: DSTID
1	AR_NIC_ORB_CFG_PRF_TRK_COMP_0: NTT
1	AR_NIC_ORB_CFG_PRF_TRK_COMP_0: P0
1	AR_NIC_ORB_CFG_PRF_TRK_COMP_0: PKT_SRC

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1 AR_NIC_ORB_CFG_PRF_TRK_COMP_0:PTAG
1 AR_NIC_ORB_CFG_PRF_TRK_COMP_0:RESERVED_33_28
1 AR_NIC_ORB_CFG_PRF_TRK_COMP_1
1 AR_NIC_ORB_CFG_PRF_TRK_COMP_1:ADDR_115_112
1 AR_NIC_ORB_CFG_PRF_TRK_COMP_1:ADDR_69_64
1 AR_NIC_ORB_CFG_PRF_TRK_COMP_1:CNT_BM
1 AR_NIC_ORB_CFG_PRF_TRK_COMP_1:MDH
1 AR_NIC_ORB_CFG_PRF_TRK_COMP_1:P1
1 AR_NIC_ORB_CFG_PRF_TRK_COMP_1:P2
1 AR_NIC_ORB_CFG_PRF_TRK_COMP_1:RC0
1 AR_NIC_ORB_CFG_PRF_TRK_COMP_1:RC2
1 AR_NIC_ORB_CFG_PRF_TRK_COMP_1:REQCMD
1 AR_NIC_ORB_CFG_PRF_TRK_COMP_1:RESERVED_105_94
1 AR_NIC_ORB_CFG_PRF_TRK_COMP_1:RESERVED_127_125
1 AR_NIC_ORB_CFG_PRF_TRK_COMP_1:SSID
1 AR_NIC_ORB_CFG_PRF_TRK_COMP_1:UV
1 AR_NIC_ORB_CFG_PRF_TRK_COMP_2
1 AR_NIC_ORB_CFG_PRF_TRK_COMP_2:EN
1 AR_NIC_ORB_CFG_PRF_TRK_COMP_2:LSTATUS
1 AR_NIC_ORB_CFG_PRF_TRK_COMP_2:P3
1 AR_NIC_ORB_CFG_PRF_TRK_COMP_2:RC1
1 AR_NIC_ORB_CFG_PRF_TRK_COMP_2:RESERVED_135_128
1 AR_NIC_ORB_CFG_PRF_TRK_COMP_3
1 AR_NIC_ORB_CFG_PRF_TRK_COMP_3:LADDR_63_36
1 AR_NIC_ORB_CFG_PRF_TRK_COMP_3:LPTAG
1 AR_NIC_ORB_CFG_PRF_TRK_COMP_3:RESERVED_17_0
1 AR_NIC_ORB_CFG_PRF_TRK_COMP_3:RESERVED_35_26
1 AR_NIC_ORB_CFG_PRF_TRK_COMP_4
1 AR_NIC_ORB_CFG_PRF_TRK_COMP_4:ECC0
1 AR_NIC_ORB_CFG_PRF_TRK_COMP_4:FR
1 AR_NIC_ORB_CFG_PRF_TRK_COMP_4:LADDR_105_94
1 AR_NIC_ORB_CFG_PRF_TRK_COMP_4:LCNT
1 AR_NIC_ORB_CFG_PRF_TRK_COMP_4:LMDH
1 AR_NIC_ORB_CFG_PRF_TRK_COMP_4:RESERVED_107
1 AR_NIC_ORB_CFG_PRF_TRK_COMP_4:RESERVED_127_112
1 AR_NIC_ORB_CFG_PRF_TRK_COMP_4:RESERVED_81_72
1 AR_NIC_ORB_CFG_PRF_TRK_COMP_5
1 AR_NIC_ORB_CFG_PRF_TRK_COMP_5:ECC1
1 AR_NIC_ORB_CFG_PRF_TRK_COMP_5:EN
1 AR_NIC_ORB_CFG_PRF_TRK_COMP_5:LWC
1 AR_NIC_ORB_CFG_PRF_TRK_COMP_5:RESERVED_134_128
1 AR_NIC_ORB_CFG_PRF_TRK_MASK_0
1 AR_NIC_ORB_CFG_PRF_TRK_MASK_0:ADDR_63_36_MSK
1 AR_NIC_ORB_CFG_PRF_TRK_MASK_0:DST
1 AR_NIC_ORB_CFG_PRF_TRK_MASK_0:DSTID_MSK
1 AR_NIC_ORB_CFG_PRF_TRK_MASK_0:NTT_MSK
1 AR_NIC_ORB_CFG_PRF_TRK_MASK_0:P0_MSK
1 AR_NIC_ORB_CFG_PRF_TRK_MASK_0:PKT_SRC_MSK
1 AR_NIC_ORB_CFG_PRF_TRK_MASK_0:PTAG_MSK
1 AR_NIC_ORB_CFG_PRF_TRK_MASK_0:RESERVED_33_28
1 AR_NIC_ORB_CFG_PRF_TRK_MASK_1
1 AR_NIC_ORB_CFG_PRF_TRK_MASK_1:ADDR_115_112_MSK
1 AR_NIC_ORB_CFG_PRF_TRK_MASK_1:ADDR_69_64_MSK
1 AR_NIC_ORB_CFG_PRF_TRK_MASK_1:CNT_BM_MSK
1 AR_NIC_ORB_CFG_PRF_TRK_MASK_1:MDH_MSK
1 AR_NIC_ORB_CFG_PRF_TRK_MASK_1:P1_MSK
1 AR_NIC_ORB_CFG_PRF_TRK_MASK_1:P2_MSK
1 AR_NIC_ORB_CFG_PRF_TRK_MASK_1:RC0_MSK
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1 AR_NIC_ORB_CFG_PRF_TRK_MASK_1:RC2_MSK
1 AR_NIC_ORB_CFG_PRF_TRK_MASK_1:REQCMD_MSK
1 AR_NIC_ORB_CFG_PRF_TRK_MASK_1:RESERVED_105_94
1 AR_NIC_ORB_CFG_PRF_TRK_MASK_1:RESERVED_127_125
1 AR_NIC_ORB_CFG_PRF_TRK_MASK_1:SSID_MSK
1 AR_NIC_ORB_CFG_PRF_TRK_MASK_1:UV_MSK
1 AR_NIC_ORB_CFG_PRF_TRK_MASK_2
1 AR_NIC_ORB_CFG_PRF_TRK_MASK_2:LSTATUS_MSK
1 AR_NIC_ORB_CFG_PRF_TRK_MASK_2:P3_MSK
1 AR_NIC_ORB_CFG_PRF_TRK_MASK_2:RC1_MSK
1 AR_NIC_ORB_CFG_PRF_TRK_MASK_2:RESERVED_135_128
1 AR_NIC_ORB_CFG_PRF_TRK_MASK_3
1 AR_NIC_ORB_CFG_PRF_TRK_MASK_3:LADDR_63_36_MSK
1 AR_NIC_ORB_CFG_PRF_TRK_MASK_3:LPTAG_MSK
1 AR_NIC_ORB_CFG_PRF_TRK_MASK_3:RESERVED_17_0
1 AR_NIC_ORB_CFG_PRF_TRK_MASK_3:RESERVED_35_26
1 AR_NIC_ORB_CFG_PRF_TRK_MASK_4
1 AR_NIC_ORB_CFG_PRF_TRK_MASK_4:ECC0_MSK
1 AR_NIC_ORB_CFG_PRF_TRK_MASK_4:FR_MSK
1 AR_NIC_ORB_CFG_PRF_TRK_MASK_4:LADDR_105_94_MSK
1 AR_NIC_ORB_CFG_PRF_TRK_MASK_4:LCNT_MSK
1 AR_NIC_ORB_CFG_PRF_TRK_MASK_4:LMDH_MSK
1 AR_NIC_ORB_CFG_PRF_TRK_MASK_4:RESERVED_107
1 AR_NIC_ORB_CFG_PRF_TRK_MASK_4:RESERVED_127_112
1 AR_NIC_ORB_CFG_PRF_TRK_MASK_4:RESERVED_81_72
1 AR_NIC_ORB_CFG_PRF_TRK_MASK_5
1 AR_NIC_ORB_CFG_PRF_TRK_MASK_5:ECC1_MSK
1 AR_NIC_ORB_CFG_PRF_TRK_MASK_5:LWC_MSK
1 AR_NIC_ORB_CFG_PRF_TRK_MASK_5:RESERVED_134_128
1 AR_NIC_ORB_PRF_STALL_DURATION_EN
1 AR_NIC_ORB_PRF_STALL_DURATION_EN:ENABLE_CRDTS
1 AR_NIC_ORB_PRF_STALL_DURATION_EN:ENABLE_PKTID
1 AR_NIC_PARB_PRF_EN
1 AR_NIC_PARB_PRF_EN:PRF_VC0_EN
1 AR_NIC_PARB_PRF_EN:PRF_VC1_EN
1 AR_NIC_PARB_PRF_EN:PRF_VC2_EN
1 AR_NIC_RAT_PRF_EN
1 AR_NIC_RAT_PRF_EN:PRF_REQ_DURATION_EN
1 AR_NIC_RAT_PRF_EN:PRF_RSP_DURATION_EN
3 AR_NIC_RAT_PRF_REQ_MASKm
1 AR_NIC_RAT_PRF_REQ_MASKm:FLIT_127_64
1 AR_NIC_RAT_PRF_REQ_MASKm:FLIT_143_128
1 AR_NIC_RAT_PRF_REQ_MASKm:FLIT_63_0
1 AR_NIC_RAT_PRF_REQ_MASKm:HDR
1 AR_NIC_RAT_PRF_REQ_MASKm:TAIL
3 AR_NIC_RAT_PRF_REQ_VALv
1 AR_NIC_RAT_PRF_REQ_VALv:FLIT_127_64
1 AR_NIC_RAT_PRF_REQ_VALv:FLIT_143_128
1 AR_NIC_RAT_PRF_REQ_VALv:FLIT_63_0
1 AR_NIC_RAT_PRF_REQ_VALv:HDR
1 AR_NIC_RAT_PRF_REQ_VALv:IOMMU_EN
1 AR_NIC_RAT_PRF_REQ_VALv:NL_EN
1 AR_NIC_RAT_PRF_REQ_VALv:TAIL
3 AR_NIC_RAT_PRF_RSP_MASKm
1 AR_NIC_RAT_PRF_RSP_MASKm:FLIT_127_64
1 AR_NIC_RAT_PRF_RSP_MASKm:FLIT_143_128
1 AR_NIC_RAT_PRF_RSP_MASKm:FLIT_63_0
1 AR_NIC_RAT_PRF_RSP_MASKm:HDR

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1 AR_NIC_RAT_PRF_RSP_MASKm:TAIL
3 AR_NIC_RAT_PRF_RSP_VALv
1 AR_NIC_RAT_PRF_RSP_VALv:EN
1 AR_NIC_RAT_PRF_RSP_VALv:FLIT_127_64
1 AR_NIC_RAT_PRF_RSP_VALv:FLIT_143_128
1 AR_NIC_RAT_PRF_RSP_VALv:FLIT_63_0
1 AR_NIC_RAT_PRF_RSP_VALv:HDR
1 AR_NIC_RAT_PRF_RSP_VALv:TAIL
1 AR_NIC_REQMON_PRF_EVENT_CNTR_CTRL
1 AR_NIC_REQMON_PRF_EVENT_CNTR_CTRL:CLEAR
1 AR_NIC_REQMON_PRF_EVENT_CNTR_CTRL:COUNTERS_CLEAR
1 AR_NIC_REQMON_PRF_EVENT_CNTR_CTRL:DISABLE_AUTO_UPDATE
1 AR_NIC_REQMON_PRF_EVENT_CNTR_CTRL:ENABLE
1 AR_NIC_RSPMON_PRF_EVENT_CNTR_CTRL
1 AR_NIC_RSPMON_PRF_EVENT_CNTR_CTRL:CLEAR
1 AR_NIC_RSPMON_PRF_EVENT_CNTR_CTRL:COUNTERS_CLEAR
1 AR_NIC_RSPMON_PRF_EVENT_CNTR_CTRL:DISABLE_AUTO_UPDATE
1 AR_NIC_RSPMON_PRF_EVENT_CNTR_CTRL:ENABLE
1 AR_NIC_SSID_CFG_EVENT_CNTR_FILTER
1 AR_NIC_SSID_CFG_EVENT_CNTR_FILTER:ALL_CHAN_EN
1 AR_NIC_SSID_CFG_EVENT_CNTR_FILTER:CHANNEL
1 AR_NIC_SSID_CFG_EVENT_CNTR_FILTER:INITIATOR
1 AR_NL_PRF_CTRL
1 AR_NL_PRF_CTRL:CLEAR
1 AR_NL_PRF_CTRL:COUNTERS_CLEAR
1 AR_NL_PRF_CTRL:DISABLE_AUTO_UPDATE
1 AR_NL_PRF_CTRL:ENABLE
1 AR_NL_PRF_CTRL:SEL_LM_TRIGGERS
1 AR_PI_PM_EVENT_CNTR_CTRL
1 AR_PI_PM_EVENT_CNTR_CTRL:CLEAR
1 AR_PI_PM_EVENT_CNTR_CTRL:COUNTERS_CLEAR
1 AR_PI_PM_EVENT_CNTR_CTRL:DISABLE_AUTO_UPDATE
1 AR_PI_PM_EVENT_CNTR_CTRL:ENABLE
1 AR_PI_PM_PCLK_EVENT_CNTR_CTRL
1 AR_PI_PM_PCLK_EVENT_CNTR_CTRL:CLEAR
1 AR_PI_PM_PCLK_EVENT_CNTR_CTRL:COUNTERS_CLEAR
1 AR_PI_PM_PCLK_EVENT_CNTR_CTRL:DISABLE_AUTO_UPDATE
1 AR_PI_PM_PCLK_EVENT_CNTR_CTRL:ENABLE
64 AR_RTR_PT_r_c_INQ_PRF_FLITf_FILTERING_MASK
64 AR_RTR_PT_r_c_INQ_PRF_FLITf_FILTERING_MASK:MASK
64 AR_RTR_PT_r_c_INQ_PRF_FLITf_FILTERING_SETUP
64 AR_RTR_PT_r_c_INQ_PRF_FLITf_FILTERING_SETUP:COMPARE_PATTERN
64 AR_RTR_PT_r_c_INQ_PRF_FLITf_FILTERING_SETUP:ENABLE
64 AR_RTR_PT_r_c_INQ_PRF_FLITf_FILTERING_SETUP:FLIT_PTR
320 AR_RTR_r_c_INQ_PRF_FLITf_FILTERING_MASK
320 AR_RTR_r_c_INQ_PRF_FLITf_FILTERING_MASK:MASK
320 AR_RTR_r_c_INQ_PRF_FLITf_FILTERING_SETUP
320 AR_RTR_r_c_INQ_PRF_FLITf_FILTERING_SETUP:COMPARE_PATTERN
320 AR_RTR_r_c_INQ_PRF_FLITf_FILTERING_SETUP:ENABLE
320 AR_RTR_r_c_INQ_PRF_FLITf_FILTERING_SETUP:FLIT_PTR

```

Where:

r = row  
c = column  
f = flit